ORIGINAL

BELLSOUTH

EX PARTE OR LATE FILED

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Suite 900
1133-21st Street, N.W.
Washington, D.C. 20036-3351

kathleen.levitz@bellsouth.com

Kathleen B. Levitz
Vice President-Federal Regulatory

202 463-4113
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RECEIVED

NOV 3 0 2001

FEDERAL COMMUNICATIONS COMMISSION
OFFICE OF THE SECRETARY

November 30, 2001

WRITTEN EX PARTE

Ms. Magalie Roman Salas
Secretary
Federal Communications Commission
The Portals
445 12th Street, S.W.
Washington, D.C. 20554

Re: CC Docket No. 01-277

Dear Ms. Salas:

On November 30, 2001, I sent electronically to Daniel Shiman of the Commission's Common Carrier Bureau, at his request, a copy of the BellSouth Georgia Monthly State Summary of performance data for the month of October, as well as a copy of a spreadsheet presenting the Georgia monthly summaries of performance for the months of May through October, 2001.

In accordance with Section 1.1206, I am filing two copies of this notice and the accompanying attachment and request that you please place them in the record of the proceeding identified above. Thank you.

Sincerely,

Kathleen B. Levitz

Kathleen B. Levitz

No. of Copies rec'd 0+2
List A B C D E

Attachments

cc: Daniel Shiman (w/o attachment)
Jessica Rosenworcel
Susan Pie
James Davis-Smith

BellSouth Monthly Performance Summary
Georgia, October 2001

		October Data						September Data						August Data						July Data						June Data						May Data					
	Benchmark/ Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Zscore	Aug-01 Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Zscore	Aug-01 Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Zscore	Jun-01 Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Zscore	May-01 Equity
Results - Graphs																																					
% Delayed Service Requests - Manhattan																																					
A.1.1.1 O-7 Residence/SWCN	Disparate			15.14%	20,697		Disparate			15.66%	24,668		Disparate			15.40%	21,135		Disparate			14.90%	18,661		Disparate			14.40%	20,241		Disparate			14.40%	19,888		Disparate
A.1.1.2 O-7 Business/SWCN	Disparate			20.00%	202		Disparate			27.40%	217		Disparate			20.00%	792		Disparate			14.40%	792		Disparate			41.40%	290		Disparate			16.40%	224		Disparate
A.1.1.3 O-7 Public Residence/SWCN	Disparate			37.40%	3		Disparate			0.00%	4		Disparate						Disparate			0.00%	1		Disparate						Disparate						Disparate
A.1.1.4 O-7 Public Business/SWCN	Disparate						Disparate						Disparate			0.00%	1		Disparate						Disparate						Disparate						Disparate
A.1.1.5 O-7 Coin/SWCN	Disparate						Disparate						Disparate						Disparate						Disparate						Disparate						Disparate
A.1.1.6 O-7 Wholesale/SWCN	Disparate						Disparate						Disparate						Disparate						Disparate						Disparate						Disparate
% Delayed Service Requests - Outside Manhattan																																					
A.1.2.1 O-7 Residence/SWCN	Disparate			30.50%	4,864		Disparate			30.70%	4,888		Disparate			30.60%	5,600		Disparate			27.60%	5,484		Disparate			28.40%	5,191		Disparate			28.11%	5,000		Disparate
A.1.2.2 O-7 Business/SWCN	Disparate			45.00%	700		Disparate			45.00%	688		Disparate			45.00%	684		Disparate			45.00%	672		Disparate			45.00%	688		Disparate			45.00%	691		Disparate
A.1.2.3 O-7 Public Residence/SWCN	Disparate			100.00%	2		Disparate			0.00%	1		Disparate			100.00%	1		Disparate			100.00%	1		Disparate			0.00%	1		Disparate			0.00%	1		Disparate
A.1.2.4 O-7 Public Business/SWCN	Disparate						Disparate						Disparate						Disparate						Disparate						Disparate						Disparate
A.1.2.5 O-7 Coin/SWCN	Disparate						Disparate						Disparate						Disparate						Disparate						Disparate						Disparate
A.1.2.6 O-7 Wholesale/SWCN	Disparate						Disparate						Disparate						Disparate						Disparate						Disparate						Disparate
% Delayed Service Requests - Non-Manhattan																																					
A.1.3.1 O-7 Residence/SWCN	Disparate			45.40%	500		Disparate			45.11%	467		Disparate			45.40%	462		Disparate			44.11%	448		Disparate			44.40%	400		Disparate			44.40%	409		Disparate
A.1.3.2 O-7 Business/SWCN	Disparate			45.40%	209		Disparate			45.40%	210		Disparate			45.40%	207		Disparate			45.40%	79		Disparate			45.40%	92		Disparate			45.40%	92		Disparate
A.1.3.3 O-7 Public Residence/SWCN	Disparate			50.00%	10		Disparate			45.40%	20		Disparate			50.71%	20		Disparate			45.71%	15		Disparate			45.40%	17		Disparate			45.40%	16		Disparate
A.1.3.4 O-7 Public Business/SWCN	Disparate			51.00%	22		Disparate			54.11%	20		Disparate			45.40%	20		Disparate			45.40%	41		Disparate			21.40%	20		Disparate			45.40%	20		Disparate
A.1.3.5 O-7 Coin/SWCN	Disparate			51.00%	22		Disparate			45.40%	20		Disparate			45.40%	20		Disparate			45.40%	20		Disparate			45.40%	20		Disparate			45.40%	20		Disparate
A.1.3.6 O-7 Wholesale/SWCN	Disparate						Disparate						Disparate						Disparate						Disparate						Disparate						Disparate
Repair Interval - Residential																																					
A.1.4.1 O-4 Residence/SWCN	>= 48% >= 5 < 10			55.40%	5,797	NO				55.70%	5,388	NO				55.60%	5,449	NO				55.40%	5,487	NO				55.60%	5,214	YES				55.11%	4,888	YES	
A.1.4.2 O-4 Business/SWCN	>= 48% >= 5 < 10			55.17%	145	NO				55.40%	160	NO				55.70%	160	NO				55.40%	148	YES				55.40%	138	YES				57.40%	139	YES	
A.1.4.3 O-4 Public Residence/SWCN	>= 48% >= 5 < 10			55.40%	3	NO																															
A.1.4.4 O-4 Public Business/SWCN	>= 48% >= 5 < 10																																				
A.1.4.5 O-4 Coin/SWCN	>= 48% >= 5 < 10																																				
A.1.4.6 O-4 Wholesale/SWCN	>= 48% >= 5 < 10																																				
Repair Interval - Residential - 30 days																																					
A.1.5.1 O-4 Residence/SWCN	>= 48% >= 30 < 240																																				
A.1.5.2 O-4 Business/SWCN	>= 48% >= 30 < 240																																				
A.1.5.3 O-4 Public Residence/SWCN	>= 48% >= 30 < 240																																				
A.1.5.4 O-4 Public Business/SWCN	>= 48% >= 30 < 240																																				
A.1.5.5 O-4 Coin/SWCN	>= 48% >= 30 < 240																																				
A.1.5.6 O-4 Wholesale/SWCN	>= 48% >= 30 < 240																																				
Repair Interval - Residential - 10 days																																					
A.1.6.1 O-4 Residence/SWCN	>= 48% >= 10 < 30			55.70%	1,750	YES				57.60%	1,767	YES				57.60%	1,844	YES																			
A.1.6.2 O-4 Business/SWCN	>= 48% >= 10 < 30			55.60%	148	YES				57.60%	152	YES				55.60%	214	YES																			
A.1.6.3 O-4 Public Residence/SWCN	>= 48% >= 10 < 30			55.40%	3	NO																															
A.1.6.4 O-4 Public Business/SWCN	>= 48% >= 10 < 30			0.00%	1	NO										0.00%	1	NO																			
A.1.6.5 O-4 Coin/SWCN	>= 48% >= 10 < 30																																				
A.1.6.6 O-4 Wholesale/SWCN	>= 48% >= 10 < 30																																				
Repair Interval - Residential - Non-Manhattan																																					
A.1.7.1 O-4 Residence/SWCN	>= 48% >= 5 < 10			87.40%	111	YES				87.40%	49	YES				87.40%	109	YES				55.40%	73	YES				55.40%	147	YES				55.40%	130	YES	
A.1.7.2 O-4 Business/SWCN	>= 48% >= 5 < 10			55.40%	48	YES				55.40%	48	YES				55.40%	48	YES				55.40%	48	YES				55.40%	20	YES				55.40%	18	YES	
A.1.7.3 O-4 Public Residence/SWCN	>= 48% >= 5 < 10			100.00%	10	YES				100.00%	10	YES				100.00%	11	YES				81.40%	13	YES				100.00%	7	YES				100.00%	7	YES	
A.1.7.4 O-4 Public Business/SWCN	>= 48% >= 5 < 10			55.40%	10	YES				55.40%	10	YES				100.00%	11	YES				55.70%	11	NO				55.40%	5	NO				55.40%	5	NO	
A.1.7.5 O-4 Coin/SWCN	>= 48% >= 5 < 10																																				
A.1.7.6 O-4 Wholesale/SWCN	>= 48% >= 5 < 10																																				
Repair Interval - Non-Residential																																					
A.1.8.1 O-4 Residence/SWCN	>= 48% >= 5 < 10			55.40%	20,694	YES				55.70%	20,407	YES				55.40%	20,300	YES				57.40%	20,792	YES				57.40%	17,400	YES				57.40%	16,000	YES	
A.1.8.2 O-4 Business/SWCN	>= 48% >= 5 < 10			55.40%	200	YES				55.40%	200	YES				55.40%	200	YES				55.40%	210	YES				55.40%	200	YES				55.40%	200	YES	
A.1.8.3 O-4 Public Residence/SWCN	>= 48% >= 5 < 10			55.40%	3	NO				100.00%	1	YES				100.00%	1	YES																			
A.1.8.4 O-4 Public Business/SWCN	>= 48% >= 5 < 10																																				
A.1.8.5 O-4 Coin/SWCN	>= 48% >= 5 < 10																																				
A.1.8.6 O-4 Wholesale/SWCN	>= 48% >= 5 < 10																																				
Repair Interval - Non-Residential - Non-Manhattan																																					
A.1.9.1 O-4 Residence/SWCN	>= 48% >= 5 < 10			55.40%	200	YES				55.40%	200	YES				55.40%	200	YES				55.40%	200	YES				55.40%	200	YES				55.40%	200	YES	
A.1.9.2 O-4 Business/SWCN	>= 48% >= 5 < 10			55.40%	200	YES				55.40%	200	YES				55.40%	200	YES				55.40%	200	YES				55.40%	200	YES				55.40%	200	YES	
A.1.9.3 O-4 Public Residence/SWCN	>= 48% >= 5 < 10			55.40%	47	YES				55.40%	47	YES				55.40%	47	YES				55.40%	47	YES				55.40%	47	YES				55.40%	47	YES	
A.1.9.4 O-4 Public Business/SWCN	>= 48% >= 5 < 10			55.40%	47	YES				55.40%	47	YES				55.40%	47	YES				55.40%	47	YES				55.40%	47	YES				55.40%	47	YES	
A.1.9.5 O-4 Coin/SWCN	>= 48% >= 5 < 10																																				
A.1.9.6 O-4 Wholesale/SWCN	>= 48% >= 5 < 10																																				
Repair Interval - Residential - Manhattan																																					
A.1.10.1 O-11 Residence/SWCN	>= 48%			55.40%	1,875	YES				55.40%	1,888	YES				55.40%	1,888	YES				55.40%	1,888	YES				55.40%	1,888	YES				55.40%	1,888	YES	
A.1.10.2 O-11 Business/SWCN	>= 48%			55.40%	48	YES				55.40%	48	YES				55.40%	48	YES				55.40%	48	YES				55.40%	48	YES				55.40%	48	YES	
A.1.10.3 O-11 Public Residence/SWCN	>= 48%			55.40%	47	YES				55.40%	47	YES				55.40%	47	YES				55.40%	47	YES				55.40%	47	YES				55.40%	47	YES	
A.1.10.4 O-11 Public Business/SWCN	>= 48%			55.40%	47	YES				55.40%	47	YES				55.40%	47	YES				55.40%	47	YES				55.40%	47	YES				55.40%	47	YES	
A.1.10.5 O-11 Coin/SWCN	>= 48%																																				
A.1.10.6 O-11 Wholesale/SWCN	>= 48%																																				
Repair Interval - Residential - Outside Manhattan																																					
A.1.11.1 O-11 Residence/SWCN	>= 48%			55.40%	4,000	YES				55.40%	4,000	YES				55.40%	4,000	YES				55.40%	4,000	YES				55.40%	4,000	YES				55.40%	4,000	YES	
A.1.11.2 O-11 Business/SWCN	>= 48%			55.40%	200	YES				55.40%	200	YES				55.40%	200	YES				55.40%	200	YES				55.40%	200	YES				55.40%	200	YES	
A.1.11.3 O-11 Public Residence/SWCN	>= 48%			55.40%	4	YES				55.40%	4	YES				55.40%	4	YES				55.40%	4	YES				55.40%	4	YES				55.40%	4	YES	
A.1.11.4 O-11 Public Business/SWCN	>= 48%			55.40%	4	NO				55.40%	4	NO				55.40%	4	NO				55.40%	4	NO				55.40%	4	NO				55.40%	4	NO	
A.1.11.5 O-11 Coin/SWCN	>= 48%																																				
A.1.11.6 O-11 Wholesale/SWCN	>= 48%																																				
Repair Interval - Non-Residential - Non-Manhattan																																					
A.1.12.1 O-11 Residence/SWCN	>= 48%			55.40%	200	NO				55.40%	197	NO				55.40%	200	NO				55.40%	200	NO				55.40%	111	NO				55.40%	200	NO	
A.1.12.2 O-11 Business/SWCN	>= 48%			55.40%	200	NO				55.40%	200	NO				55.40%	197	NO				55.40%	197	NO				55.40%	197	NO				55.40%	197	NO	
A.1.12.3 O-11 Public Residence/SWCN	>= 48%			55.40%	200	NO				55.40%	200	NO				55.40%	197	NO				55.40%	197	NO				55.40%	197	NO				55.40%	197	NO	
A.1.12.4 O-11 Public Business/SWCN	>= 48%			55.40%	47	NO				55.40%	47	NO				55.40%	47	NO				55.40%	47	NO				55.40%	47	NO				55.40%	47	NO	
A.1.12.5 O-11 Coin/SWCN	>= 48%			55.40%	47	NO				55.40%	47	NO				55.40%	47	NO				55.40%	47	NO				55.40%	47	NO				55.40%	47	NO	
A.1.12.6 O-11 Wholesale/SWCN	>= 48%																																				
Repair Interval - Non-Residential - Outside Manhattan																																					
A.1.13.1 O-11 Residence/SWCN	>= 48%			55.40%	21,000	NO				55.40%	21,000	NO				55.40%	21,000	NO				55.40%	21,000	NO				55.40%	21,000	YES				55.40%	21,000	YES	
A.1.13.2 O-11 Business/SWCN	>= 48%			55.40%	500	NO				55.40%	500	NO				55.40%	500	NO				55.40%	500	NO				55.40%	500	YES				55.40%	500	YES	
A.1.13.3 O-11 Public Residence/SWCN	>= 48%			55.40%	1	NO				55.40%	1	NO				55.40%	1	NO				55.40%	1	NO				55.40%	1	NO				55.40%	1	NO	
A.1.13.4 O-11 Public Business/SWCN	>= 48%			55.40%	1	NO				55.40%	1	NO				55.40%	1	NO				55.40%	1	NO				55.40%	1	NO				55.40%	1	NO	
A.1.13.5 O-11 Coin/SWCN	>= 48%																																				
A.1.13.6 O-11 Wholesale/SWCN	>= 48%																																				
Repair Interval - Non-Residential - Manhattan																																					
A.1.14.1 O-11 Residence/SWCN	>= 48%			55.40%	1,000	YES				55.40%	1,000	YES				55.40%	1,000	YES				55.40%	1,000	YES				55.40%	1,000	YES				55.40%	1,000	YES	
A.1.14.2 O-11 Business/SWCN	>= 48%			55.40%	200	YES				55.40%	200	YES				55.40%	200	YES				55.40%	200	YES				55.40%	200	YES				55.40%	200	YES	
A.1.14.3 O-11 Public Residence/SWCN	>= 48%			55.40%	4	YES				55.40%	4	YES				55.40%	4	YES				55.40%	4	YES				55.40%	4	YES				55.40%	4	YES	
A.1.14.4 O-11 Public Business/SWCN	>= 48%			55.40%	1	YES				55.40%	1	YES				55.40%	1	YES				55.40%	1	YES				55.40%	1	YES				55.40%	1	YES	
A.1.14.5 O-11 Coin/SWCN	>= 48%																																				
A.1.14.6 O-11 Wholesale/SWCN	>= 48%																																				
Repair Interval - Non-Residential - Non-Manhattan																																					
A.1.15.1 O-11 Residence/SWCN	>= 48%			55.40%	200	NO				55.40%	200	NO				55.40%	200	NO				55.40%	200	NO				55.40%	200	NO				55.40%	200	NO	
A.1.15.2 O-11 Business/SWCN	>= 48%			55.40%	200	NO				55.40%	200	NO				55.40%	200	NO				55.40%	200	NO				55.40%	200	NO				55.40%	200	NO	
A.1.15.3 O-11 Public Residence/SWCN	>= 48%			55.40%	47	NO				55.40%	47	NO				55.40%	47	NO				55.40%	47	NO				55.40%	47	NO				55.40%	47	NO	
A.1.15.4 O-11 Public Business/SWCN	>= 48%			55.40%	47	NO				55.40%	47	NO				55.40%	47	NO				55.40%	47	NO				55.40%	47	NO				55.40%	47	NO	
A.1.15.5 O-11 Coin/SWCN	>= 48%																																				
A.1.15.6 O-11 Wholesale/SWCN	>= 48%																																				

BellSouth Monthly Performance Summary

Georgia, October 2001

		October Data						September Data						August Data						July Data						June Data						May Data					
		BST Measure	BST Volume	CLEC Measure	CLEC Volume	Z-Score	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Z-Score	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Z-Score	Aug-01 Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Z-Score	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Z-Score	Jun-01 Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Z-Score	May-01 Equity
A.1.1.1.1																																					
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A.1.1.1.151																																					
A.1.1.1.152																																					

BellSouth Monthly Performance Summary

Georgia, October 2001

Performance Analysis	October Data						September Data						August Data						July Data						June Data						May Data					
	BST Standards	BST Volumes	CLEC Standards	CLEC Volumes	Z-Score	Equity	BST Standards	BST Volumes	CLEC Standards	CLEC Volumes	Z-Score	Equity	BST Standards	BST Volumes	CLEC Standards	CLEC Volumes	Z-Score	Aug-99 Equity	BST Standards	BST Volumes	CLEC Standards	CLEC Volumes	Z-Score	Jun-99 Equity	BST Standards	BST Volumes	CLEC Standards	CLEC Volumes	Z-Score	Apr-99 Equity	BST Standards	BST Volumes	CLEC Standards	CLEC Volumes	Z-Score	May-99 Equity
A2.1.1.2.2 P-3																																				
A2.1.1.2.3 P-3																																				
B Provisioning Timeliness within 30 Days																																				
A2.1.1.1.1 P-4																																				
A2.1.1.1.2 P-4																																				
A2.1.1.1.3 P-4																																				
A2.1.1.2.1 P-4																																				
A2.1.1.2.2 P-4																																				
A2.1.1.2.3 P-4																																				
A2.1.1.3.1 P-4																																				
A2.1.1.3.2 P-4																																				
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A2.1.2.2.2 P-4																																				
A2.1.2.2.3 P-4																																				
A2.1.2.3.1 P-4																																				
A2.1.2.3.2 P-4																																				
A2.1.2.3.3 P-4																																				
Average Completion Notice Interval - Manual																																				
A2.1.1.1.1 P-5																																				
A2.1.1.1.2 P-5																																				
A2.1.1.1.3 P-5																																				
A2.1.1.2.1 P-5																																				
A2.1.1.2.2 P-5																																				
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A2.1.2.3.1 P-5																																				
A2.1.2.3.2 P-5																																				
A2.1.2.3.3 P-5																																				
Total Service Order Order Flow - Mechanized																																				
A2.1.7.1.1 P-10																																				
A2.1.7.1.2 P-10																																				
A2.1.7.1.3 P-10																																				
A2.1.7.2.1 P-10																																				
A2.1.7.2.2 P-10																																				
A2.1.7.2.3 P-10																																				
A2.1.7.3.1 P-10																																				
A2.1.7.3.2 P-10																																				
A2.1.7.3.3 P-10																																				
Total Service Order Order Flow - Partially Mechanized																																				
A2.1.8.1.1 P-10																																				
A2.1.8.1.2 P-10																																				
A2.1.8.1.3 P-10																																				
A2.1.8.2.1 P-10																																				
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A2.1.8.3.1 P-10																																				
A2.1.8.3.2 P-10																																				
A2.1.8.3.3 P-10																																				
Total Service Order Order Flow - Non-Mechanized																																				
A2.1.9.1.1 P-10																																				
A2.1.9.1.2 P-10																																				
A2.1.9.1.3 P-10																																				
A2.1.9.2.1 P-10																																				
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A2.1.9.3.1 P-10																																				
A2.1.9.3.2 P-10																																				
A2.1.9.3.3 P-10																																				
Total Service Order Order Flow (effective - Mechanized																																				
A2.1.11.1.1 P-10																																				
A2.1.11.1.2 P-10																																				
A2.1.11.1.3 P-10																																				
A2.1.11.2.1 P-10																																				
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A2.1.11.3.1 P-10																																				
A2.1.11.3.2 P-10																																				
A2.1.11.3.3 P-10																																				

Baseball Monthly Performance Summary

Douglas, October 2001

Month	Date	Opponent	Result	Score	Pitcher	Hits	Runs	Errors	Left on Base	Batting Average
[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]

BellSouth Monthly Performance Summary

Georgia, October 2001

		Benchmark / Analog	October Data						September Data						August Data						July Data						June Data						May Data					
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	ZScore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	ZScore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	ZScore	Aug-01 Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	ZScore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	ZScore	June-01 Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	ZScore	May-01 Equity
A.1.5.4.1	[illegible]	BCH	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]
A.1.5.4.2	[illegible]	BCH	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]
	Parity - Status																																					
	Service Quality																																					
A.4.1	[illegible]	BST - Stat	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]
A.4.2	[illegible]	BST - Region	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]

BellSouth Monthly Performance Summary

Georgia, October 2001

			Benchmark / Analog	October Data: BST Measure	BST Volume	CLEC Measure	CLEC Volume
	Unbundled Network Elements - Ordering						
	% Rejected Service Requests - Mechanized						
B.1.1.1	O-7	Switch Ports/GA(%)	Diagnostic				
B.1.1.2	O-7	Local Interoffice Transport/GA(%)	Diagnostic				
B.1.1.3	O-7	Loop + Port Combinations/GA(%)	Diagnostic			17.78%	43,213
B.1.1.4	O-7	Combo Other/GA(%)	Diagnostic				
B.1.1.5	O-7	xDSL (ADSL, HDSL and UCL)/GA(%)	Diagnostic			17.47%	166
B.1.1.6	O-7	ISDN Loop (UDN, UDC)/GA(%)	Diagnostic			6.25%	16
B.1.1.7	O-7	Line Sharing/GA(%)	Diagnostic			40.91%	44
B.1.1.8	O-7	2W Analog Loop Design/GA(%)	Diagnostic			33.87%	310
B.1.1.9	O-7	2W Analog Loop Non-Design/GA(%)	Diagnostic			20.10%	199
B.1.1.10	O-7	2W Analog Loop w/INP Design/GA(%)	Diagnostic				
B.1.1.11	O-7	2W Analog Loop w/INP Non-Design/GA(%)	Diagnostic				
B.1.1.12	O-13	2W Analog Loop w/LNP Design/GA(%)	Diagnostic			31.03%	29
B.1.1.13	O-13	2W Analog Loop w/LNP Non-Design/GA(%)	Diagnostic			47.22%	36
B.1.1.14	O-7	Other Design/GA(%)	Diagnostic			33.00%	100
B.1.1.15	O-7	Other Non-Design/GA(%)	Diagnostic			21.36%	2,477
B.1.1.16	O-7	INP Standalone/GA(%)	Diagnostic				
B.1.1.17	O-13	LNP Standalone/GA(%)	Diagnostic			9.52%	2,217
	% Rejected Service Requests - Partially Mechanized						
B.1.2.1	O-7	Switch Ports/GA(%)	Diagnostic				
B.1.2.2	O-7	Local Interoffice Transport/GA(%)	Diagnostic				
B.1.2.3	O-7	Loop + Port Combinations/GA(%)	Diagnostic			31.34%	18,349
B.1.2.4	O-7	Combo Other/GA(%)	Diagnostic				
B.1.2.5	O-7	xDSL (ADSL, HDSL and UCL)/GA(%)	Diagnostic			14.29%	14
B.1.2.6	O-7	ISDN Loop (UDN, UDC)/GA(%)	Diagnostic			50.00%	4
B.1.2.7	O-7	Line Sharing/GA(%)	Diagnostic			38.89%	18
B.1.2.8	O-7	2W Analog Loop Design/GA(%)	Diagnostic			25.31%	320
B.1.2.9	O-7	2W Analog Loop Non-Design/GA(%)	Diagnostic			29.41%	102
B.1.2.10	O-7	2W Analog Loop w/INP Design/GA(%)	Diagnostic				
B.1.2.11	O-7	2W Analog Loop w/INP Non-Design/GA(%)	Diagnostic				
B.1.2.12	O-13	2W Analog Loop w/LNP Design/GA(%)	Diagnostic			40.66%	241
B.1.2.13	O-13	2W Analog Loop w/LNP Non-Design/GA(%)	Diagnostic			12.73%	1,053
B.1.2.14	O-7	Other Design/GA(%)	Diagnostic			42.11%	57
B.1.2.15	O-7	Other Non-Design/GA(%)	Diagnostic			41.64%	718
B.1.2.16	O-7	INP Standalone/GA(%)	Diagnostic				
B.1.2.17	O-13	LNP Standalone/GA(%)	Diagnostic			31.67%	1,241
	% Rejected Service Requests - Non-Mechanized						
B.1.3.1	O-7	Switch Ports/GA(%)	Diagnostic				
B.1.3.2	O-7	Local Interoffice Transport/GA(%)	Diagnostic			57.69%	26
B.1.3.3	O-7	Loop + Port Combinations/GA(%)	Diagnostic			49.70%	827
B.1.3.4	O-7	Combo Other/GA(%)	Diagnostic				
B.1.3.5	O-7	xDSL (ADSL, HDSL and UCL)/GA(%)	Diagnostic			27.80%	223

BellSouth Monthly Performance Summary

Georgia, October 2001

			Benchmark / Analog	October Data BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.1.3.6	O-7	ISDN Loop (UDN, UDC)/GA(%)	Diagnostic			13.94%	459
B.1.3.7	O-7	Line Sharing/GA(%)	Diagnostic			16.00%	50
B.1.3.8	O-7	2W Analog Loop Design/GA(%)	Diagnostic			46.37%	179
B.1.3.9	O-7	2W Analog Loop Non-Design/GA(%)	Diagnostic			37.27%	1,245
B.1.3.10	O-7	2W Analog Loop w/INP Design/GA(%)	Diagnostic			0.00%	1
B.1.3.11	O-7	2W Analog Loop w/INP Non-Design/GA(%)	Diagnostic			0.00%	4
B.1.3.12	O-13	2W Analog Loop w/LNP Design/GA(%)	Diagnostic			58.33%	36
B.1.3.13	O-13	2W Analog Loop w/LNP Non-Design/GA(%)	Diagnostic			26.98%	215
B.1.3.14	O-7	Other Design/GA(%)	Diagnostic			34.82%	336
B.1.3.15	O-7	Other Non-Design/GA(%)	Diagnostic			33.26%	1,711
B.1.3.16	O-7	INP Standalone/GA(%)	Diagnostic			46.15%	104
B.1.3.17	O-13	LNP Standalone/GA(%)	Diagnostic			24.61%	1,337
	Reject Interval - Mechanized						
B.1.4.1	O-8	Switch Ports/GA(%)	>= 97% w in 1 hr				
B.1.4.2	O-8	Local Interoffice Transport/GA(%)	>= 97% w in 1 hr				
B.1.4.3	O-8	Loop + Port Combinations/GA(%)	>= 97% w in 1 hr			86.03%	7,746
B.1.4.4	O-8	Combo Other/GA(%)	>= 97% w in 1 hr				
B.1.4.5	O-8	xDSL (ADSL, HDSL and UCL)/GA(%)	>= 97% w in 1 hr			93.10%	29
B.1.4.6	O-8	ISDN Loop (UDN, UDC)/GA(%)	>= 97% w in 1 hr			100.00%	1
B.1.4.7	O-8	Line Sharing/GA(%)	>= 97% w in 1 hr			72.22%	18
B.1.4.8	O-8	2W Analog Loop Design/GA(%)	>= 97% w in 1 hr			73.58%	106
B.1.4.9	O-8	2W Analog Loop Non-Design/GA(%)	>= 97% w in 1 hr			80.49%	41
B.1.4.10	O-8	2W Analog Loop w/INP Design/GA(%)	>= 97% w in 1 hr				
B.1.4.11	O-8	2W Analog Loop w/INP Non-Design/GA(%)	>= 97% w in 1 hr				
B.1.4.12	O-14	2W Analog Loop w/LNP Design/GA(%)	>= 97% w in 1 hr			88.89%	9
B.1.4.13	O-14	2W Analog Loop w/LNP Non-Design/GA(%)	>= 97% w in 1 hr			82.35%	17
B.1.4.14	O-8	Other Design/GA(%)	>= 97% w in 1 hr			87.88%	33
B.1.4.15	O-8	Other Non-Design/GA(%)	>= 97% w in 1 hr			64.77%	545
B.1.4.16	O-8	INP Standalone/GA(%)	>= 97% w in 1 hr				
B.1.4.17	O-14	LNP Standalone/GA(%)	>= 97% w in 1 hr			93.36%	211
	Reject Interval - Partially Mechanized - 24 hours						
B.1.5.1	O-8	Switch Ports/GA(%)	>= 85% w in 24 hrs	This data not applicable after 5-1-2001, see b			
B.1.5.2	O-8	Local Interoffice Transport/GA(%)	>= 85% w in 24 hrs	This data not applicable after 5-1-2001, see b			
B.1.5.3	O-8	Loop + Port Combinations/GA(%)	>= 85% w in 24 hrs	This data not applicable after 5-1-2001, see b			
B.1.5.4	O-8	Combo Other/GA(%)	>= 85% w in 24 hrs	This data not applicable after 5-1-2001, see b			
B.1.5.5	O-8	xDSL (ADSL, HDSL and UCL)/GA(%)	>= 85% w in 24 hrs	This data not applicable after 5-1-2001, see b			
B.1.5.6	O-8	ISDN Loop (UDN, UDC)/GA(%)	>= 85% w in 24 hrs	This data not applicable after 5-1-2001, see b			
B.1.5.7	O-8	Line Sharing/GA(%)	>= 85% w in 24 hrs	This data not applicable after 5-1-2001, see b			
B.1.5.8	O-8	2W Analog Loop Design/GA(%)	>= 85% w in 24 hrs	This data not applicable after 5-1-2001, see b			
B.1.5.9	O-8	2W Analog Loop Non-Design/GA(%)	>= 85% w in 24 hrs	This data not applicable after 5-1-2001, see b			
B.1.5.10	O-8	2W Analog Loop w/INP Design/GA(%)	>= 85% w in 24 hrs	This data not applicable after 5-1-2001, see b			
B.1.5.11	O-8	2W Analog Loop w/INP Non-Design/GA(%)	>= 85% w in 24 hrs	This data not applicable after 5-1-2001, see b			
B.1.5.12	O-14	2W Analog Loop w/LNP Design/GA(%)	>= 85% w in 24 hrs	This data not applicable after 5-1-2001, see b			
B.1.5.13	O-14	2W Analog Loop w/LNP Non-Design/GA(%)	>= 85% w in 24 hrs	This data not applicable after 5-1-2001, see b			
B.1.5.14	O-8	Other Design/GA(%)	>= 85% w in 24 hrs	This data not applicable after 5-1-2001, see b			
B.1.5.15	O-8	Other Non-Design/GA(%)	>= 85% w in 24 hrs	This data not applicable after 5-1-2001, see b			

BellSouth Monthly Performance Summary

Georgia, October 2001

			Benchmark / Analog	October Data: BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.1.5.16	O-8	INP Standalone/GA(%)	>= 85% w in 24 hrs	This data not applicable after 5-1-2001, see b			
B.1.5.17	O-14	LNP (Standalone)/GA(%)	>= 85% w in 24 hrs	This data not applicable after 5-1-2001, see b			
	Reject Interval - Partially Mechanized - 18 hours						
B.1.6.1	O-8	Switch Ports/GA(%)	>= 85% w in 18 hrs	This data not applicable after 8-1-2001, see b			
B.1.6.2	O-8	Local Interoffice Transport/GA(%)	>= 85% w in 18 hrs	This data not applicable after 8-1-2001, see b			
B.1.6.3	O-8	Loop + Port Combinations/GA(%)	>= 85% w in 18 hrs	This data not applicable after 8-1-2001, see b			
B.1.6.4	O-8	Combo Other/GA(%)	>= 85% w in 18 hrs	This data not applicable after 8-1-2001, see b			
B.1.6.5	O-8	xDSL (ADSL, HDSL and UCL)/GA(%)	>= 85% w in 18 hrs	This data not applicable after 8-1-2001, see b			
B.1.6.6	O-8	ISDN Loop (UDN, UDC)/GA(%)	>= 85% w in 18 hrs	This data not applicable after 8-1-2001, see b			
B.1.6.7	O-8	Line Sharing/GA(%)	>= 85% w in 18 hrs	This data not applicable after 8-1-2001, see b			
B.1.6.8	O-8	2W Analog Loop Design/GA(%)	>= 85% w in 18 hrs	This data not applicable after 8-1-2001, see b			
B.1.6.9	O-8	2W Analog Loop Non-Design/GA(%)	>= 85% w in 18 hrs	This data not applicable after 8-1-2001, see b			
B.1.6.10	O-8	2W Analog Loop w/INP Design/GA(%)	>= 85% w in 18 hrs	This data not applicable after 8-1-2001, see b			
B.1.6.11	O-8	2W Analog Loop w/INP Non-Design/GA(%)	>= 85% w in 18 hrs	This data not applicable after 8-1-2001, see b			
B.1.6.12	O-14	2W Analog Loop w/LNP Design/GA(%)	>= 85% w in 18 hrs	This data not applicable after 8-1-2001, see b			
B.1.6.13	O-14	2W Analog Loop w/LNP Non-Design/GA(%)	>= 85% w in 18 hrs	This data not applicable after 8-1-2001, see b			
B.1.6.14	O-8	Other Design/GA(%)	>= 85% w in 18 hrs	This data not applicable after 8-1-2001, see b			
B.1.6.15	O-8	Other Non-Design/GA(%)	>= 85% w in 18 hrs	This data not applicable after 8-1-2001, see b			
B.1.6.16	O-8	INP Standalone/GA(%)	>= 85% w in 18 hrs	This data not applicable after 8-1-2001, see b			
B.1.6.17	O-14	LNP Standalone/GA(%)	>= 85% w in 18 hrs	This data not applicable after 8-1-2001, see b			
	Reject Interval - Partially Mechanized - 10 hours						
B.1.7.1	O-8	Switch Ports/GA(%)	>= 85% w in 10 hrs				
B.1.7.2	O-8	Local Interoffice Transport/GA(%)	>= 85% w in 10 hrs				
B.1.7.3	O-8	Loop + Port Combinations/GA(%)	>= 85% w in 10 hrs			94.86%	5,853
B.1.7.4	O-8	Combo Other/GA(%)	>= 85% w in 10 hrs				
B.1.7.5	O-8	xDSL (ADSL, HDSL and UCL)/GA(%)	>= 85% w in 10 hrs			100.00%	2
B.1.7.6	O-8	ISDN Loop (UDN, UDC)/GA(%)	>= 85% w in 10 hrs			100.00%	2
B.1.7.7	O-8	Line Sharing/GA(%)	>= 85% w in 10 hrs			100.00%	7
B.1.7.8	O-8	2W Analog Loop Design/GA(%)	>= 85% w in 10 hrs			88.76%	89
B.1.7.9	O-8	2W Analog Loop Non-Design/GA(%)	>= 85% w in 10 hrs			80.00%	30
B.1.7.10	O-8	2W Analog Loop w/INP Design/GA(%)	>= 85% w in 10 hrs				
B.1.7.11	O-8	2W Analog Loop w/INP Non-Design/GA(%)	>= 85% w in 10 hrs				
B.1.7.12	O-14	2W Analog Loop w/LNP Design/GA(%)	>= 85% w in 10 hrs			80.81%	99
B.1.7.13	O-14	2W Analog Loop w/LNP Non-Design/GA(%)	>= 85% w in 10 hrs			96.30%	135
B.1.7.14	O-8	Other Design/GA(%)	>= 85% w in 10 hrs			91.67%	24
B.1.7.15	O-8	Other Non-Design/GA(%)	>= 85% w in 10 hrs			94.37%	302
B.1.7.16	O-8	INP Standalone/GA(%)	>= 85% w in 10 hrs				
B.1.7.17	O-14	LNP Standalone/GA(%)	>= 85% w in 10 hrs			90.63%	395
	Reject Interval - Non-Mechanized						
B.1.8.1	O-8	Switch Ports/GA(%)	>= 85% w in 24 hrs				
B.1.8.2	O-8	Local Interoffice Transport/GA(%)	>= 85% w in 24 hrs			93.33%	15
B.1.8.3	O-8	Loop + Port Combinations/GA(%)	>= 85% w in 24 hrs			98.32%	416
B.1.8.4	O-8	Combo Other/GA(%)	>= 85% w in 24 hrs				
B.1.8.5	O-8	xDSL (ADSL, HDSL and UCL)/GA(%)	>= 85% w in 24 hrs			100.00%	63
B.1.8.6	O-8	ISDN Loop (UDN, UDC)/GA(%)	>= 85% w in 24 hrs			98.46%	65
B.1.8.7	O-8	Line Sharing/GA(%)	>= 85% w in 24 hrs			100.00%	8

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			Benchmark / Analog	October Data BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.1.8.8	O-8	2W Analog Loop Design/GA(%)	>= 85% w in 24 hrs			100.00%	84
B.1.8.9	O-8	2W Analog Loop Non-Design/GA(%)	>= 85% w in 24 hrs			99.58%	476
B.1.8.10	O-8	2W Analog Loop w/INP Design/GA(%)	>= 85% w in 24 hrs				
B.1.8.11	O-8	2W Analog Loop w/INP Non-Design/GA(%)	>= 85% w in 24 hrs				
B.1.8.12	O-14	2W Analog Loop w/LNP Design/GA(%)	>= 85% w in 24 hrs			100.00%	21
B.1.8.13	O-14	2W Analog Loop w/LNP Non-Design/GA(%)	>= 85% w in 24 hrs			100.00%	66
B.1.8.14	O-8	Other Design/GA(%)	>= 85% w in 24 hrs			98.32%	119
B.1.8.15	O-8	Other Non-Design/GA(%)	>= 85% w in 24 hrs			99.48%	575
B.1.8.16	O-8	INP Standalone/GA(%)	>= 85% w in 24 hrs			100.00%	48
B.1.8.17	O-14	LNP Standalone/GA(%)	>= 85% w in 24 hrs			99.70%	333
	FOC Timeliness - Mechanized						
B.1.9.1	O-9	Switch Ports/GA(%)	>= 95% w in 3 hrs				
B.1.9.2	O-9	Local Interoffice Transport/GA(%)	>= 95% w in 3 hrs				
B.1.9.3	O-9	Loop + Port Combinations/GA(%)	>= 95% w in 3 hrs			99.01%	35,639
B.1.9.4	O-9	Combo Other/GA(%)	>= 95% w in 3 hrs				
B.1.9.5	O-9	xDSL (ADSL, HDSL and UCL)/GA(%)	>= 95% w in 3 hrs			95.70%	93
B.1.9.6	O-9	ISDN Loop (UDN, UDC)/GA(%)	>= 95% w in 3 hrs			100.00%	15
B.1.9.7	O-9	Line Sharing/GA(%)	>= 95% w in 3 hrs			100.00%	28
B.1.9.8	O-9	2W Analog Loop Design/GA(%)	>= 95% w in 3 hrs			100.00%	215
B.1.9.9	O-9	2W Analog Loop Non-Design/GA(%)	>= 95% w in 3 hrs			99.38%	160
B.1.9.10	O-9	2W Analog Loop w/INP Design/GA(%)	>= 95% w in 3 hrs				
B.1.9.11	O-9	2W Analog Loop w/INP Non-Design/GA(%)	>= 95% w in 3 hrs				
B.1.9.12	O-15	2W Analog Loop w/LNP Design/GA(%)	>= 95% w in 3 hrs			95.00%	20
B.1.9.13	O-15	2W Analog Loop w/LNP Non-Design/GA(%)	>= 95% w in 3 hrs			100.00%	19
B.1.9.14	O-9	Other Design/GA(%)	>= 95% w in 3 hrs			100.00%	65
B.1.9.15	O-9	Other Non-Design/GA(%)	>= 95% w in 3 hrs			99.37%	2,070
B.1.9.16	O-9	INP Standalone/GA(%)	>= 95% w in 3 hrs				
B.1.9.17	O-15	LNP Standalone/GA(%)	>= 95% w in 3 hrs			96.35%	2,002
	FOC Timeliness - Partially Mechanized						
B.1.10.1	O-9	Switch Ports/GA(%)	>= 85% w in 36 hrs	This data not applicable after 5-1-2001, see b			
B.1.10.2	O-9	Local Interoffice Transport/GA(%)	>= 85% w in 36 hrs	This data not applicable after 5-1-2001, see b			
B.1.10.3	O-9	Loop + Port Combinations/GA(%)	>= 85% w in 36 hrs	This data not applicable after 5-1-2001, see b			
B.1.10.4	O-9	Combo Other/GA(%)	>= 85% w in 36 hrs	This data not applicable after 5-1-2001, see b			
B.1.10.5	O-9	xDSL (ADSL, HDSL and UCL)/GA(%)	>= 85% w in 36 hrs	This data not applicable after 5-1-2001, see b			
B.1.10.6	O-9	ISDN Loop (UDN, UDC)/GA(%)	>= 85% w in 36 hrs	This data not applicable after 5-1-2001, see b			
B.1.10.7	O-9	Line Sharing/GA(%)	>= 85% w in 36 hrs	This data not applicable after 5-1-2001, see b			
B.1.10.8	O-9	2W Analog Loop Design/GA(%)	>= 85% w in 36 hrs	This data not applicable after 5-1-2001, see b			
B.1.10.9	O-9	2W Analog Loop Non-Design/GA(%)	>= 85% w in 36 hrs	This data not applicable after 5-1-2001, see b			
B.1.10.10	O-9	2W Analog Loop w/INP Design/GA(%)	>= 85% w in 36 hrs	This data not applicable after 5-1-2001, see b			
B.1.10.11	O-9	2W Analog Loop w/INP Non-Design/GA(%)	>= 85% w in 36 hrs	This data not applicable after 5-1-2001, see b			
B.1.10.12	O-15	2W Analog Loop w/LNP Design/GA(%)	>= 85% w in 36 hrs	This data not applicable after 5-1-2001, see b			
B.1.10.13	O-15	2W Analog Loop w/LNP Non-Design/GA(%)	>= 85% w in 36 hrs	This data not applicable after 5-1-2001, see b			
B.1.10.14	O-9	Other Design/GA(%)	>= 85% w in 36 hrs	This data not applicable after 5-1-2001, see b			
B.1.10.15	O-9	Other Non-Design/GA(%)	>= 85% w in 36 hrs	This data not applicable after 5-1-2001, see b			
B.1.10.16	O-9	INP Standalone/GA(%)	>= 85% w in 36 hrs	This data not applicable after 5-1-2001, see b			
B.1.10.17	O-15	LNP Standalone/GA(%)	>= 85% w in 36 hrs	This data not applicable after 5-1-2001, see b			

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			Benchmark / Analog	October Data: BST Measure	BST Volume	CLEC Measure	CLEC Volume
	FOC Timeliness - Partially Mechanized - 18 hours						
B.1.11.1	O-9	Switch Ports/GA(%)	>= 85% w in 18 hrs	*This data not applicable after 8-1-2001, see b*			
B.1.11.2	O-9	Local Interoffice Transport/GA(%)	>= 85% w in 18 hrs	*This data not applicable after 8-1-2001, see b*			
B.1.11.3	O-9	Loop + Port Combinations/GA(%)	>= 85% w in 18 hrs	*This data not applicable after 8-1-2001, see b*			
B.1.11.4	O-9	Combo Other/GA(%)	>= 85% w in 18 hrs	*This data not applicable after 8-1-2001, see b*			
B.1.11.5	O-9	xDSL (ADSL, HDSL and UCL)/GA(%)	>= 85% w in 18 hrs	*This data not applicable after 8-1-2001, see b*			
B.1.11.6	O-9	ISDN Loop (UDN, UDC)/GA(%)	>= 85% w in 18 hrs	*This data not applicable after 8-1-2001, see b*			
B.1.11.7	O-9	Line Sharing/GA(%)	>= 85% w in 18 hrs	*This data not applicable after 8-1-2001, see b*			
B.1.11.8	O-9	2W Analog Loop Design/GA(%)	>= 85% w in 18 hrs	*This data not applicable after 8-1-2001, see b*			
B.1.11.9	O-9	2W Analog Loop Non-Design/GA(%)	>= 85% w in 18 hrs	*This data not applicable after 8-1-2001, see b*			
B.1.11.10	O-9	2W Analog Loop w/INP Design/GA(%)	>= 85% w in 18 hrs	*This data not applicable after 8-1-2001, see b*			
B.1.11.11	O-9	2W Analog Loop w/INP Non-Design/GA(%)	>= 85% w in 18 hrs	*This data not applicable after 8-1-2001, see b*			
B.1.11.12	O-15	2W Analog Loop w/LNP Design/GA(%)	>= 85% w in 18 hrs	*This data not applicable after 8-1-2001, see b*			
B.1.11.13	O-15	2W Analog Loop w/LNP Non-Design/GA(%)	>= 85% w in 18 hrs	*This data not applicable after 8-1-2001, see b*			
B.1.11.14	O-9	Other Design/GA(%)	>= 85% w in 18 hrs	*This data not applicable after 8-1-2001, see b*			
B.1.11.15	O-9	Other Non-Design/GA(%)	>= 85% w in 18 hrs	*This data not applicable after 8-1-2001, see b*			
B.1.11.16	O-9	INP Standalone/GA(%)	>= 85% w in 18 hrs	*This data not applicable after 8-1-2001, see b*			
B.1.11.17	O-15	LNP Standalone/GA(%)	>= 85% w in 18 hrs	*This data not applicable after 8-1-2001, see b*			
	FOC Timeliness - Partially Mechanized - 10 hours						
B.1.12.1	O-9	Switch Ports/GA(%)	>= 85% w in 10 hrs				
B.1.12.2	O-9	Local Interoffice Transport/GA(%)	>= 85% w in 10 hrs				
B.1.12.3	O-9	Loop + Port Combinations/GA(%)	>= 85% w in 10 hrs			96.71%	12,757
B.1.12.4	O-9	Combo Other/GA(%)	>= 85% w in 10 hrs				
B.1.12.5	O-9	xDSL (ADSL, HDSL and UCL)/GA(%)	>= 85% w in 10 hrs			66.67%	6
B.1.12.6	O-9	ISDN Loop (UDN, UDC)/GA(%)	>= 85% w in 10 hrs			100.00%	2
B.1.12.7	O-9	Line Sharing/GA(%)	>= 85% w in 10 hrs			100.00%	8
B.1.12.8	O-9	2W Analog Loop Design/GA(%)	>= 85% w in 10 hrs			88.24%	255
B.1.12.9	O-9	2W Analog Loop Non-Design/GA(%)	>= 85% w in 10 hrs			85.71%	77
B.1.12.10	O-9	2W Analog Loop w/INP Design/GA(%)	>= 85% w in 10 hrs				
B.1.12.11	O-9	2W Analog Loop w/INP Non-Design/GA(%)	>= 85% w in 10 hrs				
B.1.12.12	O-15	2W Analog Loop w/LNP Design/GA(%)	>= 85% w in 10 hrs			85.26%	156
B.1.12.13	O-15	2W Analog Loop w/LNP Non-Design/GA(%)	>= 85% w in 10 hrs			98.12%	956
B.1.12.14	O-9	Other Design/GA(%)	>= 85% w in 10 hrs			94.44%	36
B.1.12.15	O-9	Other Non-Design/GA(%)	>= 85% w in 10 hrs			95.76%	425
B.1.12.16	O-9	INP Standalone/GA(%)	>= 85% w in 10 hrs				
B.1.12.17	O-15	LNP Standalone/GA(%)	>= 85% w in 10 hrs			96.39%	832
	FOC Timeliness - Non-Mechanized						
B.1.13.1	O-9	Switch Ports/GA(%)	>= 85% w in 36 hrs				
B.1.13.2	O-9	Local Interoffice Transport/GA(%)	>= 85% w in 36 hrs			100.00%	16
B.1.13.3	O-9	Loop + Port Combinations/GA(%)	>= 85% w in 36 hrs			97.59%	374
B.1.13.4	O-9	Combo Other/GA(%)	>= 85% w in 36 hrs				
B.1.13.5	O-9	xDSL (ADSL, HDSL and UCL)/GA(%)	>= 85% w in 36 hrs			99.39%	164
B.1.13.6	O-9	ISDN Loop (UDN, UDC)/GA(%)	>= 85% w in 36 hrs			100.00%	380
B.1.13.7	O-9	Line Sharing/GA(%)	>= 85% w in 36 hrs			100.00%	39
B.1.13.8	O-9	2W Analog Loop Design/GA(%)	>= 85% w in 36 hrs			98.95%	95
B.1.13.9	O-9	2W Analog Loop Non-Design/GA(%)	>= 85% w in 36 hrs			99.33%	741

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			Benchmark / Analog	October Data: BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.1.13.10	O-9	2W Analog Loop w/INP Design/GA(%)	>= 85% w in 36 hrs			100.00%	1
B.1.13.11	O-9	2W Analog Loop w/INP Non-Design/GA(%)	>= 85% w in 36 hrs			100.00%	4
B.1.13.12	O-15	2W Analog Loop w/LNP Design/GA(%)	>= 85% w in 36 hrs			100.00%	13
B.1.13.13	O-15	2W Analog Loop w/LNP Non-Design/GA(%)	>= 85% w in 36 hrs			97.22%	180
B.1.13.14	O-9	Other Design/GA(%)	>= 85% w in 36 hrs			100.00%	221
B.1.13.15	O-9	Other Non-Design/GA(%)	>= 85% w in 36 hrs			99.63%	1,090
B.1.13.16	O-9	INP Standalone/GA(%)	>= 85% w in 36 hrs			100.00%	56
B.1.13.17	O-15	LNP Standalone/GA(%)	>= 85% w in 36 hrs			99.60%	1,010
		FOC & Reject Response Completeness - Mechanized					
B.1.14.1	O-11	Switch Ports/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.14.1.1	O-11	Switch Ports/EDI/GA(%)	>= 95%				
B.1.14.1.2	O-11	Switch Ports/TAG/GA(%)	>= 95%				
B.1.14.2	O-11	Local Interoffice Transport/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.14.2.1	O-11	Local Interoffice Transport/EDI/GA(%)	>= 95%				
B.1.14.2.2	O-11	Local Interoffice Transport/TAG/GA(%)	>= 95%				
B.1.14.3	O-11	Loop + Port Combinations/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.14.3.1	O-11	Loop + Port Combinations/EDI/GA(%)	>= 95%			99.95%	27,921
B.1.14.3.2	O-11	Loop + Port Combinations/TAG/GA(%)	>= 95%			98.52%	15,292
B.1.14.4	O-11	Combo Other/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.14.4.1	O-11	Combo Other/EDI/GA(%)	>= 95%				
B.1.14.4.2	O-11	Combo Other/TAG/GA(%)	>= 95%				
B.1.14.5	O-11	xDSL (ADSL, HDSL and UCL)/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.14.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/GA(%)	>= 95%			81.82%	11
B.1.14.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/GA(%)	>= 95%			72.90%	155
B.1.14.6	O-11	ISDN Loop (UDN, UDC)/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.14.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/GA(%)	>= 95%			100.00%	5
B.1.14.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/GA(%)	>= 95%			100.00%	11
B.1.14.7	O-11	Line Sharing/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.14.7.1	O-11	Line Sharing/EDI/GA(%)	>= 95%				
B.1.14.7.2	O-11	Line Sharing/TAG/GA(%)	>= 95%			95.45%	44
B.1.14.8	O-11	2W Analog Loop Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.14.8.1	O-11	2W Analog Loop Design/EDI/GA(%)	>= 95%			99.59%	246
B.1.14.8.2	O-11	2W Analog Loop Design/TAG/GA(%)	>= 95%			100.00%	64
B.1.14.9	O-11	2W Analog Loop Non-Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.14.9.1	O-11	2W Analog Loop Non-Design/EDI/GA(%)	>= 95%			100.00%	1
B.1.14.9.2	O-11	2W Analog Loop Non-Design/TAG/GA(%)	>= 95%			98.48%	198
B.1.14.10	O-11	2W Analog Loop w/INP Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.14.10.1	O-11	2W Analog Loop w/INP Design/EDI/GA(%)	>= 95%				
B.1.14.10.2	O-11	2W Analog Loop w/INP Design/TAG/GA(%)	>= 95%				
B.1.14.11	O-11	2W Analog Loop w/INP Non-Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.14.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/GA(%)	>= 95%				
B.1.14.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/GA(%)	>= 95%				
B.1.14.12	O-11	2W Analog Loop w/LNP Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.14.12.1	O-11	2W Analog Loop w/LNP Design/EDI/GA(%)	>= 95%			100.00%	26
B.1.14.12.2	O-11	2W Analog Loop w/LNP Design/TAG/GA(%)	>= 95%			100.00%	3
B.1.14.13	O-11	2W Analog Loop w/LNP Non-Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.14.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/GA(%)	>= 95%			100.00%	9

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			Benchmark / Analog	October Data: BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.1.14.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/GA(%)	>= 95%			100.00%	27
B.1.14.14	O-11	Other Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.14.14.1	O-11	Other Design/EDI/GA(%)	>= 95%			100.00%	15
B.1.14.14.2	O-11	Other Design/TAG/GA(%)	>= 95%			98.82%	85
B.1.14.15	O-11	Other Non-Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.14.15.1	O-11	Other Non-Design/EDI/GA(%)	>= 95%			99.80%	1,524
B.1.14.15.2	O-11	Other Non-Design/TAG/GA(%)	>= 95%			98.53%	953
B.1.14.16	O-11	INP Standalone/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.14.16.1	O-11	INP Standalone/EDI/GA(%)	>= 95%				
B.1.14.16.2	O-11	INP Standalone/TAG/GA(%)	>= 95%				
B.1.14.17	O-11	LNP Standalone/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.14.17.1	O-11	LNP Standalone/EDI/GA(%)	>= 95%			99.89%	1,878
B.1.14.17.2	O-11	LNP Standalone/TAG/GA(%)	>= 95%			99.41%	339
		FOC & Reject Response Completeness - Partially Mechanized					
B.1.15.1	O-11	Switch Ports/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.15.1.1	O-11	Switch Ports/EDI/GA(%)	>= 95%				
B.1.15.1.2	O-11	Switch Ports/TAG/GA(%)	>= 95%				
B.1.15.2	O-11	Local Interoffice Transport/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.15.2.1	O-11	Local Interoffice Transport/EDI/GA(%)	>= 95%				
B.1.15.2.2	O-11	Local Interoffice Transport/TAG/GA(%)	>= 95%				
B.1.15.3	O-11	Loop + Port Combinations/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.15.3.1	O-11	Loop + Port Combinations/EDI/GA(%)	>= 95%			99.97%	11,761
B.1.15.3.2	O-11	Loop + Port Combinations/TAG/GA(%)	>= 95%			99.80%	6,588
B.1.15.4	O-11	Combo Other/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.15.4.1	O-11	Combo Other/EDI/GA(%)	>= 95%				
B.1.15.4.2	O-11	Combo Other/TAG/GA(%)	>= 95%				
B.1.15.5	O-11	xDSL (ADSL, HDSL and UCL)/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.15.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/GA(%)	>= 95%				
B.1.15.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/GA(%)	>= 95%			57.14%	14
B.1.15.6	O-11	ISDN Loop (UDN, UDC)/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.15.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/GA(%)	>= 95%				
B.1.15.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/GA(%)	>= 95%			100.00%	4
B.1.15.7	O-11	Line Sharing/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.15.7.1	O-11	Line Sharing/EDI/GA(%)	>= 95%				
B.1.15.7.2	O-11	Line Sharing/TAG/GA(%)	>= 95%			94.44%	18
B.1.15.8	O-11	2W Analog Loop Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.15.8.1	O-11	2W Analog Loop Design/EDI/GA(%)	>= 95%			100.00%	289
B.1.15.8.2	O-11	2W Analog Loop Design/TAG/GA(%)	>= 95%			100.00%	31
B.1.15.9	O-11	2W Analog Loop Non-Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.15.9.1	O-11	2W Analog Loop Non-Design/EDI/GA(%)	>= 95%			100.00%	2
B.1.15.9.2	O-11	2W Analog Loop Non-Design/TAG/GA(%)	>= 95%			100.00%	100
B.1.15.10	O-11	2W Analog Loop w/INP Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.15.10.1	O-11	2W Analog Loop w/INP Design/EDI/GA(%)	>= 95%				
B.1.15.10.2	O-11	2W Analog Loop w/INP Design/TAG/GA(%)	>= 95%				
B.1.15.11	O-11	2W Analog Loop w/INP Non-Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.15.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/GA(%)	>= 95%				
B.1.15.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/GA(%)	>= 95%				

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			Benchmark / Analog	October Data: BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.1.15.12	O-11	2W Analog Loop w/LNP Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.15.12.1	O-11	2W Analog Loop w/LNP Design/EDI/GA(%)	>= 95%			99.11%	224
B.1.15.12.2	O-11	2W Analog Loop w/LNP Design/TAG/GA(%)	>= 95%			94.12%	17
B.1.15.13	O-11	2W Analog Loop w/LNP Non-Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.15.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/GA(%)	>= 95%			100.00%	827
B.1.15.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/GA(%)	>= 95%			99.56%	226
B.1.15.14	O-11	Other Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.15.14.1	O-11	Other Design/EDI/GA(%)	>= 95%			100.00%	23
B.1.15.14.2	O-11	Other Design/TAG/GA(%)	>= 95%			100.00%	34
B.1.15.15	O-11	Other Non-Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.15.15.1	O-11	Other Non-Design/EDI/GA(%)	>= 95%			100.00%	201
B.1.15.15.2	O-11	Other Non-Design/TAG/GA(%)	>= 95%			99.81%	517
B.1.15.16	O-11	INP Standalone/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.15.16.1	O-11	INP Standalone/EDI/GA(%)	>= 95%				
B.1.15.16.2	O-11	INP Standalone/TAG/GA(%)	>= 95%				
B.1.15.17	O-11	LNP Standalone/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.15.17.1	O-11	LNP Standalone/EDI/GA(%)	>= 95%			98.65%	1,036
B.1.15.17.2	O-11	LNP Standalone/TAG/GA(%)	>= 95%			100.00%	205
		FOC & Reject Response Completeness - Non-Mechanized					
B.1.16.1	O-11	Switch Ports/GA(%)	>= 95%				
B.1.16.2	O-11	Local Interoffice Transport/GA(%)	>= 95%			100.00%	26
B.1.16.3	O-11	Loop + Port Combinations/GA(%)	>= 95%			91.78%	827
B.1.16.4	O-11	Combo Other/GA(%)	>= 95%				
B.1.16.5	O-11	xDSL (ADSL, HDSL and UCL)/GA(%)	>= 95%			98.21%	223
B.1.16.6	O-11	ISDN Loop (UDN, UDC)/GA(%)	>= 95%			97.60%	459
B.1.16.7	O-11	Line Sharing/GA(%)	>= 95%			94.00%	50
B.1.16.8	O-11	2W Analog Loop Design/GA(%)	>= 95%			96.09%	179
B.1.16.9	O-11	2W Analog Loop Non-Design/GA(%)	>= 95%			94.22%	1,245
B.1.16.10	O-11	2W Analog Loop w/INP Design/GA(%)	>= 95%			100.00%	1
B.1.16.11	O-11	2W Analog Loop w/INP Non-Design/GA(%)	>= 95%			100.00%	4
B.1.16.12	O-11	2W Analog Loop w/LNP Design/GA(%)	>= 95%			91.67%	36
B.1.16.13	O-11	2W Analog Loop w/LNP Non-Design/GA(%)	>= 95%			97.67%	215
B.1.16.14	O-11	Other Design/GA(%)	>= 95%			96.43%	336
B.1.16.15	O-11	Other Non-Design/GA(%)	>= 95%			96.90%	1,711
B.1.16.16	O-11	INP Standalone/GA(%)	>= 95%			99.04%	104
B.1.16.17	O-11	LNP Standalone/GA(%)	>= 95%			98.20%	1,337
		FOC & Reject Response Completeness (Multiple Responses) - Mechanized					
B.1.17.1	O-11	Switch Ports/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.17.1.1	O-11	Switch Ports/EDI/GA(%)	>= 95%				
B.1.17.1.2	O-11	Switch Ports/TAG/GA(%)	>= 95%				
B.1.17.2	O-11	Local Interoffice Transport/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.17.2.1	O-11	Local Interoffice Transport/EDI/GA(%)	>= 95%				
B.1.17.2.2	O-11	Local Interoffice Transport/TAG/GA(%)	>= 95%				
B.1.17.3	O-11	Loop + Port Combinations/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.17.3.1	O-11	Loop + Port Combinations/EDI/GA(%)	>= 95%			70.45%	27,907
B.1.17.3.2	O-11	Loop + Port Combinations/TAG/GA(%)	>= 95%			87.85%	15,066

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			Benchmark / Analog	October Data: BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.1.17.4	O-11	Combo Other/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.17.4.1	O-11	Combo Other/EDI/GA(%)	>= 95%				
B.1.17.4.2	O-11	Combo Other/TAG/GA(%)	>= 95%				
B.1.17.5	O-11	xDSL (ADSL, HDSL and UCL)/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.17.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/GA(%)	>= 95%			100.00%	9
B.1.17.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/GA(%)	>= 95%			100.00%	113
B.1.17.6	O-11	ISDN Loop (UDN, UDC)/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.17.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/GA(%)	>= 95%			100.00%	5
B.1.17.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/GA(%)	>= 95%			90.91%	11
B.1.17.7	O-11	Line Sharing/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.17.7.1	O-11	Line Sharing/EDI/GA(%)	>= 95%				
B.1.17.7.2	O-11	Line Sharing/TAG/GA(%)	>= 95%			88.10%	42
B.1.17.8	O-11	2W Analog Loop Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.17.8.1	O-11	2W Analog Loop Design/EDI/GA(%)	>= 95%			63.67%	245
B.1.17.8.2	O-11	2W Analog Loop Design/TAG/GA(%)	>= 95%			82.81%	64
B.1.17.9	O-11	2W Analog Loop Non-Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.17.9.1	O-11	2W Analog Loop Non-Design/EDI/GA(%)	>= 95%			0.00%	1
B.1.17.9.2	O-11	2W Analog Loop Non-Design/TAG/GA(%)	>= 95%			81.54%	195
B.1.17.10	O-11	2W Analog Loop w/INP Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.17.10.1	O-11	2W Analog Loop w/INP Design/EDI/GA(%)	>= 95%				
B.1.17.10.2	O-11	2W Analog Loop w/INP Design/TAG/GA(%)	>= 95%				
B.1.17.11	O-11	2W Analog Loop w/INP Non-Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.17.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/GA(%)	>= 95%				
B.1.17.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/GA(%)	>= 95%				
B.1.17.12	O-11	2W Analog Loop w/LNP Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.17.12.1	O-11	2W Analog Loop w/LNP Design/EDI/GA(%)	>= 95%			100.00%	26
B.1.17.12.2	O-11	2W Analog Loop w/LNP Design/TAG/GA(%)	>= 95%			100.00%	3
B.1.17.13	O-11	2W Analog Loop w/LNP Non-Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.17.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/GA(%)	>= 95%			100.00%	9
B.1.17.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/GA(%)	>= 95%			100.00%	27
B.1.17.14	O-11	Other Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.17.14.1	O-11	Other Design/EDI/GA(%)	>= 95%			66.67%	15
B.1.17.14.2	O-11	Other Design/TAG/GA(%)	>= 95%			85.71%	84
B.1.17.15	O-11	Other Non-Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.17.15.1	O-11	Other Non-Design/EDI/GA(%)	>= 95%			76.33%	1,521
B.1.17.15.2	O-11	Other Non-Design/TAG/GA(%)	>= 95%			82.22%	939
B.1.17.16	O-11	INP Standalone/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.17.16.1	O-11	INP Standalone/EDI/GA(%)	>= 95%				
B.1.17.16.2	O-11	INP Standalone/TAG/GA(%)	>= 95%				
B.1.17.17	O-11	LNP Standalone/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.17.17.1	O-11	LNP Standalone/EDI/GA(%)	>= 95%			100.00%	1,876
B.1.17.17.2	O-11	LNP Standalone/TAG/GA(%)	>= 95%			100.00%	337
		FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized					
B.1.18.1	O-11	Switch Ports/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.18.1.1	O-11	Switch Ports/EDI/GA(%)	>= 95%				
B.1.18.1.2	O-11	Switch Ports/TAG/GA(%)	>= 95%				
B.1.18.2	O-11	Local Interoffice Transport/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			

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			Benchmark / Analog	October Data			
				BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.1.18.2.1	O-11	Local Interoffice Transport/EDI/GA(%)	>= 95%				
B.1.18.2.2	O-11	Local Interoffice Transport/TAG/GA(%)	>= 95%				
B.1.18.3	O-11	Loop + Port Combinations/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.18.3.1	O-11	Loop + Port Combinations/EDI/GA(%)	>= 95%			93.77%	11,758
B.1.18.3.2	O-11	Loop + Port Combinations/TAG/GA(%)	>= 95%			92.52%	6,575
B.1.18.4	O-11	Combo Other/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.18.4.1	O-11	Combo Other/EDI/GA(%)	>= 95%				
B.1.18.4.2	O-11	Combo Other/TAG/GA(%)	>= 95%				
B.1.18.5	O-11	xDSL (ADSL, HDSL and UCL)/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.18.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/GA(%)	>= 95%				
B.1.18.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/GA(%)	>= 95%			100.00%	8
B.1.18.6	O-11	ISDN Loop (UDN, UDC)/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.18.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/GA(%)	>= 95%				
B.1.18.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/GA(%)	>= 95%			100.00%	4
B.1.18.7	O-11	Line Sharing/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.18.7.1	O-11	Line Sharing/EDI/GA(%)	>= 95%				
B.1.18.7.2	O-11	Line Sharing/TAG/GA(%)	>= 95%			64.71%	17
B.1.18.8	O-11	2W Analog Loop Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.18.8.1	O-11	2W Analog Loop Design/EDI/GA(%)	>= 95%			96.19%	289
B.1.18.8.2	O-11	2W Analog Loop Design/TAG/GA(%)	>= 95%			96.77%	31
B.1.18.9	O-11	2W Analog Loop Non-Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.18.9.1	O-11	2W Analog Loop Non-Design/EDI/GA(%)	>= 95%			100.00%	2
B.1.18.9.2	O-11	2W Analog Loop Non-Design/TAG/GA(%)	>= 95%			94.00%	100
B.1.18.10	O-11	2W Analog Loop w/INP Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.18.10.1	O-11	2W Analog Loop w/INP Design/EDI/GA(%)	>= 95%				
B.1.18.10.2	O-11	2W Analog Loop w/INP Design/TAG/GA(%)	>= 95%				
B.1.18.11	O-11	2W Analog Loop w/INP Non-Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.18.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/GA(%)	>= 95%				
B.1.18.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/GA(%)	>= 95%				
B.1.18.12	O-11	2W Analog Loop w/LNP Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.18.12.1	O-11	2W Analog Loop w/LNP Design/EDI/GA(%)	>= 95%			95.50%	222
B.1.18.12.2	O-11	2W Analog Loop w/LNP Design/TAG/GA(%)	>= 95%			87.50%	16
B.1.18.13	O-11	2W Analog Loop w/LNP Non-Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.18.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/GA(%)	>= 95%			98.55%	827
B.1.18.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/GA(%)	>= 95%			95.11%	225
B.1.18.14	O-11	Other Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.18.14.1	O-11	Other Design/EDI/GA(%)	>= 95%			91.30%	23
B.1.18.14.2	O-11	Other Design/TAG/GA(%)	>= 95%			91.18%	34
B.1.18.15	O-11	Other Non-Design/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.18.15.1	O-11	Other Non-Design/EDI/GA(%)	>= 95%			95.52%	201
B.1.18.15.2	O-11	Other Non-Design/TAG/GA(%)	>= 95%			97.87%	516
B.1.18.16	O-11	INP Standalone/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.18.16.1	O-11	INP Standalone/EDI/GA(%)	>= 95%				
B.1.18.16.2	O-11	INP Standalone/TAG/GA(%)	>= 95%				
B.1.18.17	O-11	LNP Standalone/GA(%)	>= 95%	This data not applicable after 10-1-2001, see			
B.1.18.17.1	O-11	LNP Standalone/EDI/GA(%)	>= 95%			98.34%	1,022
B.1.18.17.2	O-11	LNP Standalone/TAG/GA(%)	>= 95%			99.51%	205

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			Benchmark / Analog	October Data: BST Measure	BST Volume	CLEC Measure	CLEC Volume
	FOC & Reject Response Completeness (Multiple Responses) - Non-Mechanized						
B.1.19.1	O-11	Switch Ports/GA(%)	>= 95%				
B.1.19.2	O-11	Local Interoffice Transport/GA(%)	>= 95%			80.77%	26
B.1.19.3	O-11	Loop + Port Combinations/GA(%)	>= 95%			92.09%	759
B.1.19.4	O-11	Combo Other/GA(%)	>= 95%				
B.1.19.5	O-11	xDSL (ADSL, HDSL and UCL)/GA(%)	>= 95%			93.15%	219
B.1.19.6	O-11	ISDN Loop (UDN, UDC)/GA(%)	>= 95%			97.99%	448
B.1.19.7	O-11	Line Sharing/GA(%)	>= 95%			95.74%	47
B.1.19.8	O-11	2W Analog Loop Design/GA(%)	>= 95%			95.93%	172
B.1.19.9	O-11	2W Analog Loop Non-Design/GA(%)	>= 95%			92.33%	1,173
B.1.19.10	O-11	2W Analog Loop w/INP Design/GA(%)	>= 95%			100.00%	1
B.1.19.11	O-11	2W Analog Loop w/INP Non-Design/GA(%)	>= 95%			75.00%	4
B.1.19.12	O-11	2W Analog Loop w/LNP Design/GA(%)	>= 95%			78.79%	33
B.1.19.13	O-11	2W Analog Loop w/LNP Non-Design/GA(%)	>= 95%			74.29%	210
B.1.19.14	O-11	Other Design/GA(%)	>= 95%			91.98%	324
B.1.19.15	O-11	Other Non-Design/GA(%)	>= 95%			97.47%	1,658
B.1.19.16	O-11	INP Standalone/GA(%)	>= 95%			97.09%	103
B.1.19.17	O-11	LNP Standalone/GA(%)	>= 95%			95.35%	1,313
	Unbundled Network Elements - Provisioning						
	Order Completion Interval						
B.2.1.1.1.1	P-4	Switch Ports/<10 circuits/Dispatch/GA(days)	R&B (POTS)	4.45	56,951		
B.2.1.1.1.2	P-4	Switch Ports/<10 circuits/Non-Dispatch/GA(days)	R&B (POTS)	0.92	472,149		
B.2.1.1.2.1	P-4	Switch Ports/>=10 circuits/Dispatch/GA(days)	R&B (POTS)	10.30	164		
B.2.1.1.2.2	P-4	Switch Ports/>=10 circuits/Non-Dispatch/GA(days)	R&B (POTS)	2.90	14		
B.2.1.2.1.1	P-4	Local Interoffice Transport/<10 circuits/Dispatch/GA(days)	DS1/DS3	17.30	2,187	14.00	1
B.2.1.2.1.2	P-4	Local Interoffice Transport/<10 circuits/Non-Dispatch/GA(days)	DS1/DS3				
B.2.1.2.2.1	P-4	Local Interoffice Transport/>=10 circuits/Dispatch/GA(days)	DS1/DS3				
B.2.1.2.2.2	P-4	Local Interoffice Transport/>=10 circuits/Non-Dispatch/GA(days)	DS1/DS3				
B.2.1.3.1.1	P-4	Loop + Port Combinations/<10 circuits/Dispatch/GA(days)	R&B	4.53	58,516	5.01	710
B.2.1.3.1.2	P-4	Loop + Port Combinations/<10 circuits/Non-Dispatch/GA(days)	R&B	0.92	474,193	0.77	15,670
B.2.1.3.1.3	P-4	Loop + Port Combinations/<10 circuits/Switch Based Orders/GA(days)	R&B	0.33	267,953	0.33	10,768
B.2.1.3.1.4	P-4	Loop + Port Combinations/<10 circuits/Dispatch In/GA(days)	R&B	1.68	206,240	1.74	4,902
B.2.1.3.2.1	P-4	Loop + Port Combinations/>=10 circuits/Dispatch/GA(days)	R&B	11.80	227	4.14	7
B.2.1.3.2.2	P-4	Loop + Port Combinations/>=10 circuits/Non-Dispatch/GA(days)	R&B	1.99	172	3.00	1
B.2.1.3.2.3	P-4	Loop + Port Combinations/>=10 circuits/Switch Based Orders/GA(days)	R&B	0.33	42		
B.2.1.3.2.4	P-4	Loop + Port Combinations/>=10 circuits/Dispatch In/GA(days)	R&B	2.53	130	3.00	1
B.2.1.4.1.1	P-4	Combo Other/<10 circuits/Dispatch/GA(days)	R&B&D - Disp	5.46	60,760	11.79	197
B.2.1.4.1.4	P-4	Combo Other/<10 circuits/Dispatch In/GA(days)	R&B&D - Disp	5.46	60,760		
B.2.1.4.2.1	P-4	Combo Other/>=10 circuits/Dispatch/GA(days)	R&B&D - Disp	17.13	302		
B.2.1.4.2.4	P-4	Combo Other/>=10 circuits/Dispatch In/GA(days)	R&B&D - Disp	17.13	302		
B.2.1.5.3.1	P-4	xDSL (ADSL, HDSL and UCL)/<6 circuits/Dispatch/GA(days)	ADSL to Retail	4.03	1,929	4.74	102
B.2.1.5.3.2	P-4	xDSL (ADSL, HDSL and UCL)/<6 circuits/Non-Dispatch/GA(days)	ADSL to Retail	3.30	1,051		
B.2.1.5.4.1	P-4	xDSL (ADSL, HDSL and UCL)/6-13 circuits/Dispatch/GA(days)	ADSL to Retail	3.50	4		
B.2.1.5.4.2	P-4	xDSL (ADSL, HDSL and UCL)/6-13 circuits/Non-Dispatch/GA(days)	ADSL to Retail	10.00	1		

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			Benchmark / Analog	October Data BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.2.1.5.5.1	P-4	xDSL (ADSL, HDSL and UCL)/>=14 circuits/Dispatch/GA(days)	ADSL to Retail	4.00	1		
B.2.1.5.5.2	P-4	xDSL (ADSL, HDSL and UCL)/>=14 circuits/Non-Dispatch/GA(days)	ADSL to Retail				
B.2.1.6.3.1	P-4	UNE ISDN/<6 circuits/Dispatch/GA(days)	ISDN - BRI	12.90	465	11.31	157
B.2.1.6.3.2	P-4	UNE ISDN/<6 circuits/Non-Dispatch/GA(days)	ISDN - BRI	1.41	582		
B.2.1.6.4.1	P-4	UNE ISDN/6-13 circuits/Dispatch/GA(days)	ISDN - BRI				
B.2.1.6.4.2	P-4	UNE ISDN/6-13 circuits/Non-Dispatch/GA(days)	ISDN - BRI	0.33	2		
B.2.1.6.5.1	P-4	UNE ISDN/>=14 circuits/Dispatch/GA(days)	ISDN - BRI				
B.2.1.6.5.2	P-4	UNE ISDN/>=14 circuits/Non-Dispatch/GA(days)	ISDN - BRI				
B.2.1.7.3.1	P-4	Line Sharing/<6 circuits/Dispatch/GA(days)	ADSL to Retail	4.03	1,929		
B.2.1.7.3.2	P-4	Line Sharing/<6 circuits/Non-Dispatch/GA(days)	ADSL to Retail	3.30	1,051	2.17	2
B.2.1.7.4.1	P-4	Line Sharing/6-13 circuits/Dispatch/GA(days)	ADSL to Retail	3.50	4		
B.2.1.7.4.2	P-4	Line Sharing/6-13 circuits/Non-Dispatch/GA(days)	ADSL to Retail	10.00	1		
B.2.1.7.5.1	P-4	Line Sharing/>=14 circuits/Dispatch/GA(days)	ADSL to Retail	4.00	1		
B.2.1.7.5.2	P-4	Line Sharing/>=14 circuits/Non-Dispatch/GA(days)	ADSL to Retail				
B.2.1.8.1.1	P-4	2W Analog Loop Design/<10 circuits/Dispatch/GA(days)	R&B - Disp	4.53	58,516	5.45	20
B.2.1.8.1.2	P-4	2W Analog Loop Design/<10 circuits/Non-Dispatch/GA(days)	R&B - Disp	4.53	58,516		
B.2.1.8.2.1	P-4	2W Analog Loop Design/>=10 circuits/Dispatch/GA(days)	R&B - Disp	11.80	227		
B.2.1.8.2.2	P-4	2W Analog Loop Design/>=10 circuits/Non-Dispatch/GA(days)	R&B - Disp	11.80	227		
B.2.1.9.1.1	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch/GA(days)	R&B (POTS) excl SB Or	4.45	56,951	4.53	30
B.2.1.9.1.4	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch In/GA(days)	R&B (POTS) excl SB Or	1.67	205,132		
B.2.1.9.2.1	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch/GA(days)	R&B (POTS) excl SB Or	10.30	164	6.00	1
B.2.1.9.2.4	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/GA(days)	R&B (POTS) excl SB Or	6.33	6		
B.2.1.10.1.1	P-4	2W Analog Loop w/INP Design/<10 circuits/Dispatch/GA(days)	R&B - Disp	4.53	58,516		
B.2.1.10.1.2	P-4	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/GA(days)	R&B - Disp	4.53	58,516		
B.2.1.10.2.1	P-4	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/GA(days)	R&B - Disp	11.80	227		
B.2.1.10.2.2	P-4	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/GA(days)	R&B - Disp	11.80	227		
B.2.1.11.1.1	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/GA(days)	R&B (POTS) excl SB Or	4.45	56,951		
B.2.1.11.1.4	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/GA(days)	R&B (POTS) excl SB Or	1.67	205,132		
B.2.1.11.2.1	P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/GA(days)	R&B (POTS) excl SB Or	10.30	164		
B.2.1.11.2.4	P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/GA(days)	R&B (POTS) excl SB Or	6.33	6		
B.2.1.12.1.1	P-4	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/GA(days)	R&B - Disp	4.53	58,516	5.28	46
B.2.1.12.1.2	P-4	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/GA(days)	R&B - Disp	4.53	58,516		
B.2.1.12.2.1	P-4	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/GA(days)	R&B - Disp	11.80	227		
B.2.1.12.2.2	P-4	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/GA(days)	R&B - Disp	11.80	227		
B.2.1.13.1.1	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/GA(days)	R&B (POTS) excl SB Or	4.45	56,951	5.49	102
B.2.1.13.1.4	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/GA(days)	R&B (POTS) excl SB Or	1.67	205,132		
B.2.1.13.2.1	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/GA(days)	R&B (POTS) excl SB Or	10.30	164	6.67	3
B.2.1.13.2.4	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/GA(days)	R&B (POTS) excl SB Or	6.33	6		
B.2.1.14.1.1	P-4	Other Design/<10 circuits/Dispatch/GA(days)	Design	29.74	2,244	4.85	87
B.2.1.14.1.2	P-4	Other Design/<10 circuits/Non-Dispatch/GA(days)	Design	12.38	520		
B.2.1.14.2.1	P-4	Other Design/>=10 circuits/Dispatch/GA(days)	Design	33.24	75	5.00	1
B.2.1.14.2.2	P-4	Other Design/>=10 circuits/Non-Dispatch/GA(days)	Design	4.46	254		
B.2.1.15.1.1	P-4	Other Non-Design/<10 circuits/Dispatch/GA(days)	R&B	4.53	58,516	3.31	129
B.2.1.15.1.2	P-4	Other Non-Design/<10 circuits/Non-Dispatch/GA(days)	R&B	0.92	474,193	1.81	7
B.2.1.15.2.1	P-4	Other Non-Design/>=10 circuits/Dispatch/GA(days)	R&B	11.80	227	6.00	2
B.2.1.15.2.2	P-4	Other Non-Design/>=10 circuits/Non-Dispatch/GA(days)	R&B	1.99	172	1.17	2
B.2.1.16.1.1	P-4	INP (Standalone)/<10 circuits/Dispatch/GA(days)	R&B (POTS)	4.45	56,951		

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B.2.1.16.1.2	P-4	INP (Standalone)/<10 circuits/Non-Dispatch/GA(days)	R&B (POTS)	0.92	472,149	1.53	5
B.2.1.16.2.1	P-4	INP (Standalone)/>=10 circuits/Dispatch/GA(days)	R&B (POTS)	10.30	164		
B.2.1.16.2.2	P-4	INP (Standalone)/>=10 circuits/Non-Dispatch/GA(days)	R&B (POTS)	2.90	14		
B.2.1.17.1.1	P-4	LNP (Standalone)/<10 circuits/Dispatch/GA(days)	R&B (POTS)	4.45	56,951	3.17	2
B.2.1.17.1.2	P-4	LNP (Standalone)/<10 circuits/Non-Dispatch/GA(days)	R&B (POTS)	0.92	472,149	0.70	3,005
B.2.1.17.2.1	P-4	LNP (Standalone)/>=10 circuits/Dispatch/GA(days)	R&B (POTS)	10.30	164		
B.2.1.17.2.2	P-4	LNP (Standalone)/>=10 circuits/Non-Dispatch/GA(days)	R&B (POTS)	2.90	14	0.63	11
B.2.1.18.1.1	P-4	Digital Loop < DS1/<10 circuits/Dispatch/GA(days)	Digital Loop < DS1	9.08	2,808	8.96	243
B.2.1.18.1.2	P-4	Digital Loop < DS1/<10 circuits/Non-Dispatch/GA(days)	Digital Loop < DS1	4.04	1,955		
B.2.1.18.2.1	P-4	Digital Loop < DS1/>=10 circuits/Dispatch/GA(days)	Digital Loop < DS1	3.60	5		
B.2.1.18.2.2	P-4	Digital Loop < DS1/>=10 circuits/Non-Dispatch/GA(days)	Digital Loop < DS1	5.00	1		
B.2.1.19.1.1	P-4	Digital Loop >= DS1/<10 circuits/Dispatch/GA(days)	Digital Loop >= DS1	44.46	417	6.28	110
B.2.1.19.1.2	P-4	Digital Loop >= DS1/<10 circuits/Non-Dispatch/GA(days)	Digital Loop >= DS1	10.84	442		
B.2.1.19.2.1	P-4	Digital Loop >= DS1/>=10 circuits/Dispatch/GA(days)	Digital Loop >= DS1	3.50	2		
B.2.1.19.2.2	P-4	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/GA(days)	Digital Loop >= DS1	4.46	253		
	Order Completion Interval within X days						
B.2.2.1	P-4	xDSL (ADSL, HDSL and UCL) Loop with Conditioning/<6 circuits/Dispatch/GA(days)	14 days			12.00	1
B.2.2.2	P-4	xDSL (ADSL, HDSL and UCL) Loop w/o Conditioning/<6 circuits/Dispatch/GA(days)	7 days			4.66	101
	Held Orders						
B.2.3.1.1.1	P-1	Switch Ports/<10 circuits/Facility/GA(days)	R&B (POTS)	10.28	226		
B.2.3.1.1.2	P-1	Switch Ports/<10 circuits/Equipment/GA(days)	R&B (POTS)	8.33	3		
B.2.3.1.1.3	P-1	Switch Ports/<10 circuits/Other/GA(days)	R&B (POTS)	8.54	13		
B.2.3.1.2.1	P-1	Switch Ports/>=10 circuits/Facility/GA(days)	R&B (POTS)	9.50	2		
B.2.3.1.2.2	P-1	Switch Ports/>=10 circuits/Equipment/GA(days)	R&B (POTS)	0.00	0		
B.2.3.1.2.3	P-1	Switch Ports/>=10 circuits/Other/GA(days)	R&B (POTS)	0.00	0		
B.2.3.2.1.1	P-1	Local Interoffice Transport/<10 circuits/Facility/GA(days)	DS1/ DS3 - Interoffice	22.00	1	0.00	0
B.2.3.2.1.2	P-1	Local Interoffice Transport/<10 circuits/Equipment/GA(days)	DS1/ DS3 - Interoffice	0.00	0	0.00	0
B.2.3.2.1.3	P-1	Local Interoffice Transport/<10 circuits/Other/GA(days)	DS1/ DS3 - Interoffice	10.50	12	0.00	0
B.2.3.2.2.1	P-1	Local Interoffice Transport/>=10 circuits/Facility/GA(days)	DS1/ DS3 - Interoffice				
B.2.3.2.2.2	P-1	Local Interoffice Transport/>=10 circuits/Equipment/GA(days)	DS1/ DS3 - Interoffice				
B.2.3.2.2.3	P-1	Local Interoffice Transport/>=10 circuits/Other/GA(days)	DS1/ DS3 - Interoffice				
B.2.3.3.1.1	P-1	Loop + Port Combinations/<10 circuits/Facility/GA(days)	R&B	10.14	233	14.00	2
B.2.3.3.1.2	P-1	Loop + Port Combinations/<10 circuits/Equipment/GA(days)	R&B	8.33	3	0.00	0
B.2.3.3.1.3	P-1	Loop + Port Combinations/<10 circuits/Other/GA(days)	R&B	8.54	13	0.00	0
B.2.3.3.2.1	P-1	Loop + Port Combinations/>=10 circuits/Facility/GA(days)	R&B	29.33	3	0.00	0
B.2.3.3.2.2	P-1	Loop + Port Combinations/>=10 circuits/Equipment/GA(days)	R&B	0.00	0	0.00	0
B.2.3.3.2.3	P-1	Loop + Port Combinations/>=10 circuits/Other/GA(days)	R&B	0.00	0	0.00	0
B.2.3.4.1.1	P-1	Combo Other/<10 circuits/Facility/GA(days)	R&B&D - Disp	10.14	233	0.00	0
B.2.3.4.1.2	P-1	Combo Other/<10 circuits/Equipment/GA(days)	R&B&D - Disp	8.33	3	0.00	0
B.2.3.4.1.3	P-1	Combo Other/<10 circuits/Other/GA(days)	R&B&D - Disp	22.35	17	0.00	0
B.2.3.4.2.1	P-1	Combo Other/>=10 circuits/Facility/GA(days)	R&B&D - Disp	29.33	3		
B.2.3.4.2.2	P-1	Combo Other/>=10 circuits/Equipment/GA(days)	R&B&D - Disp	12.00	2		
B.2.3.4.2.3	P-1	Combo Other/>=10 circuits/Other/GA(days)	R&B&D - Disp	14.00	1		
B.2.3.5.1.1	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Facility/GA(days)	ADSL to Retail	24.42	142	0.00	0
B.2.3.5.1.2	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Equipment/GA(days)	ADSL to Retail	1.00	1	0.00	0
B.2.3.5.1.3	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Other/GA(days)	ADSL to Retail	11.57	7	0.00	0

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			Benchmark / Analog	October Data BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.2.3.5.2.1	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Facility/GA(days)	ADSL to Retail	0.00	0		
B.2.3.5.2.2	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Equipment/GA(days)	ADSL to Retail	0.00	0		
B.2.3.5.2.3	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Other/GA(days)	ADSL to Retail	0.00	0		
B.2.3.6.1.1	P-1	UNE ISDN/<10 circuits/Facility/GA(days)	ISDN - BRI	3.00	1	0.00	0
B.2.3.6.1.2	P-1	UNE ISDN/<10 circuits/Equipment/GA(days)	ISDN - BRI	0.00	0	0.00	0
B.2.3.6.1.3	P-1	UNE ISDN/<10 circuits/Other/GA(days)	ISDN - BRI	140.00	1	0.00	0
B.2.3.6.2.1	P-1	UNE ISDN/>=10 circuits/Facility/GA(days)	ISDN - BRI				
B.2.3.6.2.2	P-1	UNE ISDN/>=10 circuits/Equipment/GA(days)	ISDN - BRI				
B.2.3.6.2.3	P-1	UNE ISDN/>=10 circuits/Other/GA(days)	ISDN - BRI				
B.2.3.7.1.1	P-1	Line Sharing/<10 circuits/Facility/GA(days)	ADSL to Retail	24.42	142	0.00	0
B.2.3.7.1.2	P-1	Line Sharing/<10 circuits/Equipment/GA(days)	ADSL to Retail	1.00	1	0.00	0
B.2.3.7.1.3	P-1	Line Sharing/<10 circuits/Other/GA(days)	ADSL to Retail	11.57	7	0.00	0
B.2.3.7.2.1	P-1	Line Sharing/>=10 circuits/Facility/GA(days)	ADSL to Retail	0.00	0		
B.2.3.7.2.2	P-1	Line Sharing/>=10 circuits/Equipment/GA(days)	ADSL to Retail	0.00	0		
B.2.3.7.2.3	P-1	Line Sharing/>=10 circuits/Other/GA(days)	ADSL to Retail	0.00	0		
B.2.3.8.1.1	P-1	2W Analog Loop Design/<10 circuits/Facility/GA(days)	R&B - Disp	10.14	233	0.00	0
B.2.3.8.1.2	P-1	2W Analog Loop Design/<10 circuits/Equipment/GA(days)	R&B - Disp	8.33	3	0.00	0
B.2.3.8.1.3	P-1	2W Analog Loop Design/<10 circuits/Other/GA(days)	R&B - Disp	8.54	13	0.00	0
B.2.3.8.2.1	P-1	2W Analog Loop Design/>=10 circuits/Facility/GA(days)	R&B - Disp	29.33	3	0.00	0
B.2.3.8.2.2	P-1	2W Analog Loop Design/>=10 circuits/Equipment/GA(days)	R&B - Disp	0.00	0	0.00	0
B.2.3.8.2.3	P-1	2W Analog Loop Design/>=10 circuits/Other/GA(days)	R&B - Disp	0.00	0	0.00	0
B.2.3.9.1.1	P-1	2W Analog Loop Non-Design/<10 circuits/Facility/GA(days)	R&B (POTS) excl SB Or	10.28	226	0.00	0
B.2.3.9.1.2	P-1	2W Analog Loop Non-Design/<10 circuits/Equipment/GA(days)	R&B (POTS) excl SB Or	8.33	3	0.00	0
B.2.3.9.1.3	P-1	2W Analog Loop Non-Design/<10 circuits/Other/GA(days)	R&B (POTS) excl SB Or	8.54	13	0.00	0
B.2.3.9.2.1	P-1	2W Analog Loop Non-Design/>=10 circuits/Facility/GA(days)	R&B (POTS) excl SB Or	9.50	2	0.00	0
B.2.3.9.2.2	P-1	2W Analog Loop Non-Design/>=10 circuits/Equipment/GA(days)	R&B (POTS) excl SB Or	0.00	0	0.00	0
B.2.3.9.2.3	P-1	2W Analog Loop Non-Design/>=10 circuits/Other/GA(days)	R&B (POTS) excl SB Or	0.00	0	0.00	0
B.2.3.10.1.1	P-1	2W Analog Loop w/INP Design/<10 circuits/Facility/GA(days)	R&B - Disp	10.14	233		
B.2.3.10.1.2	P-1	2W Analog Loop w/INP Design/<10 circuits/Equipment/GA(days)	R&B - Disp	8.33	3		
B.2.3.10.1.3	P-1	2W Analog Loop w/INP Design/<10 circuits/Other/GA(days)	R&B - Disp	8.54	13		
B.2.3.10.2.1	P-1	2W Analog Loop w/INP Design/>=10 circuits/Facility/GA(days)	R&B - Disp	29.33	3		
B.2.3.10.2.2	P-1	2W Analog Loop w/INP Design/>=10 circuits/Equipment/GA(days)	R&B - Disp	0.00	0		
B.2.3.10.2.3	P-1	2W Analog Loop w/INP Design/>=10 circuits/Other/GA(days)	R&B - Disp	0.00	0		
B.2.3.11.1.1	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Facility/GA(days)	R&B (POTS) excl SB Or	10.28	226		
B.2.3.11.1.2	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Equipment/GA(days)	R&B (POTS) excl SB Or	8.33	3		
B.2.3.11.1.3	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Other/GA(days)	R&B (POTS) excl SB Or	8.54	13		
B.2.3.11.2.1	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Facility/GA(days)	R&B (POTS) excl SB Or	9.50	2		
B.2.3.11.2.2	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Equipment/GA(days)	R&B (POTS) excl SB Or	0.00	0		
B.2.3.11.2.3	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Other/GA(days)	R&B (POTS) excl SB Or	0.00	0		
B.2.3.12.1.1	P-1	2W Analog Loop w/LNP Design/<10 circuits/Facility/GA(days)	R&B - Disp	10.14	233	0.00	0
B.2.3.12.1.2	P-1	2W Analog Loop w/LNP Design/<10 circuits/Equipment/GA(days)	R&B - Disp	8.33	3	0.00	0
B.2.3.12.1.3	P-1	2W Analog Loop w/LNP Design/<10 circuits/Other/GA(days)	R&B - Disp	8.54	13	0.00	0
B.2.3.12.2.1	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Facility/GA(days)	R&B - Disp	29.33	3	0.00	0
B.2.3.12.2.2	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Equipment/GA(days)	R&B - Disp	0.00	0	0.00	0
B.2.3.12.2.3	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Other/GA(days)	R&B - Disp	0.00	0	0.00	0
B.2.3.13.1.1	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Facility/GA(days)	R&B (POTS) excl SB Or	10.28	226	15.00	1
B.2.3.13.1.2	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Equipment/GA(days)	R&B (POTS) excl SB Or	8.33	3	0.00	0

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			Benchmark / Analog	October Data BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.2.3.13.1.3	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Other/GA(days)	R&B (POTS) excl SB Or	8.54	13	0.00	0
B.2.3.13.2.1	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Facility/GA(days)	R&B (POTS) excl SB Or	9.50	2	0.00	0
B.2.3.13.2.2	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Equipment/GA(days)	R&B (POTS) excl SB Or	0.00	0	0.00	0
B.2.3.13.2.3	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Other/GA(days)	R&B (POTS) excl SB Or	0.00	0	0.00	0
B.2.3.14.1.1	P-1	Other Design/<10 circuits/Facility/GA(days)	Design	0.00	0	0.00	0
B.2.3.14.1.2	P-1	Other Design/<10 circuits/Equipment/GA(days)	Design	0.00	0	0.00	0
B.2.3.14.1.3	P-1	Other Design/<10 circuits/Other/GA(days)	Design	67.25	4	0.00	0
B.2.3.14.2.1	P-1	Other Design/>=10 circuits/Facility/GA(days)	Design	0.00	0	0.00	0
B.2.3.14.2.2	P-1	Other Design/>=10 circuits/Equipment/GA(days)	Design	12.00	2	0.00	0
B.2.3.14.2.3	P-1	Other Design/>=10 circuits/Other/GA(days)	Design	14.00	1	0.00	0
B.2.3.15.1.1	P-1	Other Non-Design/<10 circuits/Facility/GA(days)	R&B	10.14	233	7.00	1
B.2.3.15.1.2	P-1	Other Non-Design/<10 circuits/Equipment/GA(days)	R&B	8.33	3	0.00	0
B.2.3.15.1.3	P-1	Other Non-Design/<10 circuits/Other/GA(days)	R&B	8.54	13	0.00	0
B.2.3.15.2.1	P-1	Other Non-Design/>=10 circuits/Facility/GA(days)	R&B	29.33	3	0.00	0
B.2.3.15.2.2	P-1	Other Non-Design/>=10 circuits/Equipment/GA(days)	R&B	0.00	0	0.00	0
B.2.3.15.2.3	P-1	Other Non-Design/>=10 circuits/Other/GA(days)	R&B	0.00	0	0.00	0
B.2.3.16.1.1	P-1	INP (Standalone)/<10 circuits/Facility/GA(days)	R&B (POTS)	10.28	226	0.00	0
B.2.3.16.1.2	P-1	INP (Standalone)/<10 circuits/Equipment/GA(days)	R&B (POTS)	8.33	3	0.00	0
B.2.3.16.1.3	P-1	INP (Standalone)/<10 circuits/Other/GA(days)	R&B (POTS)	8.54	13	0.00	0
B.2.3.16.2.1	P-1	INP (Standalone)/>=10 circuits/Facility/GA(days)	R&B (POTS)	9.50	2		
B.2.3.16.2.2	P-1	INP (Standalone)/>=10 circuits/Equipment/GA(days)	R&B (POTS)	0.00	0		
B.2.3.16.2.3	P-1	INP (Standalone)/>=10 circuits/Other/GA(days)	R&B (POTS)	0.00	0		
B.2.3.17.1.1	P-1	LNP (Standalone)/<10 circuits/Facility/GA(days)	R&B (POTS)	10.28	226	0.00	0
B.2.3.17.1.2	P-1	LNP (Standalone)/<10 circuits/Equipment/GA(days)	R&B (POTS)	8.33	3	0.00	0
B.2.3.17.1.3	P-1	LNP (Standalone)/<10 circuits/Other/GA(days)	R&B (POTS)	8.54	13	0.00	0
B.2.3.17.2.1	P-1	LNP (Standalone)/>=10 circuits/Facility/GA(days)	R&B (POTS)	9.50	2	0.00	0
B.2.3.17.2.2	P-1	LNP (Standalone)/>=10 circuits/Equipment/GA(days)	R&B (POTS)	0.00	0	0.00	0
B.2.3.17.2.3	P-1	LNP (Standalone)/>=10 circuits/Other/GA(days)	R&B (POTS)	0.00	0	0.00	0
B.2.3.18.1.1	P-1	Digital Loop < DS1/<10 circuits/Facility/GA(days)	Digital Loop < DS1	23.71	147	0.00	0
B.2.3.18.1.2	P-1	Digital Loop < DS1/<10 circuits/Equipment/GA(days)	Digital Loop < DS1	1.00	1	0.00	0
B.2.3.18.1.3	P-1	Digital Loop < DS1/<10 circuits/Other/GA(days)	Digital Loop < DS1	36.17	12	0.00	0
B.2.3.18.2.1	P-1	Digital Loop < DS1/>=10 circuits/Facility/GA(days)	Digital Loop < DS1	0.00	0		
B.2.3.18.2.2	P-1	Digital Loop < DS1/>=10 circuits/Equipment/GA(days)	Digital Loop < DS1	0.00	0		
B.2.3.18.2.3	P-1	Digital Loop < DS1/>=10 circuits/Other/GA(days)	Digital Loop < DS1	0.00	0		
B.2.3.19.1.1	P-1	Digital Loop >= DS1/<10 circuits/Facility/GA(days)	Digital Loop >= DS1	0.00	0	15.00	1
B.2.3.19.1.2	P-1	Digital Loop >= DS1/<10 circuits/Equipment/GA(days)	Digital Loop >= DS1	0.00	0	0.00	0
B.2.3.19.1.3	P-1	Digital Loop >= DS1/<10 circuits/Other/GA(days)	Digital Loop >= DS1	0.00	0	0.00	0
B.2.3.19.2.1	P-1	Digital Loop >= DS1/>=10 circuits/Facility/GA(days)	Digital Loop >= DS1	0.00	0		
B.2.3.19.2.2	P-1	Digital Loop >= DS1/>=10 circuits/Equipment/GA(days)	Digital Loop >= DS1	0.00	0		
B.2.3.19.2.3	P-1	Digital Loop >= DS1/>=10 circuits/Other/GA(days)	Digital Loop >= DS1	0.00	0		
		% Jeopardies - Mechanized					
B.2.5.1	P-2	Switch Ports/GA(%)	R&B (POTS)	0.49%	555,609		
B.2.5.2	P-2	Local Interoffice Transport/GA(%)	DS1/ DS3 - Interoffice	35.02%	2,504		
B.2.5.3	P-2	Loop + Port Combinations/GA(%)	R&B	0.52%	559,572	0.12%	32,848
B.2.5.4	P-2	Combo Other/GA(%)	R&B&D - Disp	4.30%	72,209	11.11%	18
B.2.5.5	P-2	xDSL (ADSL, HDSL and UCL)/GA(%)	ADSL to Retail	16.02%	12,024	2.13%	94
B.2.5.6	P-2	UNE ISDN/GA(%)	ISDN - BRI	6.96%	1,107	4.35%	46

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B.2.5.7	P-2	Line Sharing/GA(%)	ADSL to Retail	16.02%	12,024	0.00%	2
B.2.5.8	P-2	2W Analog Loop Design/GA(%)	R&B - Disp	0.52%	559,572	0.00%	21
B.2.5.9	P-2	2W Analog Loop Non-Design/GA(%)	R&B (POTS) excl SB Or	0.95%	288,149	10.34%	29
B.2.5.10	P-2	2W Analog Loop w/INP Design/GA(%)	R&B - Disp	0.52%	559,572		
B.2.5.11	P-2	2W Analog Loop w/INP Non-Design/GA(%)	R&B (POTS) excl SB Or	0.95%	288,149		
B.2.5.12	P-2	2W Analog Loop w/LNP Design/GA(%)	R&B - Disp	0.52%	559,572	0.00%	1,322
B.2.5.13	P-2	2W Analog Loop w/LNP Non-Design/GA(%)	R&B (POTS) excl SB Or	0.95%	288,149	0.07%	5,549
B.2.5.14	P-2	Other Design/GA(%)	Design	5.75%	3,910	10.00%	110
B.2.5.15	P-2	Other Non-Design/GA(%)	R&B	0.52%	559,572	4.30%	186
B.2.5.16	P-2	INP (Standalone)/GA(%)	R&B (POTS)	0.49%	555,609	0.00%	5
B.2.5.17	P-2	LNP (Standalone)/GA(%)	R&B (POTS)	0.49%	555,609	0.00%	3,768
B.2.5.18	P-2	Digital Loop < DS1/GA(%)	Digital Loop < DS1	15.12%	13,912	1.57%	127
B.2.5.19	P-2	Digital Loop >= DS1/GA(%)	Digital Loop >= DS1	5.71%	1,314	40.58%	69
	% Jeopardies - Non-Mechanized						
B.2.6.1	P-2	Switch Ports/GA(%)	Diagnostic				
B.2.6.2	P-2	Local Interoffice Transport/GA(%)	Diagnostic			0.00%	1
B.2.6.3	P-2	Loop + Port Combinations/GA(%)	Diagnostic			0.87%	230
B.2.6.4	P-2	Combo Other/GA(%)	Diagnostic			19.14%	209
B.2.6.5	P-2	xDSL (ADSL, HDSL and UCL)/GA(%)	Diagnostic			5.13%	78
B.2.6.6	P-2	UNE ISDN/GA(%)	Diagnostic			11.69%	154
B.2.6.7	P-2	Line Sharing/GA(%)	Diagnostic			0.00%	1
B.2.6.8	P-2	2W Analog Loop Design/GA(%)	Diagnostic			16.67%	6
B.2.6.9	P-2	2W Analog Loop Non-Design/GA(%)	Diagnostic			0.00%	29
B.2.6.10	P-2	2W Analog Loop w/INP Design/GA(%)	Diagnostic				
B.2.6.11	P-2	2W Analog Loop w/INP Non-Design/GA(%)	Diagnostic				
B.2.6.12	P-2	2W Analog Loop w/LNP Design/GA(%)	Diagnostic			4.17%	120
B.2.6.13	P-2	2W Analog Loop w/LNP Non-Design/GA(%)	Diagnostic			3.24%	1,359
B.2.6.14	P-2	Other Design/GA(%)	Diagnostic			0.00%	21
B.2.6.15	P-2	Other Non-Design/GA(%)	Diagnostic			1.85%	54
B.2.6.16	P-2	INP (Standalone)/GA(%)	Diagnostic			0.00%	1
B.2.6.17	P-2	LNP (Standalone)/GA(%)	Diagnostic			0.00%	1,618
B.2.6.18	P-2	Digital Loop < DS1/GA(%)	Diagnostic			8.80%	216
B.2.6.19	P-2	Digital Loop >= DS1/GA(%)	Diagnostic			14.95%	107
	Average Jeopardy Notice Interval - Mechanized						
B.2.8.1	P-2	Switch Ports/GA(hours)	>= 48 hrs				
B.2.8.2	P-2	Local Interoffice Transport/GA(hours)	>= 48 hrs				
B.2.8.3	P-2	Loop + Port Combinations/GA(hours)	>= 48 hrs			174.15	39
B.2.8.4	P-2	Combo Other/GA(hours)	>= 48 hrs			252.00	2
B.2.8.5	P-2	xDSL (ADSL, HDSL and UCL)/GA(hours)	>= 48 hrs			432.00	2
B.2.8.6	P-2	UNE ISDN/GA(hours)	>= 48 hrs			348.00	2
B.2.8.7	P-2	Line Sharing/GA(hours)	>= 48 hrs				
B.2.8.8	P-2	2W Analog Loop Design/GA(hours)	>= 48 hrs				
B.2.8.9	P-2	2W Analog Loop Non-Design/GA(hours)	>= 48 hrs			216.00	3
B.2.8.10	P-2	2W Analog Loop w/INP Design/GA(hours)	>= 48 hrs				
B.2.8.11	P-2	2W Analog Loop w/INP Non-Design/GA(hours)	>= 48 hrs				
B.2.8.12	P-2	2W Analog Loop w/LNP Design/GA(hours)	>= 48 hrs				

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			Benchmark / Analog	October Data BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.2.8.13	P-2	2W Analog Loop w/LNP Non-Design/GA(hours)	>= 48 hrs			28.35	4
B.2.8.14	P-2	Other Design/GA(hours)	>= 48 hrs			248.73	11
B.2.8.15	P-2	Other Non-Design/GA(hours)	>= 48 hrs			195.00	8
B.2.8.16	P-2	INP (Standalone)/GA(hours)	>= 48 hrs				
B.2.8.17	P-2	LNP (Standalone)/GA(hours)	>= 48 hrs				
B.2.8.18	P-2	Digital Loop < DS1/GA(hours)	>= 48 hrs			348.00	2
B.2.8.19	P-2	Digital Loop >= DS1/GA(hours)	>= 48 hrs			187.71	28
	Average Jeopardy Notice Interval - Non-Mechanized						
B.2.9.1	P-2	Switch Ports/GA(hours)	Diagnostic				
B.2.9.2	P-2	Local Interoffice Transport/GA(hours)	Diagnostic				
B.2.9.3	P-2	Loop + Port Combinations/GA(hours)	Diagnostic			228.00	2
B.2.9.4	P-2	Combo Other/GA(hours)	Diagnostic			240.00	40
B.2.9.5	P-2	xDSL (ADSL, HDSL and UCL)/GA(hours)	Diagnostic			120.00	4
B.2.9.6	P-2	UNE ISDN/GA(hours)	Diagnostic			262.67	18
B.2.9.7	P-2	Line Sharing/GA(hours)	Diagnostic				
B.2.9.8	P-2	2W Analog Loop Design/GA(hours)	Diagnostic			408.00	1
B.2.9.9	P-2	2W Analog Loop Non-Design/GA(hours)	Diagnostic				
B.2.9.10	P-2	2W Analog Loop w/INP Design/GA(hours)	Diagnostic				
B.2.9.11	P-2	2W Analog Loop w/INP Non-Design/GA(hours)	Diagnostic				
B.2.9.12	P-2	2W Analog Loop w/LNP Design/GA(hours)	Diagnostic			78.17	5
B.2.9.13	P-2	2W Analog Loop w/LNP Non-Design/GA(hours)	Diagnostic			108.26	44
B.2.9.14	P-2	Other Design/GA(hours)	Diagnostic				
B.2.9.15	P-2	Other Non-Design/GA(hours)	Diagnostic			360.00	1
B.2.9.16	P-2	INP (Standalone)/GA(hours)	Diagnostic				
B.2.9.17	P-2	LNP (Standalone)/GA(hours)	Diagnostic				
B.2.9.18	P-2	Digital Loop < DS1/GA(hours)	Diagnostic			257.68	19
B.2.9.19	P-2	Digital Loop >= DS1/GA(hours)	Diagnostic			171.00	16
	% Jeopardy Notice >= 48 hours - Mechanized						
B.2.10.1	P-2	Switch Ports/GA(%)	95% >= 48 hrs				
B.2.10.2	P-2	Local Interoffice Transport/GA(%)	95% >= 48 hrs				
B.2.10.3	P-2	Loop + Port Combinations/GA(%)	95% >= 48 hrs			100.00%	39
B.2.10.4	P-2	Combo Other/GA(%)	95% >= 48 hrs			100.00%	2
B.2.10.5	P-2	xDSL (ADSL, HDSL and UCL)/GA(%)	95% >= 48 hrs			100.00%	2
B.2.10.6	P-2	UNE ISDN/GA(%)	95% >= 48 hrs			100.00%	2
B.2.10.7	P-2	Line Sharing/GA(%)	95% >= 48 hrs				
B.2.10.8	P-2	2W Analog Loop Design/GA(%)	95% >= 48 hrs				
B.2.10.9	P-2	2W Analog Loop Non-Design/GA(%)	95% >= 48 hrs			100.00%	3
B.2.10.10	P-2	2W Analog Loop w/INP Design/GA(%)	95% >= 48 hrs				
B.2.10.11	P-2	2W Analog Loop w/INP Non-Design/GA(%)	95% >= 48 hrs				
B.2.10.12	P-2	2W Analog Loop w/LNP Design/GA(%)	95% >= 48 hrs				
B.2.10.13	P-2	2W Analog Loop w/LNP Non-Design/GA(%)	95% >= 48 hrs			0.00%	4
B.2.10.14	P-2	Other Design/GA(%)	95% >= 48 hrs			100.00%	11
B.2.10.15	P-2	Other Non-Design/GA(%)	95% >= 48 hrs			100.00%	8
B.2.10.16	P-2	INP (Standalone)/GA(%)	95% >= 48 hrs				
B.2.10.17	P-2	LNP (Standalone)/GA(%)	95% >= 48 hrs				
B.2.10.18	P-2	Digital Loop < DS1/GA(%)	95% >= 48 hrs			100.00%	2

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			Benchmark / Analog	October Data BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.2.10.19	P-2	Digital Loop >= DS1/GA(%)	95% >= 48 hrs			100.00%	28
		% Jeopardy Notice >= 48 hours - Non-Mechanized					
B.2.11.1	P-2	Switch Ports/GA(%)	Diagnostic				
B.2.11.2	P-2	Local Interoffice Transport/GA(%)	Diagnostic				
B.2.11.3	P-2	Loop + Port Combinations/GA(%)	Diagnostic			100.00%	2
B.2.11.4	P-2	Combo Other/GA(%)	Diagnostic			100.00%	40
B.2.11.5	P-2	xDSL (ADSL, HDSL and UCL)/GA(%)	Diagnostic			100.00%	4
B.2.11.6	P-2	UNE ISDN/GA(%)	Diagnostic			100.00%	18
B.2.11.7	P-2	Line Sharing/GA(%)	Diagnostic				
B.2.11.8	P-2	2W Analog Loop Design/GA(%)	Diagnostic			100.00%	1
B.2.11.9	P-2	2W Analog Loop Non-Design/GA(%)	Diagnostic				
B.2.11.10	P-2	2W Analog Loop w/INP Design/GA(%)	Diagnostic				
B.2.11.11	P-2	2W Analog Loop w/INP Non-Design/GA(%)	Diagnostic				
B.2.11.12	P-2	2W Analog Loop w/LNP Design/GA(%)	Diagnostic			100.00%	5
B.2.11.13	P-2	2W Analog Loop w/LNP Non-Design/GA(%)	Diagnostic			63.64%	44
B.2.11.14	P-2	Other Design/GA(%)	Diagnostic				
B.2.11.15	P-2	Other Non-Design/GA(%)	Diagnostic			100.00%	1
B.2.11.16	P-2	INP (Standalone)/GA(%)	Diagnostic				
B.2.11.17	P-2	LNP (Standalone)/GA(%)	Diagnostic				
B.2.11.18	P-2	Digital Loop < DS1/GA(%)	Diagnostic			100.00%	19
B.2.11.19	P-2	Digital Loop >= DS1/GA(%)	Diagnostic			100.00%	16
		Coordinated Customers Conversions					
B.2.12.1	P-7	Loops with INP/GA(%)	>= 95% w in 15 min			100.00%	1
B.2.12.2	P-7	Loops with LNP/GA(%)	>= 95% w in 15 min			99.72%	2,522
		% Hot Cuts > 15 minutes Early					
B.2.13.1	P-7A	Time-Specific SL1/GA(%)	<= 5%			0.00%	791
B.2.13.2	P-7A	Time-Specific SL2/GA(%)	<= 5%			0.00%	66
B.2.13.3	P-7A	Non-Time Specific SL1/GA(%)	<= 5%			0.00%	123
B.2.13.4	P-7A	Non-Time Specific SL2/GA(%)	<= 5%			0.00%	106
		Hot Cut Timeliness					
B.2.14.1	P-7A	Time-Specific SL1/GA(%)	>= 95% w in 15 min			99.49%	791
B.2.14.2	P-7A	Time-Specific SL2/GA(%)	>= 95% w in 15 min			100.00%	66
B.2.14.3	P-7A	Non-Time Specific SL1/GA(%)	>= 95% w in 15 min			100.00%	123
B.2.14.4	P-7A	Non-Time Specific SL2/GA(%)	>= 95% w in 15 min			100.00%	106
		% Hot Cuts > 15 minutes Late					
B.2.15.1	P-7A	Time-Specific SL1/GA(%)	<= 5%			0.51%	791
B.2.15.2	P-7A	Time-Specific SL2/GA(%)	<= 5%			0.00%	66
B.2.15.3	P-7A	Non-Time Specific SL1/GA(%)	<= 5%			0.00%	123
B.2.15.4	P-7A	Non-Time Specific SL2/GA(%)	<= 5%			0.00%	106
		Average Recovery Time - CCC					
B.2.16.1	P-7B	Loops with INP/GA(time units)	Diagnostic				
B.2.16.2	P-7B	Loops with LNP/GA(time units)	Diagnostic			1470.26	7
		% Provisioning Troubles within 7 Days - Hot Cuts					

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			Benchmark / Analog	October Data BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.2.17.1.1	P-7C	UNE Loop Design/Dispatch/GA(%)	<= 5%			2.81%	463
B.2.17.1.2	P-7C	UNE Loop Design/Non-Dispatch/GA(%)	<= 5%			0.00%	1
B.2.17.2.1	P-7C	UNE Loop Non-Design/Dispatch/GA(%)	<= 5%			1.53%	1,441
B.2.17.2.2	P-7C	UNE Loop Non-Design/Non-Dispatch/GA(%)	<= 5%			1.13%	353
	% Missed Installation Appointments						
B.2.18.1.1.1	P-3	Switch Ports/<10 circuits/Dispatch/GA(%)	R&B (POTS)	3.76%	64,103		
B.2.18.1.1.2	P-3	Switch Ports/<10 circuits/Non-Dispatch/GA(%)	R&B (POTS)	0.03%	488,191		
B.2.18.1.2.1	P-3	Switch Ports/>=10 circuits/Dispatch/GA(%)	R&B (POTS)	5.64%	195		
B.2.18.1.2.2	P-3	Switch Ports/>=10 circuits/Non-Dispatch/GA(%)	R&B (POTS)	0.00%	15		
B.2.18.2.1.1	P-3	Local Interoffice Transport/<10 circuits/Dispatch/GA(%)	DS1/DS3	2.70%	2,187	100.00%	1
B.2.18.2.1.2	P-3	Local Interoffice Transport/<10 circuits/Non-Dispatch/GA(%)	DS1/DS3				
B.2.18.2.2.1	P-3	Local Interoffice Transport/>=10 circuits/Dispatch/GA(%)	DS1/DS3				
B.2.18.2.2.2	P-3	Local Interoffice Transport/>=10 circuits/Non-Dispatch/GA(%)	DS1/DS3				
B.2.18.3.1.1	P-3	Loop + Port Combinations/<10 circuits/Dispatch/GA(%)	R&B	3.75%	65,720	3.02%	1,158
B.2.18.3.1.2	P-3	Loop + Port Combinations/<10 circuits/Non-Dispatch/GA(%)	R&B	0.03%	490,256	0.06%	41,491
B.2.18.3.1.3	P-3	Loop + Port Combinations/<10 circuits/Switch Based Orders/GA(%)	R&B	0.00%	268,108	0.00%	10,856
B.2.18.3.1.4	P-3	Loop + Port Combinations/<10 circuits/Dispatch In/GA(%)	R&B	0.06%	222,148	0.08%	30,635
B.2.18.3.2.1	P-3	Loop + Port Combinations/>=10 circuits/Dispatch/GA(%)	R&B	6.13%	261	12.50%	8
B.2.18.3.2.2	P-3	Loop + Port Combinations/>=10 circuits/Non-Dispatch/GA(%)	R&B	0.00%	177	0.00%	1
B.2.18.3.2.3	P-3	Loop + Port Combinations/>=10 circuits/Switch Based Orders/GA(%)	R&B	0.00%	43		
B.2.18.3.2.4	P-3	Loop + Port Combinations/>=10 circuits/Dispatch In/GA(%)	R&B	0.00%	134	0.00%	1
B.2.18.4.1.1	P-3	Combo Other/<10 circuits/Dispatch/GA(%)	R&B&D - Disp	3.80%	68,095	3.08%	227
B.2.18.4.1.4	P-3	Combo Other/<10 circuits/Dispatch In/GA(%)	R&B&D - Disp	3.80%	68,095		
B.2.18.4.2.1	P-3	Combo Other/>=10 circuits/Dispatch/GA(%)	R&B&D - Disp	4.61%	347		
B.2.18.4.2.4	P-3	Combo Other/>=10 circuits/Dispatch In/GA(%)	R&B&D - Disp	4.61%	347		
B.2.18.5.1.1	P-3	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/GA(%)	ADSL to Retail	8.41%	8,613	0.00%	164
B.2.18.5.1.2	P-3	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/GA(%)	ADSL to Retail	0.00%	3,379		
B.2.18.5.2.1	P-3	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/GA(%)	ADSL to Retail	6.25%	16		
B.2.18.5.2.2	P-3	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/GA(%)	ADSL to Retail				
B.2.18.6.1.1	P-3	UNE ISDN/<10 circuits/Dispatch/GA(%)	ISDN - BRI	5.76%	486	2.59%	193
B.2.18.6.1.2	P-3	UNE ISDN/<10 circuits/Non-Dispatch/GA(%)	ISDN - BRI	0.68%	587		
B.2.18.6.2.1	P-3	UNE ISDN/>=10 circuits/Dispatch/GA(%)	ISDN - BRI				
B.2.18.6.2.2	P-3	UNE ISDN/>=10 circuits/Non-Dispatch/GA(%)	ISDN - BRI				
B.2.18.7.1.1	P-3	Line Sharing/<10 circuits/Dispatch/GA(%)	ADSL to Retail	8.41%	8,613		
B.2.18.7.1.2	P-3	Line Sharing/<10 circuits/Non-Dispatch/GA(%)	ADSL to Retail	0.00%	3,379	0.00%	3
B.2.18.7.2.1	P-3	Line Sharing/>=10 circuits/Dispatch/GA(%)	ADSL to Retail	6.25%	16		
B.2.18.7.2.2	P-3	Line Sharing/>=10 circuits/Non-Dispatch/GA(%)	ADSL to Retail				
B.2.18.8.1.1	P-3	2W Analog Loop Design/<10 circuits/Dispatch/GA(%)	R&B - Disp	3.75%	65,720	2.04%	49
B.2.18.8.1.2	P-3	2W Analog Loop Design/<10 circuits/Non-Dispatch/GA(%)	R&B - Disp	3.75%	65,720		
B.2.18.8.2.1	P-3	2W Analog Loop Design/>=10 circuits/Dispatch/GA(%)	R&B - Disp	6.13%	261	0.00%	1
B.2.18.8.2.2	P-3	2W Analog Loop Design/>=10 circuits/Non-Dispatch/GA(%)	R&B - Disp	6.13%	261		
B.2.18.9.1.1	P-3	2W Analog Loop Non-Design/<10 circuits/Dispatch/GA(%)	R&B (POTS) excl SB Or	3.76%	64,103	1.64%	61
B.2.18.9.1.4	P-3	2W Analog Loop Non-Design/<10 circuits/Dispatch In/GA(%)	R&B (POTS) excl SB Or	0.06%	221,023		
B.2.18.9.2.1	P-3	2W Analog Loop Non-Design/>=10 circuits/Dispatch/GA(%)	R&B (POTS) excl SB Or	5.64%	195	0.00%	2
B.2.18.9.2.4	P-3	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/GA(%)	R&B (POTS) excl SB Or	0.00%	7		
B.2.18.10.1.1	P-3	2W Analog Loop w/INP Design/<10 circuits/Dispatch/GA(%)	R&B - Disp	3.75%	65,720		
B.2.18.10.1.2	P-3	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/GA(%)	R&B - Disp	3.75%	65,720		