# BellSouth Monthly State Summary

## *Georgia, October 2001*

| | | | Benchmark / Analog | BST Measure | BST Volume | CLEC Measure | CLEC Volume | Standard Deviation | Standard Error | ZScore | Equity |
|---|---|---|---|---|---|---|---|---|---|---|---|
| A.3.1.3.2 | M&R-1 | Design (Specials)/Non-Dispatch/GA(%) | Design | 0.42% | 2,126 | 0.00% | 85 | | 0.00718 | 0.5895 | YES |
| A.3.1.4.1 | M&R-1 | PBX/Dispatch/GA(%) | PBX | 13.88% | 245 | 5.00% | 20 | | 0.08040 | 1.1042 | YES |
| A.3.1.4.2 | M&R-1 | PBX/Non-Dispatch/GA(%) | PBX | 0.86% | 116 | 50.00% | 2 | | 0.06593 | -7.4530 | NO |
| A.3.1.5.1 | M&R-1 | Centrex/Dispatch/GA(%) | Centrex | 11.24% | 1,850 | 3.85% | 26 | | 0.06239 | 1.1857 | YES |
| A.3.1.5.2 | M&R-1 | Centrex/Non-Dispatch/GA(%) | Centrex | 5.32% | 1,279 | 0.00% | 13 | | 0.06254 | 0.8501 | YES |
| A.3.1.6.1 | M&R-1 | ISDN/Dispatch/GA(%) | ISDN | 15.13% | 522 | 7.69% | 13 | | 0.10063 | 0.7395 | YES |
| A.3.1.6.2 | M&R-1 | ISDN/Non-Dispatch/GA(%) | ISDN | 2.32% | 732 | 20.00% | 10 | | 0.04795 | -3.6865 | NO |
| | ***Customer Trouble Report Rate*** | | | | | | | | | | |
| A.3.2.1.1 | M&R-2 | Residence/Dispatch/GA(%) | Res | 2.43% | 2,694,126 | 2.40% | 74,439 | | 0.00058 | 0.5414 | YES |
| A.3.2.1.2 | M&R-2 | Residence/Non-Dispatch/GA(%) | Res | 1.42% | 2,694,126 | 0.74% | 74,439 | | 0.00044 | 15.4627 | YES |
| A.3.2.2.1 | M&R-2 | Business/Dispatch/GA(%) | Bus | 1.35% | 727,050 | 1.58% | 16,811 | | 0.00091 | -2.4564 | NO |
| A.3.2.2.2 | M&R-2 | Business/Non-Dispatch/GA(%) | Bus | 0.90% | 727,050 | 0.88% | 16,811 | | 0.00074 | 0.3212 | YES |
| A.3.2.3.1 | M&R-2 | Design (Specials)/Dispatch/GA(%) | Design | 0.35% | 310,404 | 1.29% | 5,746 | | 0.00079 | -11.7968 | NO |
| A.3.2.3.2 | M&R-2 | Design (Specials)/Non-Dispatch/GA(%) | Design | 0.68% | 310,404 | 1.48% | 5,746 | | 0.00110 | -7.2095 | NO |
| A.3.2.4.1 | M&R-2 | PBX/Dispatch/GA(%) | PBX | 0.17% | 140,236 | 0.52% | 3,853 | | 0.00068 | -5.0453 | NO |
| A.3.2.4.2 | M&R-2 | PBX/Non-Dispatch/GA(%) | PBX | 0.08% | 140,236 | 0.05% | 3,853 | | 0.00047 | 0.6560 | YES |
| A.3.2.5.1 | M&R-2 | Centrex/Dispatch/GA(%) | Centrex | 0.53% | 351,811 | 0.59% | 4,372 | | 0.00110 | -0.6239 | YES |
| A.3.2.5.2 | M&R-2 | Centrex/Non-Dispatch/GA(%) | Centrex | 0.36% | 351,811 | 0.30% | 4,372 | | 0.00092 | 0.7215 | YES |
| A.3.2.6.1 | M&R-2 | ISDN/Dispatch/GA(%) | ISDN | 0.20% | 258,558 | 0.53% | 2,471 | | 0.00091 | -3.5698 | NO |
| A.3.2.6.2 | M&R-2 | ISDN/Non-Dispatch/GA(%) | ISDN | 0.28% | 258,558 | 0.40% | 2,471 | | 0.00108 | -1.1305 | YES |
| | ***Maintenance Average Duration*** | | | | | | | | | | |
| A.3.3.1.1 | M&R-3 | Residence/Dispatch/GA(hours) | Res | 21.17 | 65,521 | 11.50 | 1,787 | 36.518 | 0.87557 | 11.0347 | YES |
| A.3.3.1.2 | M&R-3 | Residence/Non-Dispatch/GA(hours) | Res | 6.62 | 38,380 | 2.55 | 550 | 17.036 | 0.73160 | 5.5623 | YES |
| A.3.3.2.1 | M&R-3 | Business/Dispatch/GA(hours) | Bus | 11.55 | 9,840 | 12.95 | 265 | 21.707 | 1.35126 | -1.0415 | YES |
| A.3.3.2.2 | M&R-3 | Business/Non-Dispatch/GA(hours) | Bus | 3.84 | 6,574 | 2.63 | 148 | 10.111 | 0.84042 | 1.4459 | YES |
| A.3.3.3.1 | M&R-3 | Design (Specials)/Dispatch/GA(hours) | Design | 7.95 | 2,561 | 4.39 | 74 | 67.666 | 7.97879 | 0.4468 | YES |
| A.3.3.3.2 | M&R-3 | Design (Specials)/Non-Dispatch/GA(hours) | Design | 2.58 | 4,221 | 2.38 | 85 | 7.052 | 0.77257 | 0.2629 | YES |
| A.3.3.4.1 | M&R-3 | PBX/Dispatch/GA(hours) | PBX | 18.98 | 245 | 16.53 | 20 | 32.908 | 7.65300 | 0.3197 | YES |
| A.3.3.4.2 | M&R-3 | PBX/Non-Dispatch/GA(hours) | PBX | 2.89 | 116 | 5.17 | 2 | 5.177 | 3.69183 | -0.6170 | YES |
| A.3.3.5.1 | M&R-3 | Centrex/Dispatch/GA(hours) | Centrex | 11.96 | 1,850 | 16.27 | 26 | 17.462 | 3.44846 | -1.2501 | YES |
| A.3.3.5.2 | M&R-3 | Centrex/Non-Dispatch/GA(hours) | Centrex | 3.83 | 1,279 | 2.48 | 13 | 8.146 | 2.27083 | 0.5926 | YES |
| A.3.3.6.1 | M&R-3 | ISDN/Dispatch/GA(hours) | ISDN | 14.59 | 522 | 17.71 | 13 | 19.516 | 5.47981 | -0.5689 | YES |
| A.3.3.6.2 | M&R-3 | ISDN/Non-Dispatch/GA(hours) | ISDN | 3.81 | 732 | 12.25 | 10 | 13.524 | 4.30575 | -1.9606 | NO |
| | ***% Repeat Troubles within 30 Days*** | | | | | | | | | | |
| A.3.4.1.1 | M&R-4 | Residence/Dispatch/GA(%) | Res | 21.66% | 65,521 | 15.72% | 1,787 | | 0.00988 | 6.0084 | YES |
| A.3.4.1.2 | M&R-4 | Residence/Non-Dispatch/GA(%) | Res | 17.96% | 38,380 | 17.09% | 550 | | 0.01649 | 0.5287 | YES |
| A.3.4.2.1 | M&R-4 | Business/Dispatch/GA(%) | Bus | 17.68% | 9,840 | 12.08% | 265 | | 0.02375 | 2.3610 | YES |
| A.3.4.2.2 | M&R-4 | Business/Non-Dispatch/GA(%) | Bus | 17.33% | 6,574 | 24.32% | 148 | | 0.03146 | -2.2247 | NO |
| A.3.4.3.1 | M&R-4 | Design (Specials)/Dispatch/GA(%) | Design | 36.16% | 1,098 | 40.54% | 74 | | 0.05770 | -0.7597 | YES |
| A.3.4.3.2 | M&R-4 | Design (Specials)/Non-Dispatch/GA(%) | Design | 35.32% | 2,126 | 45.88% | 85 | | 0.05287 | -1.9969 | NO |
| A.3.4.4.1 | M&R-4 | PBX/Dispatch/GA(%) | PBX | 25.31% | 245 | 15.00% | 20 | | 0.10111 | 1.0193 | YES |
| A.3.4.4.2 | M&R-4 | PBX/Non-Dispatch/GA(%) | PBX | 20.69% | 116 | 0.00% | 2 | | 0.28889 | 0.7162 | YES |
| A.3.4.5.1 | M&R-4 | Centrex/Dispatch/GA(%) | Centrex | 15.68% | 1,850 | 7.69% | 26 | | 0.07180 | 1.1119 | YES |
| A.3.4.5.2 | M&R-4 | Centrex/Non-Dispatch/GA(%) | Centrex | 16.89% | 1,279 | 23.08% | 13 | | 0.10444 | -0.5926 | YES |
| A.3.4.6.1 | M&R-4 | ISDN/Dispatch/GA(%) | ISDN | 32.57% | 522 | 61.54% | 13 | | 0.13158 | -2.2018 | NO |
| A.3.4.6.2 | M&R-4 | ISDN/Non-Dispatch/GA(%) | ISDN | 25.96% | 732 | 60.00% | 10 | | 0.13958 | -2.4391 | NO |
| | ***Out of Service > 24 hours*** | | | | | | | | | | |
| A.3.5.1.1 | M&R-5 | Residence/Dispatch/GA(%) | Res | 18.80% | 42,299 | 6.61% | 1,407 | | 0.01059 | 11.5162 | YES |
| A.3.5.1.2 | M&R-5 | Residence/Non-Dispatch/GA(%) | Res | 6.71% | 10,039 | 1.75% | 228 | | 0.01676 | 2.9589 | YES |
| A.3.5.2.1 | M&R-5 | Business/Dispatch/GA(%) | Bus | 8.32% | 6,190 | 4.12% | 194 | | 0.02014 | 2.0838 | YES |
| A.3.5.2.2 | M&R-5 | Business/Non-Dispatch/GA(%) | Bus | 2.92% | 2,499 | 0.00% | 66 | | 0.02100 | 1.3910 | YES |
| A.3.5.3.1 | M&R-5 | Design (Specials)/Dispatch/GA(%) | Design | 1.37% | 1,098 | 0.00% | 74 | | 0.01394 | 0.9799 | YES |
| A.3.5.3.2 | M&R-5 | Design (Specials)/Non-Dispatch/GA(%) | Design | 0.42% | 2,126 | 0.00% | 85 | | 0.00718 | 0.5895 | YES |
| A.3.5.4.1 | M&R-5 | PBX/Dispatch/GA(%) | PBX | 16.29% | 178 | 12.50% | 8 | | 0.13347 | 0.2841 | YES |
| A.3.5.4.2 | M&R-5 | PBX/Non-Dispatch/GA(%) | PBX | 1.25% | 80 | 0.00% | 1 | | 0.11179 | 0.1118 | YES |
| A.3.5.5.1 | M&R-5 | Centrex/Dispatch/GA(%) | Centrex | 9.30% | 1,204 | 15.79% | 19 | | 0.06716 | -0.9659 | YES |
| A.3.5.5.2 | M&R-5 | Centrex/Non-Dispatch/GA(%) | Centrex | 1.72% | 641 | 0.00% | 11 | | 0.03949 | 0.4345 | YES |
| A.3.5.6.1 | M&R-5 | ISDN/Dispatch/GA(%) | ISDN | 14.79% | 453 | 10.00% | 10 | | 0.11349 | 0.4221 | YES |

## BellSouth Monthly State Summary

*Georgia, October 2001*

| | | | Benchmark / Analog | BST Measure | BST Volume | CLEC Measure | CLEC Volume | Standard Deviation | Standard Error | ZScore | Equity |
|---|---|---|---|---|---|---|---|---|---|---|---|
| A.3.5.6.2 | M&R-5 | ISDN/Non-Dispatch/GA(%) | ISDN | 1.09% | 641 | 22.22% | 9 | | 0.03489 | -6.0570 | NO |
| | **Resale - Billing** | | | | | | | | | | |
| | ***Invoice Accuracy*** | | | | | | | | | | |
| A.4.1 | B-1 | GA(%) | BST - State | 98.50% | $376,413,875 | 99.86% | $5,580,791 | | 0.00005 | -263.2808 | YES |
| | ***Mean Time to Deliver Invoices - CRIS*** | | | | | | | | | | |
| A.4.2 | B-2 | Region(business days) | BST - Region | 3.91 | 1 | 3.36 | 1,820 | | | | YES |

# BellSouth Monthly State Summary
## *Georgia, October 2001*

| | | | Benchmark / Analog | BST Measure | BST Volume | CLEC Measure | CLEC Volume | Standard Deviation | Standard Error | ZScore | Equity |
|---|---|---|---|---|---|---|---|---|---|---|---|
| | **Unbundled Network Elements - Ordering** | | | | | | | | | | |
| | ***% Rejected Service Requests - Mechanized*** | | | | | | | | | | |
| B.1.1.1 | O-7 | Switch Ports/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.1.1.2 | O-7 | Local Interoffice Transport/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.1.1.3 | O-7 | Loop + Port Combinations/GA(%) | Diagnostic | | | 17.78% | 43,213 | | | | Diagnostic |
| B.1.1.4 | O-7 | Combo Other/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.1.1.5 | O-7 | xDSL (ADSL, HDSL and UCL)/GA(%) | Diagnostic | | | 17.47% | 166 | | | | Diagnostic |
| B.1.1.6 | O-7 | ISDN Loop (UDN, UDC)/GA(%) | Diagnostic | | | 6.25% | 16 | | | | Diagnostic |
| B.1.1.7 | O-7 | Line Sharing/GA(%) | Diagnostic | | | 40.91% | 44 | | | | Diagnostic |
| B.1.1.8 | O-7 | 2W Analog Loop Design/GA(%) | Diagnostic | | | 33.87% | 310 | | | | Diagnostic |
| B.1.1.9 | O-7 | 2W Analog Loop Non-Design/GA(%) | Diagnostic | | | 20.10% | 199 | | | | Diagnostic |
| B.1.1.10 | O-7 | 2W Analog Loop w/INP Design/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.1.1.11 | O-7 | 2W Analog Loop w/INP Non-Design/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.1.1.12 | O-13 | 2W Analog Loop w/LNP Design/GA(%) | Diagnostic | | | 31.03% | 29 | | | | Diagnostic |
| B.1.1.13 | O-13 | 2W Analog Loop w/LNP Non-Design/GA(%) | Diagnostic | | | 47.22% | 36 | | | | Diagnostic |
| B.1.1.14 | O-7 | Other Design/GA(%) | Diagnostic | | | 33.00% | 100 | | | | Diagnostic |
| B.1.1.15 | O-7 | Other Non-Design/GA(%) | Diagnostic | | | 21.36% | 2,477 | | | | Diagnostic |
| B.1.1.16 | O-7 | INP Standalone/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.1.1.17 | O-13 | LNP Standalone/GA(%) | Diagnostic | | | 9.52% | 2,217 | | | | Diagnostic |
| | ***% Rejected Service Requests - Partially Mechanized*** | | | | | | | | | | |
| B.1.2.1 | O-7 | Switch Ports/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.1.2.2 | O-7 | Local Interoffice Transport/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.1.2.3 | O-7 | Loop + Port Combinations/GA(%) | Diagnostic | | | 31.34% | 18,349 | | | | Diagnostic |
| B.1.2.4 | O-7 | Combo Other/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.1.2.5 | O-7 | xDSL (ADSL, HDSL and UCL)/GA(%) | Diagnostic | | | 14.29% | 14 | | | | Diagnostic |
| B.1.2.6 | O-7 | ISDN Loop (UDN, UDC)/GA(%) | Diagnostic | | | 50.00% | 4 | | | | Diagnostic |
| B.1.2.7 | O-7 | Line Sharing/GA(%) | Diagnostic | | | 38.89% | 18 | | | | Diagnostic |
| B.1.2.8 | O-7 | 2W Analog Loop Design/GA(%) | Diagnostic | | | 25.31% | 320 | | | | Diagnostic |
| B.1.2.9 | O-7 | 2W Analog Loop Non-Design/GA(%) | Diagnostic | | | 29.41% | 102 | | | | Diagnostic |
| B.1.2.10 | O-7 | 2W Analog Loop w/INP Design/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.1.2.11 | O-7 | 2W Analog Loop w/INP Non-Design/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.1.2.12 | O-13 | 2W Analog Loop w/LNP Design/GA(%) | Diagnostic | | | 40.66% | 241 | | | | Diagnostic |
| B.1.2.13 | O-13 | 2W Analog Loop w/LNP Non-Design/GA(%) | Diagnostic | | | 12.73% | 1,053 | | | | Diagnostic |
| B.1.2.14 | O-7 | Other Design/GA(%) | Diagnostic | | | 42.11% | 57 | | | | Diagnostic |
| B.1.2.15 | O-7 | Other Non-Design/GA(%) | Diagnostic | | | 41.64% | 718 | | | | Diagnostic |
| B.1.2.16 | O-7 | INP Standalone/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.1.2.17 | O-13 | LNP Standalone/GA(%) | Diagnostic | | | 31.67% | 1,241 | | | | Diagnostic |
| | ***% Rejected Service Requests - Non-Mechanized*** | | | | | | | | | | |
| B.1.3.1 | O-7 | Switch Ports/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.1.3.2 | O-7 | Local Interoffice Transport/GA(%) | Diagnostic | | | 57.69% | 26 | | | | Diagnostic |
| B.1.3.3 | O-7 | Loop + Port Combinations/GA(%) | Diagnostic | | | 49.70% | 827 | | | | Diagnostic |
| B.1.3.4 | O-7 | Combo Other/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.1.3.5 | O-7 | xDSL (ADSL, HDSL and UCL)/GA(%) | Diagnostic | | | 27.80% | 223 | | | | Diagnostic |
| B.1.3.6 | O-7 | ISDN Loop (UDN, UDC)/GA(%) | Diagnostic | | | 13.94% | 459 | | | | Diagnostic |
| B.1.3.7 | O-7 | Line Sharing/GA(%) | Diagnostic | | | 16.00% | 50 | | | | Diagnostic |
| B.1.3.8 | O-7 | 2W Analog Loop Design/GA(%) | Diagnostic | | | 46.37% | 179 | | | | Diagnostic |
| B.1.3.9 | O-7 | 2W Analog Loop Non-Design/GA(%) | Diagnostic | | | 37.27% | 1,245 | | | | Diagnostic |
| B.1.3.10 | O-7 | 2W Analog Loop w/INP Design/GA(%) | Diagnostic | | | 0.00% | 1 | | | | Diagnostic |
| B.1.3.11 | O-7 | 2W Analog Loop w/INP Non-Design/GA(%) | Diagnostic | | | 0.00% | 4 | | | | Diagnostic |
| B.1.3.12 | O-13 | 2W Analog Loop w/LNP Design/GA(%) | Diagnostic | | | 58.33% | 36 | | | | Diagnostic |
| B.1.3.13 | O-13 | 2W Analog Loop w/LNP Non-Design/GA(%) | Diagnostic | | | 26.98% | 215 | | | | Diagnostic |
| B.1.3.14 | O-7 | Other Design/GA(%) | Diagnostic | | | 34.82% | 336 | | | | Diagnostic |
| B.1.3.15 | O-7 | Other Non-Design/GA(%) | Diagnostic | | | 33.26% | 1,711 | | | | Diagnostic |
| B.1.3.16 | O-7 | INP Standalone/GA(%) | Diagnostic | | | 46.15% | 104 | | | | Diagnostic |
| B.1.3.17 | O-13 | LNP Standalone/GA(%) | Diagnostic | | | 24.61% | 1,337 | | | | Diagnostic |
| | ***Reject Interval - Mechanized*** | | | | | | | | | | |

# BellSouth Monthly State Summary

## *Georgia, October 2001*

| | | | Benchmark / Analog | BST Measure | BST Volume | CLEC Measure | CLEC Volume | Standard Deviation | Standard Error | ZScore | Equity |
|---|---|---|---|---|---|---|---|---|---|---|---|
| B.1.4.1 | O-8 | Switch Ports/GA(%) | >= 97% w in 1 hr | | | | | | | | |
| B.1.4.2 | O-8 | Local Interoffice Transport/GA(%) | >= 97% w in 1 hr | | | | | | | | |
| B.1.4.3 | O-8 | Loop + Port Combinations/GA(%) | >= 97% w in 1 hr | | | 86.03% | 7,746 | | | | NO |
| B.1.4.4 | O-8 | Combo Other/GA(%) | >= 97% w in 1 hr | | | | | | | | |
| B.1.4.5 | O-8 | xDSL (ADSL, HDSL and UCL)/GA(%) | >= 97% w in 1 hr | | | 93.10% | 29 | | | | NO |
| B.1.4.6 | O-8 | ISDN Loop (UDN, UDC)/GA(%) | >= 97% w in 1 hr | | | 100.00% | 1 | | | | YES |
| B.1.4.7 | O-8 | Line Sharing/GA(%) | >= 97% w in 1 hr | | | 72.22% | 18 | | | | NO |
| B.1.4.8 | O-8 | 2W Analog Loop Design/GA(%) | >= 97% w in 1 hr | | | 73.58% | 106 | | | | NO |
| B.1.4.9 | O-8 | 2W Analog Loop Non-Design/GA(%) | >= 97% w in 1 hr | | | 80.49% | 41 | | | | NO |
| B.1.4.10 | O-8 | 2W Analog Loop w/INP Design/GA(%) | >= 97% w in 1 hr | | | | | | | | |
| B.1.4.11 | O-8 | 2W Analog Loop w/INP Non-Design/GA(%) | >= 97% w in 1 hr | | | | | | | | |
| B.1.4.12 | O-14 | 2W Analog Loop w/LNP Design/GA(%) | >= 97% w in 1 hr | | | 88.89% | 9 | | | | NO |
| B.1.4.13 | O-14 | 2W Analog Loop w/LNP Non-Design/GA(%) | >= 97% w in 1 hr | | | 82.35% | 17 | | | | NO |
| B.1.4.14 | O-8 | Other Design/GA(%) | >= 97% w in 1 hr | | | 87.88% | 33 | | | | NO |
| B.1.4.15 | O-8 | Other Non-Design/GA(%) | >= 97% w in 1 hr | | | 64.77% | 545 | | | | NO |
| B.1.4.16 | O-8 | INP Standalone/GA(%) | >= 97% w in 1 hr | | | | | | | | |
| B.1.4.17 | O-14 | LNP Standalone/GA(%) | >= 97% w in 1 hr | | | 93.36% | 211 | | | | NO |
| | ***Reject Interval - Partially Mechanized - 10 hours*** | | | | | | | | | | |
| B.1.7.1 | O-8 | Switch Ports/GA(%) | >= 85% w in 10 hrs | | | | | | | | |
| B.1.7.2 | O-8 | Local Interoffice Transport/GA(%) | >= 85% w in 10 hrs | | | | | | | | |
| B.1.7.3 | O-8 | Loop + Port Combinations/GA(%) | >= 85% w in 10 hrs | | | 94.86% | 5,853 | | | | YES |
| B.1.7.4 | O-8 | Combo Other/GA(%) | >= 85% w in 10 hrs | | | | | | | | |
| B.1.7.5 | O-8 | xDSL (ADSL, HDSL and UCL)/GA(%) | >= 85% w in 10 hrs | | | 100.00% | 2 | | | | YES |
| B.1.7.6 | O-8 | ISDN Loop (UDN, UDC)/GA(%) | >= 85% w in 10 hrs | | | 100.00% | 2 | | | | YES |
| B.1.7.7 | O-8 | Line Sharing/GA(%) | >= 85% w in 10 hrs | | | 100.00% | 7 | | | | YES |
| B.1.7.8 | O-8 | 2W Analog Loop Design/GA(%) | >= 85% w in 10 hrs | | | 88.76% | 89 | | | | YES |
| B.1.7.9 | O-8 | 2W Analog Loop Non-Design/GA(%) | >= 85% w in 10 hrs | | | 80.00% | 30 | | | | NO |
| B.1.7.10 | O-8 | 2W Analog Loop w/INP Design/GA(%) | >= 85% w in 10 hrs | | | | | | | | |
| B.1.7.11 | O-8 | 2W Analog Loop w/INP Non-Design/GA(%) | >= 85% w in 10 hrs | | | | | | | | |
| B.1.7.12 | O-14 | 2W Analog Loop w/LNP Design/GA(%) | >= 85% w in 10 hrs | | | 80.81% | 99 | | | | NO |
| B.1.7.13 | O-14 | 2W Analog Loop w/LNP Non-Design/GA(%) | >= 85% w in 10 hrs | | | 96.30% | 135 | | | | YES |
| B.1.7.14 | O-8 | Other Design/GA(%) | >= 85% w in 10 hrs | | | 91.67% | 24 | | | | YES |
| B.1.7.15 | O-8 | Other Non-Design/GA(%) | >= 85% w in 10 hrs | | | 94.37% | 302 | | | | YES |
| B.1.7.16 | O-8 | INP Standalone/GA(%) | >= 85% w in 10 hrs | | | | | | | | |
| B.1.7.17 | O-14 | LNP Standalone/GA(%) | >= 85% w in 10 hrs | | | 90.63% | 395 | | | | YES |
| | ***Reject Interval - Non-Mechanized*** | | | | | | | | | | |
| B.1.8.1 | O-8 | Switch Ports/GA(%) | >= 85% w in 24 hrs | | | | | | | | |
| B.1.8.2 | O-8 | Local Interoffice Transport/GA(%) | >= 85% w in 24 hrs | | | 93.33% | 15 | | | | YES |
| B.1.8.3 | O-8 | Loop + Port Combinations/GA(%) | >= 85% w in 24 hrs | | | 98.32% | 416 | | | | YES |
| B.1.8.4 | O-8 | Combo Other/GA(%) | >= 85% w in 24 hrs | | | | | | | | |
| B.1.8.5 | O-8 | xDSL (ADSL, HDSL and UCL)/GA(%) | >= 85% w in 24 hrs | | | 100.00% | 63 | | | | YES |
| B.1.8.6 | O-8 | ISDN Loop (UDN, UDC)/GA(%) | >= 85% w in 24 hrs | | | 98.46% | 65 | | | | YES |
| B.1.8.7 | O-8 | Line Sharing/GA(%) | >= 85% w in 24 hrs | | | 100.00% | 8 | | | | YES |
| B.1.8.8 | O-8 | 2W Analog Loop Design/GA(%) | >= 85% w in 24 hrs | | | 100.00% | 84 | | | | YES |
| B.1.8.9 | O-8 | 2W Analog Loop Non-Design/GA(%) | >= 85% w in 24 hrs | | | 99.58% | 476 | | | | YES |
| B.1.8.10 | O-8 | 2W Analog Loop w/INP Design/GA(%) | >= 85% w in 24 hrs | | | | | | | | |
| B.1.8.11 | O-8 | 2W Analog Loop w/INP Non-Design/GA(%) | >= 85% w in 24 hrs | | | | | | | | |
| B.1.8.12 | O-14 | 2W Analog Loop w/LNP Design/GA(%) | >= 85% w in 24 hrs | | | 100.00% | 21 | | | | YES |
| B.1.8.13 | O-14 | 2W Analog Loop w/LNP Non-Design/GA(%) | >= 85% w in 24 hrs | | | 100.00% | 66 | | | | YES |
| B.1.8.14 | O-8 | Other Design/GA(%) | >= 85% w in 24 hrs | | | 98.32% | 119 | | | | YES |
| B.1.8.15 | O-8 | Other Non-Design/GA(%) | >= 85% w in 24 hrs | | | 99.48% | 575 | | | | YES |
| B.1.8.16 | O-8 | INP Standalone/GA(%) | >= 85% w in 24 hrs | | | 100.00% | 48 | | | | YES |
| B.1.8.17 | O-14 | LNP Standalone/GA(%) | >= 85% w in 24 hrs | | | 99.70% | 333 | | | | YES |
| | ***FOC Timeliness - Mechanized*** | | | | | | | | | | |
| B.1.9.1 | O-9 | Switch Ports/GA(%) | >= 95% w in 3 hrs | | | | | | | | |
| B.1.9.2 | O-9 | Local Interoffice Transport/GA(%) | >= 95% w in 3 hrs | | | | | | | | |
| B.1.9.3 | O-9 | Loop + Port Combinations/GA(%) | >= 95% w in 3 hrs | | | 99.01% | 35,639 | | | | YES |
| B.1.9.4 | O-9 | Combo Other/GA(%) | >= 95% w in 3 hrs | | | | | | | | |
| B.1.9.5 | O-9 | xDSL (ADSL, HDSL and UCL)/GA(%) | >= 95% w in 3 hrs | | | 95.70% | 93 | | | | YES |

# BellSouth Monthly State Summary

## *Georgia, October 2001*

| | | | Benchmark / Analog | BST Measure | BST Volume | CLEC Measure | CLEC Volume | Standard Deviation | Standard Error | ZScore | Equity |
|---|---|---|---|---|---|---|---|---|---|---|---|
| B.1.9.6 | O-9 | ISDN Loop (UDN, UDC)/GA(%) | >= 95% w in 3 hrs | | | 100.00% | 15 | | | | YES |
| B.1.9.7 | O-9 | Line Sharing/GA(%) | >= 95% w in 3 hrs | | | 100.00% | 28 | | | | YES |
| B.1.9.8 | O-9 | 2W Analog Loop Design/GA(%) | >= 95% w in 3 hrs | | | 100.00% | 215 | | | | YES |
| B.1.9.9 | O-9 | 2W Analog Loop Non-Design/GA(%) | >= 95% w in 3 hrs | | | 99.38% | 160 | | | | YES |
| B.1.9.10 | O-9 | 2W Analog Loop w/INP Design/GA(%) | >= 95% w in 3 hrs | | | | | | | | |
| B.1.9.11 | O-9 | 2W Analog Loop w/INP Non-Design/GA(%) | >= 95% w in 3 hrs | | | | | | | | |
| B.1.9.12 | O-15 | 2W Analog Loop w/LNP Design/GA(%) | >= 95% w in 3 hrs | | | 95.00% | 20 | | | | YES |
| B.1.9.13 | O-15 | 2W Analog Loop w/LNP Non-Design/GA(%) | >= 95% w in 3 hrs | | | 100.00% | 19 | | | | YES |
| B.1.9.14 | O-9 | Other Design/GA(%) | >= 95% w in 3 hrs | | | 100.00% | 65 | | | | YES |
| B.1.9.15 | O-9 | Other Non-Design/GA(%) | >= 95% w in 3 hrs | | | 99.37% | 2,070 | | | | YES |
| B.1.9.16 | O-9 | INP Standalone/GA(%) | >= 95% w in 3 hrs | | | | | | | | |
| B.1.9.17 | O-15 | LNP Standalone/GA(%) | >= 95% w in 3 hrs | | | 96.35% | 2,002 | | | | YES |
| | ***FOC Timeliness - Partially Mechanized - 10 hours*** | | | | | | | | | | |
| B.1.12.1 | O-9 | Switch Ports/GA(%) | >= 85% w in 10 hrs | | | | | | | | |
| B.1.12.2 | O-9 | Local Interoffice Transport/GA(%) | >= 85% w in 10 hrs | | | | | | | | |
| B.1.12.3 | O-9 | Loop + Port Combinations/GA(%) | >= 85% w in 10 hrs | | | 96.71% | 12,757 | | | | YES |
| B.1.12.4 | O-9 | Combo Other/GA(%) | >= 85% w in 10 hrs | | | | | | | | |
| B.1.12.5 | O-9 | xDSL (ADSL, HDSL and UCL)/GA(%) | >= 85% w in 10 hrs | | | 66.67% | 6 | | | | NO |
| B.1.12.6 | O-9 | ISDN Loop (UDN, UDC)/GA(%) | >= 85% w in 10 hrs | | | 100.00% | 2 | | | | YES |
| B.1.12.7 | O-9 | Line Sharing/GA(%) | >= 85% w in 10 hrs | | | 100.00% | 8 | | | | YES |
| B.1.12.8 | O-9 | 2W Analog Loop Design/GA(%) | >= 85% w in 10 hrs | | | 88.24% | 255 | | | | YES |
| B.1.12.9 | O-9 | 2W Analog Loop Non-Design/GA(%) | >= 85% w in 10 hrs | | | 85.71% | 77 | | | | YES |
| B.1.12.10 | O-9 | 2W Analog Loop w/INP Design/GA(%) | >= 85% w in 10 hrs | | | | | | | | |
| B.1.12.11 | O-9 | 2W Analog Loop w/INP Non-Design/GA(%) | >= 85% w in 10 hrs | | | | | | | | |
| B.1.12.12 | O-15 | 2W Analog Loop w/LNP Design/GA(%) | >= 85% w in 10 hrs | | | 85.26% | 156 | | | | YES |
| B.1.12.13 | O-15 | 2W Analog Loop w/LNP Non-Design/GA(%) | >= 85% w in 10 hrs | | | 98.12% | 956 | | | | YES |
| B.1.12.14 | O-9 | Other Design/GA(%) | >= 85% w in 10 hrs | | | 94.44% | 36 | | | | YES |
| B.1.12.15 | O-9 | Other Non-Design/GA(%) | >= 85% w in 10 hrs | | | 95.76% | 425 | | | | YES |
| B.1.12.16 | O-9 | INP Standalone/GA(%) | >= 85% w in 10 hrs | | | | | | | | |
| B.1.12.17 | O-15 | LNP Standalone/GA(%) | >= 85% w in 10 hrs | | | 96.39% | 832 | | | | YES |
| | ***FOC Timeliness - Non-Mechanized*** | | | | | | | | | | |
| B.1.13.1 | O-9 | Switch Ports/GA(%) | >= 85% w in 36 hrs | | | | | | | | |
| B.1.13.2 | O-9 | Local Interoffice Transport/GA(%) | >= 85% w in 36 hrs | | | 100.00% | 16 | | | | YES |
| B.1.13.3 | O-9 | Loop + Port Combinations/GA(%) | >= 85% w in 36 hrs | | | 97.59% | 374 | | | | YES |
| B.1.13.4 | O-9 | Combo Other/GA(%) | >= 85% w in 36 hrs | | | | | | | | |
| B.1.13.5 | O-9 | xDSL (ADSL, HDSL and UCL)/GA(%) | >= 85% w in 36 hrs | | | 99.39% | 164 | | | | YES |
| B.1.13.6 | O-9 | ISDN Loop (UDN, UDC)/GA(%) | >= 85% w in 36 hrs | | | 100.00% | 380 | | | | YES |
| B.1.13.7 | O-9 | Line Sharing/GA(%) | >= 85% w in 36 hrs | | | 100.00% | 39 | | | | YES |
| B.1.13.8 | O-9 | 2W Analog Loop Design/GA(%) | >= 85% w in 36 hrs | | | 98.95% | 95 | | | | YES |
| B.1.13.9 | O-9 | 2W Analog Loop Non-Design/GA(%) | >= 85% w in 36 hrs | | | 99.33% | 741 | | | | YES |
| B.1.13.10 | O-9 | 2W Analog Loop w/INP Design/GA(%) | >= 85% w in 36 hrs | | | 100.00% | 1 | | | | YES |
| B.1.13.11 | O-9 | 2W Analog Loop w/INP Non-Design/GA(%) | >= 85% w in 36 hrs | | | 100.00% | 4 | | | | YES |
| B.1.13.12 | O-15 | 2W Analog Loop w/LNP Design/GA(%) | >= 85% w in 36 hrs | | | 100.00% | 13 | | | | YES |
| B.1.13.13 | O-15 | 2W Analog Loop w/LNP Non-Design/GA(%) | >= 85% w in 36 hrs | | | 97.22% | 180 | | | | YES |
| B.1.13.14 | O-9 | Other Design/GA(%) | >= 85% w in 36 hrs | | | 100.00% | 221 | | | | YES |
| B.1.13.15 | O-9 | Other Non-Design/GA(%) | >= 85% w in 36 hrs | | | 99.63% | 1,090 | | | | YES |
| B.1.13.16 | O-9 | INP Standalone/GA(%) | >= 85% w in 36 hrs | | | 100.00% | 56 | | | | YES |
| B.1.13.17 | O-15 | LNP Standalone/GA(%) | >= 85% w in 36 hrs | | | 99.60% | 1,010 | | | | YES |
| | ***FOC & Reject Response Completeness - Mechanized*** | | | | | | | | | | |
| B.1.14.1.1 | O-11 | Switch Ports/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.14.1.2 | O-11 | Switch Ports/TAG/GA(%) | >= 95% | | | | | | | | |
| B.1.14.2.1 | O-11 | Local Interoffice Transport/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.14.2.2 | O-11 | Local Interoffice Transport/TAG/GA(%) | >= 95% | | | | | | | | |
| B.1.14.3.1 | O-11 | Loop + Port Combinations/EDI/GA(%) | >= 95% | | | 99.95% | 27,921 | | | | YES |
| B.1.14.3.2 | O-11 | Loop + Port Combinations/TAG/GA(%) | >= 95% | | | 98.52% | 15,292 | | | | YES |
| B.1.14.4.1 | O-11 | Combo Other/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.14.4.2 | O-11 | Combo Other/TAG/GA(%) | >= 95% | | | | | | | | |
| B.1.14.5.1 | O-11 | xDSL (ADSL, HDSL and UCL)/EDI/GA(%) | >= 95% | | | 81.82% | 11 | | | | NO |
| B.1.14.5.2 | O-11 | xDSL (ADSL, HDSL and UCL)/TAG/GA(%) | >= 95% | | | 72.90% | 155 | | | | NO |

# BellSouth Monthly State Summary

## *Georgia, October 2001*

| | | | Benchmark / Analog | BST Measure | BST Volume | CLEC Measure | CLEC Volume | Standard Deviation | Standard Error | ZScore | Equity |
|---|---|---|---|---|---|---|---|---|---|---|---|
| B.1.14.6.1 | O-11 | ISDN Loop (UDN, UDC)/EDI/GA(%) | >= 95% | | | 100.00% | 5 | | | | YES |
| B.1.14.6.2 | O-11 | ISDN Loop (UDN, UDC)/TAG/GA(%) | >= 95% | | | 100.00% | 11 | | | | YES |
| B.1.14.7.1 | O-11 | Line Sharing/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.14.7.2 | O-11 | Line Sharing/TAG/GA(%) | >= 95% | | | 95.45% | 44 | | | | YES |
| B.1.14.8.1 | O-11 | 2W Analog Loop Design/EDI/GA(%) | >= 95% | | | 99.59% | 246 | | | | YES |
| B.1.14.8.2 | O-11 | 2W Analog Loop Design/TAG/GA(%) | >= 95% | | | 100.00% | 64 | | | | YES |
| B.1.14.9.1 | O-11 | 2W Analog Loop Non-Design/EDI/GA(%) | >= 95% | | | 100.00% | 1 | | | | YES |
| B.1.14.9.2 | O-11 | 2W Analog Loop Non-Design/TAG/GA(%) | >= 95% | | | 98.48% | 198 | | | | YES |
| B.1.14.10.1 | O-11 | 2W Analog Loop w/INP Design/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.14.10.2 | O-11 | 2W Analog Loop w/INP Design/TAG/GA(%) | >= 95% | | | | | | | | |
| B.1.14.11.1 | O-11 | 2W Analog Loop w/INP Non-Design/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.14.11.2 | O-11 | 2W Analog Loop w/INP Non-Design/TAG/GA(%) | >= 95% | | | | | | | | |
| B.1.14.12.1 | O-11 | 2W Analog Loop w/LNP Design/EDI/GA(%) | >= 95% | | | 100.00% | 26 | | | | YES |
| B.1.14.12.2 | O-11 | 2W Analog Loop w/LNP Design/TAG/GA(%) | >= 95% | | | 100.00% | 3 | | | | YES |
| B.1.14.13.1 | O-11 | 2W Analog Loop w/LNP Non-Design/EDI/GA(%) | >= 95% | | | 100.00% | 9 | | | | YES |
| B.1.14.13.2 | O-11 | 2W Analog Loop w/LNP Non-Design/TAG/GA(%) | >= 95% | | | 100.00% | 27 | | | | YES |
| B.1.14.14.1 | O-11 | Other Design/EDI/GA(%) | >= 95% | | | 100.00% | 15 | | | | YES |
| B.1.14.14.2 | O-11 | Other Design/TAG/GA(%) | >= 95% | | | 98.82% | 85 | | | | YES |
| B.1.14.15.1 | O-11 | Other Non-Design/EDI/GA(%) | >= 95% | | | 99.80% | 1,524 | | | | YES |
| B.1.14.15.2 | O-11 | Other Non-Design/TAG/GA(%) | >= 95% | | | 98.53% | 953 | | | | YES |
| B.1.14.16.1 | O-11 | INP Standalone/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.14.16.2 | O-11 | INP Standalone/TAG/GA(%) | >= 95% | | | | | | | | |
| B.1.14.17.1 | O-11 | LNP Standalone/EDI/GA(%) | >= 95% | | | 99.89% | 1,878 | | | | YES |
| B.1.14.17.2 | O-11 | LNP Standalone/TAG/GA(%) | >= 95% | | | 99.41% | 339 | | | | YES |
| | ***FOC & Reject Response Completeness - Partially Mechanized*** | | | | | | | | | | |
| B.1.15.1.1 | O-11 | Switch Ports/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.15.1.2 | O-11 | Switch Ports/TAG/GA(%) | >= 95% | | | | | | | | |
| B.1.15.2.1 | O-11 | Local Interoffice Transport/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.15.2.2 | O-11 | Local Interoffice Transport/TAG/GA(%) | >= 95% | | | | | | | | |
| B.1.15.3.1 | O-11 | Loop + Port Combinations/EDI/GA(%) | >= 95% | | | 99.97% | 11,761 | | | | YES |
| B.1.15.3.2 | O-11 | Loop + Port Combinations/TAG/GA(%) | >= 95% | | | 99.80% | 6,588 | | | | YES |
| B.1.15.4.1 | O-11 | Combo Other/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.15.4.2 | O-11 | Combo Other/TAG/GA(%) | >= 95% | | | | | | | | |
| B.1.15.5.1 | O-11 | xDSL (ADSL, HDSL and UCL)/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.15.5.2 | O-11 | xDSL (ADSL, HDSL and UCL)/TAG/GA(%) | >= 95% | | | 57.14% | 14 | | | | NO |
| B.1.15.6.1 | O-11 | ISDN Loop (UDN, UDC)/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.15.6.2 | O-11 | ISDN Loop (UDN, UDC)/TAG/GA(%) | >= 95% | | | 100.00% | 4 | | | | YES |
| B.1.15.7.1 | O-11 | Line Sharing/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.15.7.2 | O-11 | Line Sharing/TAG/GA(%) | >= 95% | | | 94.44% | 18 | | | | NO |
| B.1.15.8.1 | O-11 | 2W Analog Loop Design/EDI/GA(%) | >= 95% | | | 100.00% | 289 | | | | YES |
| B.1.15.8.2 | O-11 | 2W Analog Loop Design/TAG/GA(%) | >= 95% | | | 100.00% | 31 | | | | YES |
| B.1.15.9.1 | O-11 | 2W Analog Loop Non-Design/EDI/GA(%) | >= 95% | | | 100.00% | 2 | | | | YES |
| B.1.15.9.2 | O-11 | 2W Analog Loop Non-Design/TAG/GA(%) | >= 95% | | | 100.00% | 100 | | | | YES |
| B.1.15.10.1 | O-11 | 2W Analog Loop w/INP Design/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.15.10.2 | O-11 | 2W Analog Loop w/INP Design/TAG/GA(%) | >= 95% | | | | | | | | |
| B.1.15.11.1 | O-11 | 2W Analog Loop w/INP Non-Design/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.15.11.2 | O-11 | 2W Analog Loop w/INP Non-Design/TAG/GA(%) | >= 95% | | | | | | | | |
| B.1.15.12.1 | O-11 | 2W Analog Loop w/LNP Design/EDI/GA(%) | >= 95% | | | 99.11% | 224 | | | | YES |
| B.1.15.12.2 | O-11 | 2W Analog Loop w/LNP Design/TAG/GA(%) | >= 95% | | | 94.12% | 17 | | | | NO |
| B.1.15.13.1 | O-11 | 2W Analog Loop w/LNP Non-Design/EDI/GA(%) | >= 95% | | | 100.00% | 827 | | | | YES |
| B.1.15.13.2 | O-11 | 2W Analog Loop w/LNP Non-Design/TAG/GA(%) | >= 95% | | | 99.56% | 226 | | | | YES |
| B.1.15.14.1 | O-11 | Other Design/EDI/GA(%) | >= 95% | | | 100.00% | 23 | | | | YES |
| B.1.15.14.2 | O-11 | Other Design/TAG/GA(%) | >= 95% | | | 100.00% | 34 | | | | YES |
| B.1.15.15.1 | O-11 | Other Non-Design/EDI/GA(%) | >= 95% | | | 100.00% | 201 | | | | YES |
| B.1.15.15.2 | O-11 | Other Non-Design/TAG/GA(%) | >= 95% | | | 99.81% | 517 | | | | YES |
| B.1.15.16.1 | O-11 | INP Standalone/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.15.16.2 | O-11 | INP Standalone/TAG/GA(%) | >= 95% | | | | | | | | |
| B.1.15.17.1 | O-11 | LNP Standalone/EDI/GA(%) | >= 95% | | | 98.65% | 1,036 | | | | YES |
| B.1.15.17.2 | O-11 | LNP Standalone/TAG/GA(%) | >= 95% | | | 100.00% | 205 | | | | YES |

# BellSouth Monthly State Summary

## *Georgia, October 2001*

| | | | Benchmark / Analog | BST Measure | BST Volume | CLEC Measure | CLEC Volume | Standard Deviation | Standard Error | ZScore | Equity |
|---|---|---|---|---|---|---|---|---|---|---|---|
| | ***FOC & Reject Response Completeness - Non-Mechanized*** | | | | | | | | | | |
| B.1.16.1 | O-11 | Switch Ports/GA(%) | >= 95% | | | | | | | | |
| B.1.16.2 | O-11 | Local Interoffice Transport/GA(%) | >= 95% | | | 100.00% | 26 | | | | YES |
| B.1.16.3 | O-11 | Loop + Port Combinations/GA(%) | >= 95% | | | 91.78% | 827 | | | | NO |
| B.1.16.4 | O-11 | Combo Other/GA(%) | >= 95% | | | | | | | | |
| B.1.16.5 | O-11 | xDSL (ADSL, HDSL and UCL)/GA(%) | >= 95% | | | 98.21% | 223 | | | | YES |
| B.1.16.6 | O-11 | ISDN Loop (UDN, UDC)/GA(%) | >= 95% | | | 97.60% | 459 | | | | YES |
| B.1.16.7 | O-11 | Line Sharing/GA(%) | >= 95% | | | 94.00% | 50 | | | | NO |
| B.1.16.8 | O-11 | 2W Analog Loop Design/GA(%) | >= 95% | | | 96.09% | 179 | | | | YES |
| B.1.16.9 | O-11 | 2W Analog Loop Non-Design/GA(%) | >= 95% | | | 94.22% | 1,245 | | | | NO |
| B.1.16.10 | O-11 | 2W Analog Loop w/INP Design/GA(%) | >= 95% | | | 100.00% | 1 | | | | YES |
| B.1.16.11 | O-11 | 2W Analog Loop w/INP Non-Design/GA(%) | >= 95% | | | 100.00% | 4 | | | | YES |
| B.1.16.12 | O-11 | 2W Analog Loop w/LNP Design/GA(%) | >= 95% | | | 91.67% | 36 | | | | NO |
| B.1.16.13 | O-11 | 2W Analog Loop w/LNP Non-Design/GA(%) | >= 95% | | | 97.67% | 215 | | | | YES |
| B.1.16.14 | O-11 | Other Design/GA(%) | >= 95% | | | 96.43% | 336 | | | | YES |
| B.1.16.15 | O-11 | Other Non-Design/GA(%) | >= 95% | | | 96.90% | 1,711 | | | | YES |
| B.1.16.16 | O-11 | INP Standalone/GA(%) | >= 95% | | | 99.04% | 104 | | | | YES |
| B.1.16.17 | O-11 | LNP Standalone/GA(%) | >= 95% | | | 98.20% | 1,337 | | | | YES |
| | ***FOC & Reject Response Completeness (Multiple Responses) - Mechanized*** | | | | | | | | | | |
| B.1.17.1.1 | O-11 | Switch Ports/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.17.1.2 | O-11 | Switch Ports/TAG/GA(%) | >= 95% | | | | | | | | |
| B.1.17.2.1 | O-11 | Local Interoffice Transport/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.17.2.2 | O-11 | Local Interoffice Transport/TAG/GA(%) | >= 95% | | | | | | | | |
| B.1.17.3.1 | O-11 | Loop + Port Combinations/EDI/GA(%) | >= 95% | | | 70.45% | 27,907 | | | | NO |
| B.1.17.3.2 | O-11 | Loop + Port Combinations/TAG/GA(%) | >= 95% | | | 87.85% | 15,066 | | | | NO |
| B.1.17.4.1 | O-11 | Combo Other/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.17.4.2 | O-11 | Combo Other/TAG/GA(%) | >= 95% | | | | | | | | |
| B.1.17.5.1 | O-11 | xDSL (ADSL, HDSL and UCL)/EDI/GA(%) | >= 95% | | | 100.00% | 9 | | | | YES |
| B.1.17.5.2 | O-11 | xDSL (ADSL, HDSL and UCL)/TAG/GA(%) | >= 95% | | | 100.00% | 113 | | | | YES |
| B.1.17.6.1 | O-11 | ISDN Loop (UDN, UDC)/EDI/GA(%) | >= 95% | | | 100.00% | 5 | | | | YES |
| B.1.17.6.2 | O-11 | ISDN Loop (UDN, UDC)/TAG/GA(%) | >= 95% | | | 90.91% | 11 | | | | NO |
| B.1.17.7.1 | O-11 | Line Sharing/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.17.7.2 | O-11 | Line Sharing/TAG/GA(%) | >= 95% | | | 88.10% | 42 | | | | NO |
| B.1.17.8.1 | O-11 | 2W Analog Loop Design/EDI/GA(%) | >= 95% | | | 63.67% | 245 | | | | NO |
| B.1.17.8.2 | O-11 | 2W Analog Loop Design/TAG/GA(%) | >= 95% | | | 82.81% | 64 | | | | NO |
| B.1.17.9.1 | O-11 | 2W Analog Loop Non-Design/EDI/GA(%) | >= 95% | | | 0.00% | 1 | | | | NO |
| B.1.17.9.2 | O-11 | 2W Analog Loop Non-Design/TAG/GA(%) | >= 95% | | | 81.54% | 195 | | | | NO |
| B.1.17.10.1 | O-11 | 2W Analog Loop w/INP Design/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.17.10.2 | O-11 | 2W Analog Loop w/INP Design/TAG/GA(%) | >= 95% | | | | | | | | |
| B.1.17.11.1 | O-11 | 2W Analog Loop w/INP Non-Design/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.17.11.2 | O-11 | 2W Analog Loop w/INP Non-Design/TAG/GA(%) | >= 95% | | | | | | | | |
| B.1.17.12.1 | O-11 | 2W Analog Loop w/LNP Design/EDI/GA(%) | >= 95% | | | 100.00% | 26 | | | | YES |
| B.1.17.12.2 | O-11 | 2W Analog Loop w/LNP Design/TAG/GA(%) | >= 95% | | | 100.00% | 3 | | | | YES |
| B.1.17.13.1 | O-11 | 2W Analog Loop w/LNP Non-Design/EDI/GA(%) | >= 95% | | | 100.00% | 9 | | | | YES |
| B.1.17.13.2 | O-11 | 2W Analog Loop w/LNP Non-Design/TAG/GA(%) | >= 95% | | | 100.00% | 27 | | | | YES |
| B.1.17.14.1 | O-11 | Other Design/EDI/GA(%) | >= 95% | | | 66.67% | 15 | | | | NO |
| B.1.17.14.2 | O-11 | Other Design/TAG/GA(%) | >= 95% | | | 85.71% | 84 | | | | NO |
| B.1.17.15.1 | O-11 | Other Non-Design/EDI/GA(%) | >= 95% | | | 76.33% | 1,521 | | | | NO |
| B.1.17.15.2 | O-11 | Other Non-Design/TAG/GA(%) | >= 95% | | | 82.22% | 939 | | | | NO |
| B.1.17.16.1 | O-11 | INP Standalone/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.17.16.2 | O-11 | INP Standalone/TAG/GA(%) | >= 95% | | | | | | | | |
| B.1.17.17.1 | O-11 | LNP Standalone/EDI/GA(%) | >= 95% | | | 100.00% | 1,876 | | | | YES |
| B.1.17.17.2 | O-11 | LNP Standalone/TAG/GA(%) | >= 95% | | | 100.00% | 337 | | | | YES |
| | ***FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized*** | | | | | | | | | | |
| B.1.18.1.1 | O-11 | Switch Ports/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.18.1.2 | O-11 | Switch Ports/TAG/GA(%) | >= 95% | | | | | | | | |
| B.1.18.2.1 | O-11 | Local Interoffice Transport/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.18.2.2 | O-11 | Local Interoffice Transport/TAG/GA(%) | >= 95% | | | | | | | | |
| B.1.18.3.1 | O-11 | Loop + Port Combinations/EDI/GA(%) | >= 95% | | | 93.77% | 11,758 | | | | NO |

# BellSouth Monthly State Summary

## *Georgia, October 2001*

| | | | Benchmark / Analog | BST Measure | BST Volume | CLEC Measure | CLEC Volume | Standard Deviation | Standard Error | ZScore | Equity |
|---|---|---|---|---|---|---|---|---|---|---|---|
| B.1.18.3.2 | O-11 | Loop + Port Combinations/TAG/GA(%) | >= 95% | | | 92.52% | 6,575 | | | | NO |
| B.1.18.4.1 | O-11 | Combo Other/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.18.4.2 | O-11 | Combo Other/TAG/GA(%) | >= 95% | | | | | | | | |
| B.1.18.5.1 | O-11 | xDSL (ADSL, HDSL and UCL)/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.18.5.2 | O-11 | xDSL (ADSL, HDSL and UCL)/TAG/GA(%) | >= 95% | | | 100.00% | 8 | | | | YES |
| B.1.18.6.1 | O-11 | ISDN Loop (UDN, UDC)/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.18.6.2 | O-11 | ISDN Loop (UDN, UDC)/TAG/GA(%) | >= 95% | | | 100.00% | 4 | | | | YES |
| B.1.18.7.1 | O-11 | Line Sharing/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.18.7.2 | O-11 | Line Sharing/TAG/GA(%) | >= 95% | | | 64.71% | 17 | | | | NO |
| B.1.18.8.1 | O-11 | 2W Analog Loop Design/EDI/GA(%) | >= 95% | | | 96.19% | 289 | | | | YES |
| B.1.18.8.2 | O-11 | 2W Analog Loop Design/TAG/GA(%) | >= 95% | | | 96.77% | 31 | | | | YES |
| B.1.18.9.1 | O-11 | 2W Analog Loop Non-Design/EDI/GA(%) | >= 95% | | | 100.00% | 2 | | | | YES |
| B.1.18.9.2 | O-11 | 2W Analog Loop Non-Design/TAG/GA(%) | >= 95% | | | 94.00% | 100 | | | | NO |
| B.1.18.10.1 | O-11 | 2W Analog Loop w/INP Design/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.18.10.2 | O-11 | 2W Analog Loop w/INP Design/TAG/GA(%) | >= 95% | | | | | | | | |
| B.1.18.11.1 | O-11 | 2W Analog Loop w/INP Non-Design/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.18.11.2 | O-11 | 2W Analog Loop w/INP Non-Design/TAG/GA(%) | >= 95% | | | | | | | | |
| B.1.18.12.1 | O-11 | 2W Analog Loop w/LNP Design/EDI/GA(%) | >= 95% | | | 95.50% | 222 | | | | YES |
| B.1.18.12.2 | O-11 | 2W Analog Loop w/LNP Design/TAG/GA(%) | >= 95% | | | 87.50% | 16 | | | | NO |
| B.1.18.13.1 | O-11 | 2W Analog Loop w/LNP Non-Design/EDI/GA(%) | >= 95% | | | 98.55% | 827 | | | | YES |
| B.1.18.13.2 | O-11 | 2W Analog Loop w/LNP Non-Design/TAG/GA(%) | >= 95% | | | 95.11% | 225 | | | | YES |
| B.1.18.14.1 | O-11 | Other Design/EDI/GA(%) | >= 95% | | | 91.30% | 23 | | | | NO |
| B.1.18.14.2 | O-11 | Other Design/TAG/GA(%) | >= 95% | | | 91.18% | 34 | | | | NO |
| B.1.18.15.1 | O-11 | Other Non-Design/EDI/GA(%) | >= 95% | | | 95.52% | 201 | | | | YES |
| B.1.18.15.2 | O-11 | Other Non-Design/TAG/GA(%) | >= 95% | | | 97.87% | 516 | | | | YES |
| B.1.18.16.1 | O-11 | INP Standalone/EDI/GA(%) | >= 95% | | | | | | | | |
| B.1.18.16.2 | O-11 | INP Standalone/TAG/GA(%) | >= 95% | | | | | | | | |
| B.1.18.17.1 | O-11 | LNP Standalone/EDI/GA(%) | >= 95% | | | 98.34% | 1,022 | | | | YES |
| B.1.18.17.2 | O-11 | LNP Standalone/TAG/GA(%) | >= 95% | | | 99.51% | 205 | | | | YES |
| | | ***FOC & Reject Response Completeness (Multiple Responses) - Non-Mechanized*** | | | | | | | | | |
| B.1.19.1 | O-11 | Switch Ports/GA(%) | >= 95% | | | | | | | | |
| B.1.19.2 | O-11 | Local Interoffice Transport/GA(%) | >= 95% | | | 80.77% | 26 | | | | NO |
| B.1.19.3 | O-11 | Loop + Port Combinations/GA(%) | >= 95% | | | 92.09% | 759 | | | | NO |
| B.1.19.4 | O-11 | Combo Other/GA(%) | >= 95% | | | | | | | | |
| B.1.19.5 | O-11 | xDSL (ADSL, HDSL and UCL)/GA(%) | >= 95% | | | 93.15% | 219 | | | | NO |
| B.1.19.6 | O-11 | ISDN Loop (UDN, UDC)/GA(%) | >= 95% | | | 97.99% | 448 | | | | YES |
| B.1.19.7 | O-11 | Line Sharing/GA(%) | >= 95% | | | 95.74% | 47 | | | | YES |
| B.1.19.8 | O-11 | 2W Analog Loop Design/GA(%) | >= 95% | | | 95.93% | 172 | | | | YES |
| B.1.19.9 | O-11 | 2W Analog Loop Non-Design/GA(%) | >= 95% | | | 92.33% | 1,173 | | | | NO |
| B.1.19.10 | O-11 | 2W Analog Loop w/INP Design/GA(%) | >= 95% | | | 100.00% | 1 | | | | YES |
| B.1.19.11 | O-11 | 2W Analog Loop w/INP Non-Design/GA(%) | >= 95% | | | 75.00% | 4 | | | | NO |
| B.1.19.12 | O-11 | 2W Analog Loop w/LNP Design/GA(%) | >= 95% | | | 78.79% | 33 | | | | NO |
| B.1.19.13 | O-11 | 2W Analog Loop w/LNP Non-Design/GA(%) | >= 95% | | | 74.29% | 210 | | | | NO |
| B.1.19.14 | O-11 | Other Design/GA(%) | >= 95% | | | 91.98% | 324 | | | | NO |
| B.1.19.15 | O-11 | Other Non-Design/GA(%) | >= 95% | | | 97.47% | 1,658 | | | | YES |
| B.1.19.16 | O-11 | INP Standalone/GA(%) | >= 95% | | | 97.09% | 103 | | | | YES |
| B.1.19.17 | O-11 | LNP Standalone/GA(%) | >= 95% | | | 95.35% | 1,313 | | | | YES |
| | | **Unbundled Network Elements - Provisioning** | | | | | | | | | |
| | | ***Order Completion Interval*** | | | | | | | | | |
| B.2.1.1.1 | P-4 | Switch Ports/<10 circuits/Dispatch/GA(days) | R&B (POTS) | 4.45 | 56,951 | | | 6.331 | | | |
| B.2.1.1.2 | P-4 | Switch Ports/<10 circuits/Non-Dispatch/GA(days) | R&B (POTS) | 0.92 | 472,149 | | | 1.591 | | | |
| B.2.1.1.2.1 | P-4 | Switch Ports/>=10 circuits/Dispatch/GA(days) | R&B (POTS) | 10.30 | 164 | | | 13.226 | | | |
| B.2.1.1.2.2 | P-4 | Switch Ports/>=10 circuits/Non-Dispatch/GA(days) | R&B (POTS) | 2.90 | 14 | | | 4.514 | | | |
| B.2.1.2.1.1 | P-4 | Local Interoffice Transport/<10 circuits/Dispatch/GA(days) | DS1/DS3 | 17.30 | 2,187 | 14.00 | 1 | 16.724 | 16.72792 | 0.1972 | YES |
| B.2.1.2.1.2 | P-4 | Local Interoffice Transport/<10 circuits/Non-Dispatch/GA(days) | DS1/DS3 | | | | | | | | |
| B.2.1.2.2.1 | P-4 | Local Interoffice Transport/>=10 circuits/Dispatch/GA(days) | DS1/DS3 | | | | | | | | |
| B.2.1.2.2.2 | P-4 | Local Interoffice Transport/>=10 circuits/Non-Dispatch/GA(days) | DS1/DS3 | | | | | | | | |
| B.2.1.3.1.1 | P-4 | Loop + Port Combinations/<10 circuits/Dispatch/GA(days) | R&B | 4.53 | 58,516 | 5.01 | 710 | 6.420 | 0.24238 | -1.9894 | NO |

# BellSouth Monthly State Summary

*Georgia, October 2001*

| | | | Benchmark / Analog | BST Measure | BST Volume | CLEC Measure | CLEC Volume | Standard Deviation | Standard Error | ZScore | Equity |
|---|---|---|---|---|---|---|---|---|---|---|---|
| B.2.1.3.1.2 | P-4 | Loop + Port Combinations/<10 circuits/Non-Dispatch/GA(days) | R&B | 0.92 | 474,193 | 0.77 | 15,670 | 1.630 | 0.01323 | 11.0913 | YES |
| B.2.1.3.1.3 | P-4 | Loop + Port Combinations/<10 circuits/Switch Based Orders/GA(days) | R&B | 0.33 | 267,953 | 0.33 | 10,768 | 0.497 | 0.00488 | 0.6158 | YES |
| B.2.1.3.1.4 | P-4 | Loop + Port Combinations/<10 circuits/Dispatch In/GA(days) | R&B | 1.68 | 206,240 | 1.74 | 4,902 | 2.184 | 0.03156 | -1.9518 | NO |
| B.2.1.3.2.1 | P-4 | Loop + Port Combinations/>=10 circuits/Dispatch/GA(days) | R&B | 11.80 | 227 | 4.14 | 7 | 14.959 | 5.74033 | 1.3347 | YES |
| B.2.1.3.2.2 | P-4 | Loop + Port Combinations/>=10 circuits/Non-Dispatch/GA(days) | R&B | 1.99 | 172 | 3.00 | 1 | 2.995 | 3.00389 | -0.3351 | YES |
| B.2.1.3.2.3 | P-4 | Loop + Port Combinations/>=10 circuits/Switch Based Orders/GA(days) | R&B | 0.33 | 42 | | | 0.497 | | | |
| B.2.1.3.2.4 | P-4 | Loop + Port Combinations/>=10 circuits/Dispatch In/GA(days) | R&B | 2.53 | 130 | 3.00 | 1 | 3.271 | 3.28376 | -0.1429 | YES |
| B.2.1.4.1.1 | P-4 | Combo Other/<10 circuits/Dispatch/GA(days) | R&B&D - Disp | 5.46 | 60,760 | 11.79 | 197 | 10.174 | 0.72606 | -8.7247 | NO |
| B.2.1.4.1.4 | P-4 | Combo Other/<10 circuits/Dispatch In/GA(days) | R&B&D - Disp | 5.46 | 60,760 | | | | | | |
| B.2.1.4.2.1 | P-4 | Combo Other/>=10 circuits/Dispatch/GA(days) | R&B&D - Disp | 17.13 | 302 | | | 19.901 | | | |
| B.2.1.4.2.4 | P-4 | Combo Other/>=10 circuits/Dispatch In/GA(days) | R&B&D - Disp | 17.13 | 302 | | | | | | |
| B.2.1.5.3.1 | P-4 | xDSL (ADSL, HDSL and UCL)/<6 circuits/Dispatch/GA(days) | ADSL to Retail | 4.03 | 1,929 | 4.74 | 102 | 3.705 | 0.37644 | -1.8861 | NO |
| B.2.1.5.3.2 | P-4 | xDSL (ADSL, HDSL and UCL)/<6 circuits/Non-Dispatch/GA(days) | ADSL to Retail | 3.30 | 1,051 | | | 1.432 | | | |
| B.2.1.5.4.1 | P-4 | xDSL (ADSL, HDSL and UCL)/6-13 circuits/Dispatch/GA(days) | ADSL to Retail | 3.50 | 4 | | | 1.291 | | | |
| B.2.1.5.4.2 | P-4 | xDSL (ADSL, HDSL and UCL)/6-13 circuits/Non-Dispatch/GA(days) | ADSL to Retail | 10.00 | 1 | | | 0.000 | | | |
| B.2.1.5.5.1 | P-4 | xDSL (ADSL, HDSL and UCL)/>=14 circuits/Dispatch/GA(days) | ADSL to Retail | 4.00 | 1 | | | 0.000 | | | |
| B.2.1.5.5.2 | P-4 | xDSL (ADSL, HDSL and UCL)/>=14 circuits/Non-Dispatch/GA(days) | ADSL to Retail | | | | | | | | |
| B.2.1.6.3.1 | P-4 | UNE ISDN/<6 circuits/Dispatch/GA(days) | ISDN - BRI | 12.90 | 465 | 11.31 | 157 | 10.138 | 0.93577 | 1.6991 | YES |
| B.2.1.6.3.2 | P-4 | UNE ISDN/<6 circuits/Non-Dispatch/GA(days) | ISDN - BRI | 1.41 | 582 | | | 9.599 | | | |
| B.2.1.6.4.1 | P-4 | UNE ISDN/6-13 circuits/Dispatch/GA(days) | ISDN - BRI | | | | | | | | |
| B.2.1.6.4.2 | P-4 | UNE ISDN/6-13 circuits/Non-Dispatch/GA(days) | ISDN - BRI | 0.33 | 2 | | | 0.000 | | | |
| B.2.1.6.5.1 | P-4 | UNE ISDN/>=14 circuits/Dispatch/GA(days) | ISDN - BRI | | | | | | | | |
| B.2.1.6.5.2 | P-4 | UNE ISDN/>=14 circuits/Non-Dispatch/GA(days) | ISDN - BRI | | | | | | | | |
| B.2.1.7.3.1 | P-4 | Line Sharing/<6 circuits/Dispatch/GA(days) | ADSL to Retail | 4.03 | 1,929 | | | 3.705 | | | |
| B.2.1.7.3.2 | P-4 | Line Sharing/<6 circuits/Non-Dispatch/GA(days) | ADSL to Retail | 3.30 | 1,051 | 2.17 | 2 | 1.432 | 1.01340 | 1.1151 | YES |
| B.2.1.7.4.1 | P-4 | Line Sharing/6-13 circuits/Dispatch/GA(days) | ADSL to Retail | 3.50 | 4 | | | 1.291 | | | |
| B.2.1.7.4.2 | P-4 | Line Sharing/6-13 circuits/Non-Dispatch/GA(days) | ADSL to Retail | 10.00 | 1 | | | 0.000 | | | |
| B.2.1.7.5.1 | P-4 | Line Sharing/>=14 circuits/Dispatch/GA(days) | ADSL to Retail | 4.00 | 1 | | | 0.000 | | | |
| B.2.1.7.5.2 | P-4 | Line Sharing/>=14 circuits/Non-Dispatch/GA(days) | ADSL to Retail | | | | | | | | |
| B.2.1.8.1.1 | P-4 | 2W Analog Loop Design/<10 circuits/Dispatch/GA(days) | R&B - Disp | 4.53 | 58,516 | 5.45 | 20 | 6.420 | 1.43571 | -0.6436 | YES |
| B.2.1.8.1.2 | P-4 | 2W Analog Loop Design/<10 circuits/Non-Dispatch/GA(days) | R&B - Disp | 4.53 | 58,516 | | | 6.420 | | | |
| B.2.1.8.2.1 | P-4 | 2W Analog Loop Design/>=10 circuits/Dispatch/GA(days) | R&B - Disp | 11.80 | 227 | | | 14.959 | | | |
| B.2.1.8.2.2 | P-4 | 2W Analog Loop Design/>=10 circuits/Non-Dispatch/GA(days) | R&B - Disp | 11.80 | 227 | | | 14.959 | | | |
| B.2.1.9.1.1 | P-4 | 2W Analog Loop Non-Design/<10 circuits/Dispatch/GA(days) | R&B (POTS) excl SB Or | 4.45 | 56,951 | 4.53 | 30 | 6.331 | 1.15625 | -0.0695 | YES |
| B.2.1.9.1.4 | P-4 | 2W Analog Loop Non-Design/<10 circuits/Dispatch In/GA(days) | R&B (POTS) excl SB Or | 1.67 | 205,132 | | | 2.179 | | | |
| B.2.1.9.2.1 | P-4 | 2W Analog Loop Non-Design/>=10 circuits/Dispatch/GA(days) | R&B (POTS) excl SB Or | 10.30 | 164 | 6.00 | 1 | 13.226 | 13.26616 | 0.3242 | YES |
| B.2.1.9.2.4 | P-4 | 2W Analog Loop Non-Design/>=10 circuits/Dispatch In/GA(days) | R&B (POTS) excl SB Or | 6.33 | 6 | | | 5.317 | | | |
| B.2.1.10.1.1 | P-4 | 2W Analog Loop w/INP Design/<10 circuits/Dispatch/GA(days) | R&B - Disp | 4.53 | 58,516 | | | 6.420 | | | |
| B.2.1.10.1.2 | P-4 | 2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/GA(days) | R&B - Disp | 4.53 | 58,516 | | | 6.420 | | | |
| B.2.1.10.2.1 | P-4 | 2W Analog Loop w/INP Design/>=10 circuits/Dispatch/GA(days) | R&B - Disp | 11.80 | 227 | | | 14.959 | | | |
| B.2.1.10.2.2 | P-4 | 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/GA(days) | R&B - Disp | 11.80 | 227 | | | 14.959 | | | |
| B.2.1.11.1.1 | P-4 | 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/GA(days) | R&B (POTS) excl SB Or | 4.45 | 56,951 | | | 6.331 | | | |
| B.2.1.11.1.4 | P-4 | 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/GA(days) | R&B (POTS) excl SB Or | 1.67 | 205,132 | | | 2.179 | | | |
| B.2.1.11.2.1 | P-4 | 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/GA(days) | R&B (POTS) excl SB Or | 10.30 | 164 | | | 13.226 | | | |
| B.2.1.11.2.4 | P-4 | 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/GA(days) | R&B (POTS) excl SB Or | 6.33 | 6 | | | 5.317 | | | |
| B.2.1.12.1.1 | P-4 | 2W Analog Loop w/LNP Design/<10 circuits/Dispatch/GA(days) | R&B - Disp | 4.53 | 58,516 | 5.28 | 46 | 6.420 | 0.94689 | -0.7964 | YES |
| B.2.1.12.1.2 | P-4 | 2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/GA(days) | R&B - Disp | 4.53 | 58,516 | | | 6.420 | | | |
| B.2.1.12.2.1 | P-4 | 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/GA(days) | R&B - Disp | 11.80 | 227 | | | 14.959 | | | |
| B.2.1.12.2.2 | P-4 | 2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/GA(days) | R&B - Disp | 11.80 | 227 | | | 14.959 | | | |
| B.2.1.13.1.1 | P-4 | 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/GA(days) | R&B (POTS) excl SB Or | 4.45 | 56,951 | 5.49 | 102 | 6.331 | 0.62746 | -1.6528 | NO |
| B.2.1.13.1.4 | P-4 | 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/GA(days) | R&B (POTS) excl SB Or | 1.67 | 205,132 | | | 2.179 | | | |
| B.2.1.13.2.1 | P-4 | 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/GA(days) | R&B (POTS) excl SB Or | 10.30 | 164 | 6.67 | 3 | 13.226 | 7.70550 | 0.4712 | YES |
| B.2.1.13.2.4 | P-4 | 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/GA(days) | R&B (POTS) excl SB Or | 6.33 | 6 | | | 5.317 | | | |
| B.2.1.14.1.1 | P-4 | Other Design/<10 circuits/Dispatch/GA(days) | Design | 29.74 | 2,244 | 4.85 | 87 | 33.422 | 3.65197 | 6.8156 | YES |
| B.2.1.14.1.2 | P-4 | Other Design/<10 circuits/Non-Dispatch/GA(days) | Design | 12.38 | 520 | | | 26.292 | | | |
| B.2.1.14.2.1 | P-4 | Other Design/>=10 circuits/Dispatch/GA(days) | Design | 33.24 | 75 | 5.00 | 1 | 24.031 | 24.19058 | 1.1674 | YES |
| B.2.1.14.2.2 | P-4 | Other Design/>=10 circuits/Non-Dispatch/GA(days) | Design | 4.46 | 254 | | | 7.005 | | | |
| B.2.1.15.1.1 | P-4 | Other Non-Design/<10 circuits/Dispatch/GA(days) | R&B | 4.53 | 58,516 | 3.31 | 129 | 6.420 | 0.56584 | 2.1508 | YES |
| B.2.1.15.1.2 | P-4 | Other Non-Design/<10 circuits/Non-Dispatch/GA(days) | R&B | 0.92 | 474,193 | 1.81 | 7 | 1.630 | 0.61605 | 1.4441 | YES |
| B.2.1.15.2.1 | P-4 | Other Non-Design/>=10 circuits/Dispatch/GA(days) | R&B | 11.80 | 227 | 6.00 | 2 | 14.959 | 10.62382 | 0.5464 | YES |
| B.2.1.15.2.2 | P-4 | Other Non-Design/>=10 circuits/Non-Dispatch/GA(days) | R&B | 1.99 | 172 | 1.17 | 2 | 2.995 | 2.13020 | 0.3889 | YES |

# BellSouth Monthly State Summary

## *Georgia, October 2001*

| | | | Benchmark / Analog | BST Measure | BST Volume | CLEC Measure | CLEC Volume | Standard Deviation | Standard Error | ZScore | Equity |
|---|---|---|---|---|---|---|---|---|---|---|---|
| B.2.1.16.1.1 | P-4 | INP (Standalone)/<10 circuits/Dispatch/GA(days) | R&B (POTS) | 4.45 | 56,951 | | | 6.331 | | | |
| B.2.1.16.1.2 | P-4 | INP (Standalone)/<10 circuits/Non-Dispatch/GA(days) | R&B (POTS) | 0.92 | 472,149 | 1.53 | 5 | 1.591 | 0.71161 | -0.8653 | YES |
| B.2.1.16.2.1 | P-4 | INP (Standalone)/>=10 circuits/Dispatch/GA(days) | R&B (POTS) | 10.30 | 164 | | | 13.226 | | | |
| B.2.1.16.2.2 | P-4 | INP (Standalone)/>=10 circuits/Non-Dispatch/GA(days) | R&B (POTS) | 2.90 | 14 | | | 4.514 | | | |
| B.2.1.17.1.1 | P-4 | LNP (Standalone)/<10 circuits/Dispatch/GA(days) | R&B (POTS) | 4.45 | 56,951 | 3.17 | 2 | 6.331 | 4.47705 | 0.2866 | YES |
| B.2.1.17.1.2 | P-4 | LNP (Standalone)/<10 circuits/Non-Dispatch/GA(days) | R&B (POTS) | 0.92 | 472,149 | 0.70 | 3,005 | 1.591 | 0.02912 | 7.4273 | YES |
| B.2.1.17.2.1 | P-4 | LNP (Standalone)/>=10 circuits/Dispatch/GA(days) | R&B (POTS) | 10.30 | 164 | | | 13.226 | | | |
| B.2.1.17.2.2 | P-4 | LNP (Standalone)/>=10 circuits/Non-Dispatch/GA(days) | R&B (POTS) | 2.90 | 14 | 0.63 | 11 | 4.514 | 1.81878 | 1.2497 | YES |
| B.2.1.18.1.1 | P-4 | Digital Loop < DS1/<10 circuits/Dispatch/GA(days) | Digital Loop < DS1 | 9.08 | 2,808 | 8.96 | 243 | 60.775 | 4.06394 | 0.0295 | YES |
| B.2.1.18.1.2 | P-4 | Digital Loop < DS1/<10 circuits/Non-Dispatch/GA(days) | Digital Loop < DS1 | 4.04 | 1,955 | | | 9.474 | | | |
| B.2.1.18.2.1 | P-4 | Digital Loop < DS1/>=10 circuits/Dispatch/GA(days) | Digital Loop < DS1 | 3.60 | 5 | | | | | | |
| B.2.1.18.2.2 | P-4 | Digital Loop < DS1/>=10 circuits/Non-Dispatch/GA(days) | Digital Loop < DS1 | 5.00 | 1 | | | 0.000 | | | |
| B.2.1.19.1.1 | P-4 | Digital Loop >= DS1/<10 circuits/Dispatch/GA(days) | Digital Loop >= DS1 | 44.46 | 417 | 6.28 | 110 | 41.484 | 4.44655 | 8.5852 | YES |
| B.2.1.19.1.2 | P-4 | Digital Loop >= DS1/<10 circuits/Non-Dispatch/GA(days) | Digital Loop >= DS1 | 10.84 | 442 | | | 12.822 | | | |
| B.2.1.19.2.1 | P-4 | Digital Loop >= DS1/>=10 circuits/Dispatch/GA(days) | Digital Loop >= DS1 | 3.50 | 2 | | | | | | |
| B.2.1.19.2.2 | P-4 | Digital Loop >= DS1/>=10 circuits/Non-Dispatch/GA(days) | Digital Loop >= DS1 | 4.46 | 253 | | | | | | |
| | | ***Order Completion Interval within X days*** | | | | | | | | | |
| B.2.2.1 | P-4 | xDSL (ADSL, HDSL and UCL) Loop with Conditioning/<6 circuits/Dispatch/GA(days) | 14 days | | | 12.00 | 1 | | | | YES |
| B.2.2.2 | P-4 | xDSL (ADSL, HDSL and UCL) Loop w/o Conditioning/<6 circuits/Dispatch/GA(days) | 7 days | | | 4.66 | 101 | | | | YES |
| | | ***Held Orders*** | | | | | | | | | |
| B.2.3.1.1.1 | P-1 | Switch Ports/<10 circuits/Facility/GA(days) | R&B (POTS) | 10.28 | 226 | | | 18.288 | | | |
| B.2.3.1.1.2 | P-1 | Switch Ports/<10 circuits/Equipment/GA(days) | R&B (POTS) | 8.33 | 3 | | | 11.015 | | | |
| B.2.3.1.1.3 | P-1 | Switch Ports/<10 circuits/Other/GA(days) | R&B (POTS) | 8.54 | 13 | | | 16.014 | | | |
| B.2.3.1.2.1 | P-1 | Switch Ports/>=10 circuits/Facility/GA(days) | R&B (POTS) | 9.50 | 2 | | | 10.607 | | | |
| B.2.3.1.2.2 | P-1 | Switch Ports/>=10 circuits/Equipment/GA(days) | R&B (POTS) | 0.00 | 0 | | | | | | |
| B.2.3.1.2.3 | P-1 | Switch Ports/>=10 circuits/Other/GA(days) | R&B (POTS) | 0.00 | 0 | | | | | | |
| B.2.3.2.1.1 | P-1 | Local Interoffice Transport/<10 circuits/Facility/GA(days) | DS1/ DS3 - Interoffice | 22.00 | 1 | 0.00 | 0 | 0.000 | | | YES |
| B.2.3.2.1.2 | P-1 | Local Interoffice Transport/<10 circuits/Equipment/GA(days) | DS1/ DS3 - Interoffice | 0.00 | 0 | 0.00 | 0 | | | | YES |
| B.2.3.2.1.3 | P-1 | Local Interoffice Transport/<10 circuits/Other/GA(days) | DS1/ DS3 - Interoffice | 10.50 | 12 | 0.00 | 0 | 8.480 | | | YES |
| B.2.3.2.2.1 | P-1 | Local Interoffice Transport/>=10 circuits/Facility/GA(days) | DS1/ DS3 - Interoffice | | | | | | | | |
| B.2.3.2.2.2 | P-1 | Local Interoffice Transport/>=10 circuits/Equipment/GA(days) | DS1/ DS3 - Interoffice | | | | | | | | |
| B.2.3.2.2.3 | P-1 | Local Interoffice Transport/>=10 circuits/Other/GA(days) | DS1/ DS3 - Interoffice | | | | | | | | |
| B.2.3.3.1.1 | P-1 | Loop + Port Combinations/<10 circuits/Facility/GA(days) | R&B | 10.14 | 233 | 14.00 | 2 | 18.043 | 12.81283 | -0.3015 | YES |
| B.2.3.3.1.2 | P-1 | Loop + Port Combinations/<10 circuits/Equipment/GA(days) | R&B | 8.33 | 3 | 0.00 | 0 | 11.015 | | | YES |
| B.2.3.3.1.3 | P-1 | Loop + Port Combinations/<10 circuits/Other/GA(days) | R&B | 8.54 | 13 | 0.00 | 0 | 16.014 | | | YES |
| B.2.3.3.2.1 | P-1 | Loop + Port Combinations/>=10 circuits/Facility/GA(days) | R&B | 29.33 | 3 | 0.00 | 0 | 35.162 | | | YES |
| B.2.3.3.2.2 | P-1 | Loop + Port Combinations/>=10 circuits/Equipment/GA(days) | R&B | 0.00 | 0 | 0.00 | 0 | | | | YES |
| B.2.3.3.2.3 | P-1 | Loop + Port Combinations/>=10 circuits/Other/GA(days) | R&B | 0.00 | 0 | 0.00 | 0 | | | | YES |
| B.2.3.4.1.1 | P-1 | Combo Other/<10 circuits/Facility/GA(days) | R&B&D - Disp | 10.14 | 233 | 0.00 | 0 | 18.043 | | | YES |
| B.2.3.4.1.2 | P-1 | Combo Other/<10 circuits/Equipment/GA(days) | R&B&D - Disp | 8.33 | 3 | 0.00 | 0 | 11.015 | | | YES |
| B.2.3.4.1.3 | P-1 | Combo Other/<10 circuits/Other/GA(days) | R&B&D - Disp | 22.35 | 17 | 0.00 | 0 | 19.975 | | | YES |
| B.2.3.4.2.1 | P-1 | Combo Other/>=10 circuits/Facility/GA(days) | R&B&D - Disp | 29.33 | 3 | | | 35.162 | | | |
| B.2.3.4.2.2 | P-1 | Combo Other/>=10 circuits/Equipment/GA(days) | R&B&D - Disp | 12.00 | 2 | | | 2.828 | | | |
| B.2.3.4.2.3 | P-1 | Combo Other/>=10 circuits/Other/GA(days) | R&B&D - Disp | 14.00 | 1 | | | 0.000 | | | |
| B.2.3.5.1.1 | P-1 | xDSL (ADSL, HDSL and UCL)/<10 circuits/Facility/GA(days) | ADSL to Retail | 24.42 | 142 | 0.00 | 0 | 28.277 | | | YES |
| B.2.3.5.1.2 | P-1 | xDSL (ADSL, HDSL and UCL)/<10 circuits/Equipment/GA(days) | ADSL to Retail | 1.00 | 1 | 0.00 | 0 | 0.000 | | | YES |
| B.2.3.5.1.3 | P-1 | xDSL (ADSL, HDSL and UCL)/<10 circuits/Other/GA(days) | ADSL to Retail | 11.57 | 7 | 0.00 | 0 | 5.940 | | | YES |
| B.2.3.5.2.1 | P-1 | xDSL (ADSL, HDSL and UCL)/>=10 circuits/Facility/GA(days) | ADSL to Retail | 0.00 | 0 | | | | | | |
| B.2.3.5.2.2 | P-1 | xDSL (ADSL, HDSL and UCL)/>=10 circuits/Equipment/GA(days) | ADSL to Retail | 0.00 | 0 | | | | | | |
| B.2.3.5.2.3 | P-1 | xDSL (ADSL, HDSL and UCL)/>=10 circuits/Other/GA(days) | ADSL to Retail | 0.00 | 0 | | | | | | |
| B.2.3.6.1.1 | P-1 | UNE ISDN/<10 circuits/Facility/GA(days) | ISDN - BRI | 3.00 | 1 | 0.00 | 0 | 0.000 | | | YES |
| B.2.3.6.1.2 | P-1 | UNE ISDN/<10 circuits/Equipment/GA(days) | ISDN - BRI | 0.00 | 0 | 0.00 | 0 | | | | YES |
| B.2.3.6.1.3 | P-1 | UNE ISDN/<10 circuits/Other/GA(days) | ISDN - BRI | 140.00 | 1 | 0.00 | 0 | 0.000 | | | YES |
| B.2.3.6.2.1 | P-1 | UNE ISDN/>=10 circuits/Facility/GA(days) | ISDN - BRI | | | | | | | | |
| B.2.3.6.2.2 | P-1 | UNE ISDN/>=10 circuits/Equipment/GA(days) | ISDN - BRI | | | | | | | | |
| B.2.3.6.2.3 | P-1 | UNE ISDN/>=10 circuits/Other/GA(days) | ISDN - BRI | | | | | | | | |
| B.2.3.7.1.1 | P-1 | Line Sharing/<10 circuits/Facility/GA(days) | ADSL to Retail | 24.42 | 142 | 0.00 | 0 | 28.277 | | | YES |
| B.2.3.7.1.2 | P-1 | Line Sharing/<10 circuits/Equipment/GA(days) | ADSL to Retail | 1.00 | 1 | 0.00 | 0 | 0.000 | | | YES |
| B.2.3.7.1.3 | P-1 | Line Sharing/<10 circuits/Other/GA(days) | ADSL to Retail | 11.57 | 7 | 0.00 | 0 | 5.940 | | | YES |

# BellSouth Monthly State Summary

*Georgia, October 2001*

| | | | Benchmark / Analog | BST Measure | BST Volume | CLEC Measure | CLEC Volume | Standard Deviation | Standard Error | ZScore | Equity |
|---|---|---|---|---|---|---|---|---|---|---|---|
| B.2.3.7.2.1 | P-1 | Line Sharing/>=10 circuits/Facility/GA(days) | ADSL to Retail | 0.00 | 0 | | | | | | |
| B.2.3.7.2.2 | P-1 | Line Sharing/>=10 circuits/Equipment/GA(days) | ADSL to Retail | 0.00 | 0 | | | | | | |
| B.2.3.7.2.3 | P-1 | Line Sharing/>=10 circuits/Other/GA(days) | ADSL to Retail | 0.00 | 0 | | | | | | |
| B.2.3.8.1.1 | P-1 | 2W Analog Loop Design/<10 circuits/Facility/GA(days) | R&B - Disp | 10.14 | 233 | 0.00 | 0 | 18.043 | | | YES |
| B.2.3.8.1.2 | P-1 | 2W Analog Loop Design/<10 circuits/Equipment/GA(days) | R&B - Disp | 8.33 | 3 | 0.00 | 0 | 11.015 | | | YES |
| B.2.3.8.1.3 | P-1 | 2W Analog Loop Design/<10 circuits/Other/GA(days) | R&B - Disp | 8.54 | 13 | 0.00 | 0 | 16.014 | | | YES |
| B.2.3.8.2.1 | P-1 | 2W Analog Loop Design/>=10 circuits/Facility/GA(days) | R&B - Disp | 29.33 | 3 | 0.00 | 0 | 35.162 | | | YES |
| B.2.3.8.2.2 | P-1 | 2W Analog Loop Design/>=10 circuits/Equipment/GA(days) | R&B - Disp | 0.00 | 0 | 0.00 | 0 | | | | YES |
| B.2.3.8.2.3 | P-1 | 2W Analog Loop Design/>=10 circuits/Other/GA(days) | R&B - Disp | 0.00 | 0 | 0.00 | 0 | | | | YES |
| B.2.3.9.1.1 | P-1 | 2W Analog Loop Non-Design/<10 circuits/Facility/GA(days) | R&B (POTS) excl SB Or | 10.28 | 226 | 0.00 | 0 | 18.288 | | | YES |
| B.2.3.9.1.2 | P-1 | 2W Analog Loop Non-Design/<10 circuits/Equipment/GA(days) | R&B (POTS) excl SB Or | 8.33 | 3 | 0.00 | 0 | 11.015 | | | YES |
| B.2.3.9.1.3 | P-1 | 2W Analog Loop Non-Design/<10 circuits/Other/GA(days) | R&B (POTS) excl SB Or | 8.54 | 13 | 0.00 | 0 | 16.014 | | | YES |
| B.2.3.9.2.1 | P-1 | 2W Analog Loop Non-Design/>=10 circuits/Facility/GA(days) | R&B (POTS) excl SB Or | 9.50 | 2 | 0.00 | 0 | 10.607 | | | YES |
| B.2.3.9.2.2 | P-1 | 2W Analog Loop Non-Design/>=10 circuits/Equipment/GA(days) | R&B (POTS) excl SB Or | 0.00 | 0 | 0.00 | 0 | | | | YES |
| B.2.3.9.2.3 | P-1 | 2W Analog Loop Non-Design/>=10 circuits/Other/GA(days) | R&B (POTS) excl SB Or | 0.00 | 0 | 0.00 | 0 | | | | YES |
| B.2.3.10.1.1 | P-1 | 2W Analog Loop w/INP Design/<10 circuits/Facility/GA(days) | R&B - Disp | 10.14 | 233 | | | 18.043 | | | |
| B.2.3.10.1.2 | P-1 | 2W Analog Loop w/INP Design/<10 circuits/Equipment/GA(days) | R&B - Disp | 8.33 | 3 | | | 11.015 | | | |
| B.2.3.10.1.3 | P-1 | 2W Analog Loop w/INP Design/<10 circuits/Other/GA(days) | R&B - Disp | 8.54 | 13 | | | 16.014 | | | |
| B.2.3.10.2.1 | P-1 | 2W Analog Loop w/INP Design/>=10 circuits/Facility/GA(days) | R&B - Disp | 29.33 | 3 | | | 35.162 | | | |
| B.2.3.10.2.2 | P-1 | 2W Analog Loop w/INP Design/>=10 circuits/Equipment/GA(days) | R&B - Disp | 0.00 | 0 | | | | | | |
| B.2.3.10.2.3 | P-1 | 2W Analog Loop w/INP Design/>=10 circuits/Other/GA(days) | R&B - Disp | 0.00 | 0 | | | | | | |
| B.2.3.11.1.1 | P-1 | 2W Analog Loop w/INP Non-Design/<10 circuits/Facility/GA(days) | R&B (POTS) excl SB Or | 10.28 | 226 | | | 18.288 | | | |
| B.2.3.11.1.2 | P-1 | 2W Analog Loop w/INP Non-Design/<10 circuits/Equipment/GA(days) | R&B (POTS) excl SB Or | 8.33 | 3 | | | 11.015 | | | |
| B.2.3.11.1.3 | P-1 | 2W Analog Loop w/INP Non-Design/<10 circuits/Other/GA(days) | R&B (POTS) excl SB Or | 8.54 | 13 | | | 16.014 | | | |
| B.2.3.11.2.1 | P-1 | 2W Analog Loop w/INP Non-Design/>=10 circuits/Facility/GA(days) | R&B (POTS) excl SB Or | 9.50 | 2 | | | 10.607 | | | |
| B.2.3.11.2.2 | P-1 | 2W Analog Loop w/INP Non-Design/>=10 circuits/Equipment/GA(days) | R&B (POTS) excl SB Or | 0.00 | 0 | | | | | | |
| B.2.3.11.2.3 | P-1 | 2W Analog Loop w/INP Non-Design/>=10 circuits/Other/GA(days) | R&B (POTS) excl SB Or | 0.00 | 0 | | | | | | |
| B.2.3.12.1.1 | P-1 | 2W Analog Loop w/LNP Design/<10 circuits/Facility/GA(days) | R&B - Disp | 10.14 | 233 | 0.00 | 0 | 18.043 | | | YES |
| B.2.3.12.1.2 | P-1 | 2W Analog Loop w/LNP Design/<10 circuits/Equipment/GA(days) | R&B - Disp | 8.33 | 3 | 0.00 | 0 | 11.015 | | | YES |
| B.2.3.12.1.3 | P-1 | 2W Analog Loop w/LNP Design/<10 circuits/Other/GA(days) | R&B - Disp | 8.54 | 13 | 0.00 | 0 | 16.014 | | | YES |
| B.2.3.12.2.1 | P-1 | 2W Analog Loop w/LNP Design/>=10 circuits/Facility/GA(days) | R&B - Disp | 29.33 | 3 | 0.00 | 0 | 35.162 | | | YES |
| B.2.3.12.2.2 | P-1 | 2W Analog Loop w/LNP Design/>=10 circuits/Equipment/GA(days) | R&B - Disp | 0.00 | 0 | 0.00 | 0 | | | | YES |
| B.2.3.12.2.3 | P-1 | 2W Analog Loop w/LNP Design/>=10 circuits/Other/GA(days) | R&B - Disp | 0.00 | 0 | 0.00 | 0 | | | | YES |
| B.2.3.13.1.1 | P-1 | 2W Analog Loop w/LNP Non-Design/<10 circuits/Facility/GA(days) | R&B (POTS) excl SB Or | 10.28 | 226 | 15.00 | 1 | 18.288 | 18.32842 | -0.2576 | YES |
| B.2.3.13.1.2 | P-1 | 2W Analog Loop w/LNP Non-Design/<10 circuits/Equipment/GA(days) | R&B (POTS) excl SB Or | 8.33 | 3 | 0.00 | 0 | 11.015 | | | YES |
| B.2.3.13.1.3 | P-1 | 2W Analog Loop w/LNP Non-Design/<10 circuits/Other/GA(days) | R&B (POTS) excl SB Or | 8.54 | 13 | 0.00 | 0 | 16.014 | | | YES |
| B.2.3.13.2.1 | P-1 | 2W Analog Loop w/LNP Non-Design/>=10 circuits/Facility/GA(days) | R&B (POTS) excl SB Or | 9.50 | 2 | 0.00 | 0 | 10.607 | | | YES |
| B.2.3.13.2.2 | P-1 | 2W Analog Loop w/LNP Non-Design/>=10 circuits/Equipment/GA(days) | R&B (POTS) excl SB Or | 0.00 | 0 | 0.00 | 0 | | | | YES |
| B.2.3.13.2.3 | P-1 | 2W Analog Loop w/LNP Non-Design/>=10 circuits/Other/GA(days) | R&B (POTS) excl SB Or | 0.00 | 0 | 0.00 | 0 | | | | YES |
| B.2.3.14.1.1 | P-1 | Other Design/<10 circuits/Facility/GA(days) | Design | 0.00 | 0 | 0.00 | 0 | | | | YES |
| B.2.3.14.1.2 | P-1 | Other Design/<10 circuits/Equipment/GA(days) | Design | 0.00 | 0 | 0.00 | 0 | | | | YES |
| B.2.3.14.1.3 | P-1 | Other Design/<10 circuits/Other/GA(days) | Design | 67.25 | 4 | 0.00 | 0 | 50.023 | | | YES |
| B.2.3.14.2.1 | P-1 | Other Design/>=10 circuits/Facility/GA(days) | Design | 0.00 | 0 | 0.00 | 0 | | | | YES |
| B.2.3.14.2.2 | P-1 | Other Design/>=10 circuits/Equipment/GA(days) | Design | 12.00 | 2 | 0.00 | 0 | 2.828 | | | YES |
| B.2.3.14.2.3 | P-1 | Other Design/>=10 circuits/Other/GA(days) | Design | 14.00 | 1 | 0.00 | 0 | 0.000 | | | YES |
| B.2.3.15.1.1 | P-1 | Other Non-Design/<10 circuits/Facility/GA(days) | R&B | 10.14 | 233 | 7.00 | 1 | 18.043 | 18.08148 | 0.1735 | YES |
| B.2.3.15.1.2 | P-1 | Other Non-Design/<10 circuits/Equipment/GA(days) | R&B | 8.33 | 3 | 0.00 | 0 | 11.015 | | | YES |
| B.2.3.15.1.3 | P-1 | Other Non-Design/<10 circuits/Other/GA(days) | R&B | 8.54 | 13 | 0.00 | 0 | 16.014 | | | YES |
| B.2.3.15.2.1 | P-1 | Other Non-Design/>=10 circuits/Facility/GA(days) | R&B | 29.33 | 3 | 0.00 | 0 | 35.162 | | | YES |
| B.2.3.15.2.2 | P-1 | Other Non-Design/>=10 circuits/Equipment/GA(days) | R&B | 0.00 | 0 | 0.00 | 0 | | | | YES |
| B.2.3.15.2.3 | P-1 | Other Non-Design/>=10 circuits/Other/GA(days) | R&B | 0.00 | 0 | 0.00 | 0 | | | | YES |
| B.2.3.16.1.1 | P-1 | INP (Standalone)/<10 circuits/Facility/GA(days) | R&B (POTS) | 10.28 | 226 | 0.00 | 0 | 18.288 | | | YES |
| B.2.3.16.1.2 | P-1 | INP (Standalone)/<10 circuits/Equipment/GA(days) | R&B (POTS) | 8.33 | 3 | 0.00 | 0 | 11.015 | | | YES |
| B.2.3.16.1.3 | P-1 | INP (Standalone)/<10 circuits/Other/GA(days) | R&B (POTS) | 8.54 | 13 | 0.00 | 0 | 16.014 | | | YES |
| B.2.3.16.2.1 | P-1 | INP (Standalone)/>=10 circuits/Facility/GA(days) | R&B (POTS) | 9.50 | 2 | | | 10.607 | | | |
| B.2.3.16.2.2 | P-1 | INP (Standalone)/>=10 circuits/Equipment/GA(days) | R&B (POTS) | 0.00 | 0 | | | | | | |
| B.2.3.16.2.3 | P-1 | INP (Standalone)/>=10 circuits/Other/GA(days) | R&B (POTS) | 0.00 | 0 | | | | | | |
| B.2.3.17.1.1 | P-1 | LNP (Standalone)/<10 circuits/Facility/GA(days) | R&B (POTS) | 10.28 | 226 | 0.00 | 0 | 18.288 | | | YES |
| B.2.3.17.1.2 | P-1 | LNP (Standalone)/<10 circuits/Equipment/GA(days) | R&B (POTS) | 8.33 | 3 | 0.00 | 0 | 11.015 | | | YES |
| B.2.3.17.1.3 | P-1 | LNP (Standalone)/<10 circuits/Other/GA(days) | R&B (POTS) | 8.54 | 13 | 0.00 | 0 | 16.014 | | | YES |
| B.2.3.17.2.1 | P-1 | LNP (Standalone)/>=10 circuits/Facility/GA(days) | R&B (POTS) | 9.50 | 2 | 0.00 | 0 | 10.607 | | | YES |

# BellSouth Monthly State Summary

## *Georgia, October 2001*

| | | | Benchmark / Analog | BST Measure | BST Volume | CLEC Measure | CLEC Volume | Standard Deviation | Standard Error | ZScore | Equity |
|---|---|---|---|---|---|---|---|---|---|---|---|
| B.2.3.17.2.2 | P-1 | LNP (Standalone)/>=10 circuits/Equipment/GA(days) | R&B (POTS) | 0.00 | 0 | 0.00 | 0 | | | | YES |
| B.2.3.17.2.3 | P-1 | LNP (Standalone)/>=10 circuits/Other/GA(days) | R&B (POTS) | 0.00 | 0 | 0.00 | 0 | | | | YES |
| B.2.3.18.1.1 | P-1 | Digital Loop < DS1/<10 circuits/Facility/GA(days) | Digital Loop < DS1 | 23.71 | 147 | 0.00 | 0 | 3.559 | | | YES |
| B.2.3.18.1.2 | P-1 | Digital Loop < DS1/<10 circuits/Equipment/GA(days) | Digital Loop < DS1 | 1.00 | 1 | 0.00 | 0 | | | | YES |
| B.2.3.18.1.3 | P-1 | Digital Loop < DS1/<10 circuits/Other/GA(days) | Digital Loop < DS1 | 36.17 | 12 | 0.00 | 0 | 50.487 | | | YES |
| B.2.3.18.2.1 | P-1 | Digital Loop < DS1/>=10 circuits/Facility/GA(days) | Digital Loop < DS1 | 0.00 | 0 | | | | | | |
| B.2.3.18.2.2 | P-1 | Digital Loop < DS1/>=10 circuits/Equipment/GA(days) | Digital Loop < DS1 | 0.00 | 0 | | | | | | |
| B.2.3.18.2.3 | P-1 | Digital Loop < DS1/>=10 circuits/Other/GA(days) | Digital Loop < DS1 | 0.00 | 0 | | | | | | |
| B.2.3.19.1.1 | P-1 | Digital Loop >= DS1/<10 circuits/Facility/GA(days) | Digital Loop >= DS1 | 0.00 | 0 | 15.00 | 1 | | | | NO |
| B.2.3.19.1.2 | P-1 | Digital Loop >= DS1/<10 circuits/Equipment/GA(days) | Digital Loop >= DS1 | 0.00 | 0 | 0.00 | 0 | | | | YES |
| B.2.3.19.1.3 | P-1 | Digital Loop >= DS1/<10 circuits/Other/GA(days) | Digital Loop >= DS1 | 0.00 | 0 | 0.00 | 0 | | | | YES |
| B.2.3.19.2.1 | P-1 | Digital Loop >= DS1/>=10 circuits/Facility/GA(days) | Digital Loop >= DS1 | 0.00 | 0 | | | | | | |
| B.2.3.19.2.2 | P-1 | Digital Loop >= DS1/>=10 circuits/Equipment/GA(days) | Digital Loop >= DS1 | 0.00 | 0 | | | | | | |
| B.2.3.19.2.3 | P-1 | Digital Loop >= DS1/>=10 circuits/Other/GA(days) | Digital Loop >= DS1 | 0.00 | 0 | | | | | | |
| | | ***% Jeopardies - Mechanized*** | | | | | | | | | |
| B.2.5.1 | P-2 | Switch Ports/GA(%) | R&B (POTS) | 0.49% | 555,609 | | | | | | |
| B.2.5.2 | P-2 | Local Interoffice Transport/GA(%) | DS1/ DS3 - Interoffice | 35.02% | 2,504 | | | | | | |
| B.2.5.3 | P-2 | Loop + Port Combinations/GA(%) | R&B | 0.52% | 559,572 | 0.12% | 32,848 | | 0.00041 | 9.7522 | YES |
| B.2.5.4 | P-2 | Combo Other/GA(%) | R&B&D - Disp | 4.30% | 72,209 | 11.11% | 18 | | 0.04783 | -1.4238 | YES |
| B.2.5.5 | P-2 | xDSL (ADSL, HDSL and UCL)/GA(%) | ADSL to Retail | 16.02% | 12,024 | 2.13% | 94 | | 0.03798 | 3.6575 | YES |
| B.2.5.6 | P-2 | UNE ISDN/GA(%) | ISDN - BRI | 6.96% | 1,107 | 4.35% | 46 | | 0.03828 | 0.6813 | YES |
| B.2.5.7 | P-2 | Line Sharing/GA(%) | ADSL to Retail | 16.02% | 12,024 | 0.00% | 2 | | 0.25937 | 0.6176 | YES |
| B.2.5.8 | P-2 | 2W Analog Loop Design/GA(%) | R&B - Disp | 0.52% | 559,572 | 0.00% | 21 | | 0.01562 | 0.3297 | YES |
| B.2.5.9 | P-2 | 2W Analog Loop Non-Design/GA(%) | R&B (POTS) excl SB Or | 0.95% | 288,149 | 10.34% | 29 | | 0.01802 | -5.2123 | NO |
| B.2.5.10 | P-2 | 2W Analog Loop w/INP Design/GA(%) | R&B - Disp | 0.52% | 559,572 | | | | | | |
| B.2.5.11 | P-2 | 2W Analog Loop w/INP Non-Design/GA(%) | R&B (POTS) excl SB Or | 0.95% | 288,149 | | | | | | |
| B.2.5.12 | P-2 | 2W Analog Loop w/LNP Design/GA(%) | R&B - Disp | 0.52% | 559,572 | 0.00% | 1,322 | | 0.00197 | 2.6130 | YES |
| B.2.5.13 | P-2 | 2W Analog Loop w/LNP Non-Design/GA(%) | R&B (POTS) excl SB Or | 0.95% | 288,149 | 0.07% | 5,549 | | 0.00132 | 6.6973 | YES |
| B.2.5.14 | P-2 | Other Design/GA(%) | Design | 5.75% | 3,910 | 10.00% | 110 | | 0.02251 | -1.8857 | NO |
| B.2.5.15 | P-2 | Other Non-Design/GA(%) | R&B | 0.52% | 559,572 | 4.30% | 186 | | 0.00525 | -7.2123 | NO |
| B.2.5.16 | P-2 | INP (Standalone)/GA(%) | R&B (POTS) | 0.49% | 555,609 | 0.00% | 5 | | 0.03133 | 0.1574 | YES |
| B.2.5.17 | P-2 | LNP (Standalone)/GA(%) | R&B (POTS) | 0.49% | 555,609 | 0.00% | 3,768 | | 0.00115 | 4.3068 | YES |
| B.2.5.18 | P-2 | Digital Loop < DS1/GA(%) | Digital Loop < DS1 | 15.12% | 13,912 | 1.57% | 127 | | 0.03193 | 4.2410 | YES |
| B.2.5.19 | P-2 | Digital Loop >= DS1/GA(%) | Digital Loop >= DS1 | 5.71% | 1,314 | 40.58% | 69 | | 0.02865 | -12.1707 | NO |
| | | ***% Jeopardies - Non-Mechanized*** | | | | | | | | | |
| B.2.6.1 | P-2 | Switch Ports/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.2.6.2 | P-2 | Local Interoffice Transport/GA(%) | Diagnostic | | | 0.00% | 1 | | | | Diagnostic |
| B.2.6.3 | P-2 | Loop + Port Combinations/GA(%) | Diagnostic | | | 0.87% | 230 | | | | Diagnostic |
| B.2.6.4 | P-2 | Combo Other/GA(%) | Diagnostic | | | 19.14% | 209 | | | | Diagnostic |
| B.2.6.5 | P-2 | xDSL (ADSL, HDSL and UCL)/GA(%) | Diagnostic | | | 5.13% | 78 | | | | Diagnostic |
| B.2.6.6 | P-2 | UNE ISDN/GA(%) | Diagnostic | | | 11.69% | 154 | | | | Diagnostic |
| B.2.6.7 | P-2 | Line Sharing/GA(%) | Diagnostic | | | 0.00% | 1 | | | | Diagnostic |
| B.2.6.8 | P-2 | 2W Analog Loop Design/GA(%) | Diagnostic | | | 16.67% | 6 | | | | Diagnostic |
| B.2.6.9 | P-2 | 2W Analog Loop Non-Design/GA(%) | Diagnostic | | | 0.00% | 29 | | | | Diagnostic |
| B.2.6.10 | P-2 | 2W Analog Loop w/INP Design/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.2.6.11 | P-2 | 2W Analog Loop w/INP Non-Design/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.2.6.12 | P-2 | 2W Analog Loop w/LNP Design/GA(%) | Diagnostic | | | 4.17% | 120 | | | | Diagnostic |
| B.2.6.13 | P-2 | 2W Analog Loop w/LNP Non-Design/GA(%) | Diagnostic | | | 3.24% | 1,359 | | | | Diagnostic |
| B.2.6.14 | P-2 | Other Design/GA(%) | Diagnostic | | | 0.00% | 21 | | | | Diagnostic |
| B.2.6.15 | P-2 | Other Non-Design/GA(%) | Diagnostic | | | 1.85% | 54 | | | | Diagnostic |
| B.2.6.16 | P-2 | INP (Standalone)/GA(%) | Diagnostic | | | 0.00% | 1 | | | | Diagnostic |
| B.2.6.17 | P-2 | LNP (Standalone)/GA(%) | Diagnostic | | | 0.00% | 1,618 | | | | Diagnostic |
| B.2.6.18 | P-2 | Digital Loop < DS1/GA(%) | Diagnostic | | | 8.80% | 216 | | | | Diagnostic |
| B.2.6.19 | P-2 | Digital Loop >= DS1/GA(%) | Diagnostic | | | 14.95% | 107 | | | | Diagnostic |
| | | ***Average Jeopardy Notice Interval - Mechanized*** | | | | | | | | | |
| B.2.8.1 | P-2 | Switch Ports/GA(hours) | >= 48 hrs | | | | | | | | |
| B.2.8.2 | P-2 | Local Interoffice Transport/GA(hours) | >= 48 hrs | | | | | | | | |
| B.2.8.3 | P-2 | Loop + Port Combinations/GA(hours) | >= 48 hrs | | | 174.15 | 39 | | | | YES |
| B.2.8.4 | P-2 | Combo Other/GA(hours) | >= 48 hrs | | | 252.00 | 2 | | | | YES |

# BellSouth Monthly State Summary

## *Georgia, October 2001*

| | | | Benchmark / Analog | BST Measure | BST Volume | CLEC Measure | CLEC Volume | Standard Deviation | Standard Error | ZScore | Equity |
|---|---|---|---|---|---|---|---|---|---|---|---|
| B.2.8.5 | P-2 | xDSL (ADSL, HDSL and UCL)/GA(hours) | >= 48 hrs | | | 432.00 | 2 | | | | YES |
| B.2.8.6 | P-2 | UNE ISDN/GA(hours) | >= 48 hrs | | | 348.00 | 2 | | | | YES |
| B.2.8.7 | P-2 | Line Sharing/GA(hours) | >= 48 hrs | | | | | | | | |
| B.2.8.8 | P-2 | 2W Analog Loop Design/GA(hours) | >= 48 hrs | | | | | | | | |
| B.2.8.9 | P-2 | 2W Analog Loop Non-Design/GA(hours) | >= 48 hrs | | | 216.00 | 3 | | | | YES |
| B.2.8.10 | P-2 | 2W Analog Loop w/INP Design/GA(hours) | >= 48 hrs | | | | | | | | |
| B.2.8.11 | P-2 | 2W Analog Loop w/INP Non-Design/GA(hours) | >= 48 hrs | | | | | | | | |
| B.2.8.12 | P-2 | 2W Analog Loop w/LNP Design/GA(hours) | >= 48 hrs | | | | | | | | |
| B.2.8.13 | P-2 | 2W Analog Loop w/LNP Non-Design/GA(hours) | >= 48 hrs | | | 28.35 | 4 | | | | NO |
| B.2.8.14 | P-2 | Other Design/GA(hours) | >= 48 hrs | | | 248.73 | 11 | | | | YES |
| B.2.8.15 | P-2 | Other Non-Design/GA(hours) | >= 48 hrs | | | 195.00 | 8 | | | | YES |
| B.2.8.16 | P-2 | INP (Standalone)/GA(hours) | >= 48 hrs | | | | | | | | |
| B.2.8.17 | P-2 | LNP (Standalone)/GA(hours) | >= 48 hrs | | | | | | | | |
| B.2.8.18 | P-2 | Digital Loop < DS1/GA(hours) | >= 48 hrs | | | 348.00 | 2 | | | | YES |
| B.2.8.19 | P-2 | Digital Loop >= DS1/GA(hours) | >= 48 hrs | | | 187.71 | 28 | | | | YES |
| | ***Average Jeopardy Notice Interval - Non-Mechanized*** | | | | | | | | | | |
| B.2.9.1 | P-2 | Switch Ports/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.9.2 | P-2 | Local Interoffice Transport/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.9.3 | P-2 | Loop + Port Combinations/GA(hours) | Diagnostic | | | 228.00 | 2 | | | | Diagnostic |
| B.2.9.4 | P-2 | Combo Other/GA(hours) | Diagnostic | | | 240.00 | 40 | | | | Diagnostic |
| B.2.9.5 | P-2 | xDSL (ADSL, HDSL and UCL)/GA(hours) | Diagnostic | | | 120.00 | 4 | | | | Diagnostic |
| B.2.9.6 | P-2 | UNE ISDN/GA(hours) | Diagnostic | | | 262.67 | 18 | | | | Diagnostic |
| B.2.9.7 | P-2 | Line Sharing/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.9.8 | P-2 | 2W Analog Loop Design/GA(hours) | Diagnostic | | | 408.00 | 1 | | | | Diagnostic |
| B.2.9.9 | P-2 | 2W Analog Loop Non-Design/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.9.10 | P-2 | 2W Analog Loop w/INP Design/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.9.11 | P-2 | 2W Analog Loop w/INP Non-Design/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.9.12 | P-2 | 2W Analog Loop w/LNP Design/GA(hours) | Diagnostic | | | 78.17 | 5 | | | | Diagnostic |
| B.2.9.13 | P-2 | 2W Analog Loop w/LNP Non-Design/GA(hours) | Diagnostic | | | 108.26 | 44 | | | | Diagnostic |
| B.2.9.14 | P-2 | Other Design/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.9.15 | P-2 | Other Non-Design/GA(hours) | Diagnostic | | | 360.00 | 1 | | | | Diagnostic |
| B.2.9.16 | P-2 | INP (Standalone)/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.9.17 | P-2 | LNP (Standalone)/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.9.18 | P-2 | Digital Loop < DS1/GA(hours) | Diagnostic | | | 257.68 | 19 | | | | Diagnostic |
| B.2.9.19 | P-2 | Digital Loop >= DS1/GA(hours) | Diagnostic | | | 171.00 | 16 | | | | Diagnostic |
| | ***% Jeopardy Notice >= 48 hours - Mechanized*** | | | | | | | | | | |
| B.2.10.1 | P-2 | Switch Ports/GA(%) | 95% >= 48 hrs | | | | | | | | |
| B.2.10.2 | P-2 | Local Interoffice Transport/GA(%) | 95% >= 48 hrs | | | | | | | | |
| B.2.10.3 | P-2 | Loop + Port Combinations/GA(%) | 95% >= 48 hrs | | | 100.00% | 39 | | | | YES |
| B.2.10.4 | P-2 | Combo Other/GA(%) | 95% >= 48 hrs | | | 100.00% | 2 | | | | YES |
| B.2.10.5 | P-2 | xDSL (ADSL, HDSL and UCL)/GA(%) | 95% >= 48 hrs | | | 100.00% | 2 | | | | YES |
| B.2.10.6 | P-2 | UNE ISDN/GA(%) | 95% >= 48 hrs | | | 100.00% | 2 | | | | YES |
| B.2.10.7 | P-2 | Line Sharing/GA(%) | 95% >= 48 hrs | | | | | | | | |
| B.2.10.8 | P-2 | 2W Analog Loop Design/GA(%) | 95% >= 48 hrs | | | | | | | | |
| B.2.10.9 | P-2 | 2W Analog Loop Non-Design/GA(%) | 95% >= 48 hrs | | | 100.00% | 3 | | | | YES |
| B.2.10.10 | P-2 | 2W Analog Loop w/INP Design/GA(%) | 95% >= 48 hrs | | | | | | | | |
| B.2.10.11 | P-2 | 2W Analog Loop w/INP Non-Design/GA(%) | 95% >= 48 hrs | | | | | | | | |
| B.2.10.12 | P-2 | 2W Analog Loop w/LNP Design/GA(%) | 95% >= 48 hrs | | | | | | | | |
| B.2.10.13 | P-2 | 2W Analog Loop w/LNP Non-Design/GA(%) | 95% >= 48 hrs | | | 0.00% | 4 | | | | NO |
| B.2.10.14 | P-2 | Other Design/GA(%) | 95% >= 48 hrs | | | 100.00% | 11 | | | | YES |
| B.2.10.15 | P-2 | Other Non-Design/GA(%) | 95% >= 48 hrs | | | 100.00% | 8 | | | | YES |
| B.2.10.16 | P-2 | INP (Standalone)/GA(%) | 95% >= 48 hrs | | | | | | | | |
| B.2.10.17 | P-2 | LNP (Standalone)/GA(%) | 95% >= 48 hrs | | | | | | | | |
| B.2.10.18 | P-2 | Digital Loop < DS1/GA(%) | 95% >= 48 hrs | | | 100.00% | 2 | | | | YES |
| B.2.10.19 | P-2 | Digital Loop >= DS1/GA(%) | 95% >= 48 hrs | | | 100.00% | 28 | | | | YES |
| | ***% Jeopardy Notice >= 48 hours - Non-Mechanized*** | | | | | | | | | | |
| B.2.11.1 | P-2 | Switch Ports/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.2.11.2 | P-2 | Local Interoffice Transport/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.2.11.3 | P-2 | Loop + Port Combinations/GA(%) | Diagnostic | | | 100.00% | 2 | | | | Diagnostic |

# BellSouth Monthly State Summary

## *Georgia, October 2001*

| | | | Benchmark / Analog | BST Measure | BST Volume | CLEC Measure | CLEC Volume | Standard Deviation | Standard Error | ZScore | Equity |
|---|---|---|---|---|---|---|---|---|---|---|---|
| B.2.11.4 | P-2 | Combo Other/GA(%) | Diagnostic | | | 100.00% | 40 | | | | Diagnostic |
| B.2.11.5 | P-2 | xDSL (ADSL, HDSL and UCL)/GA(%) | Diagnostic | | | 100.00% | 4 | | | | Diagnostic |
| B.2.11.6 | P-2 | UNE ISDN/GA(%) | Diagnostic | | | 100.00% | 18 | | | | Diagnostic |
| B.2.11.7 | P-2 | Line Sharing/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.2.11.8 | P-2 | 2W Analog Loop Design/GA(%) | Diagnostic | | | 100.00% | 1 | | | | Diagnostic |
| B.2.11.9 | P-2 | 2W Analog Loop Non-Design/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.2.11.10 | P-2 | 2W Analog Loop w/INP Design/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.2.11.11 | P-2 | 2W Analog Loop w/INP Non-Design/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.2.11.12 | P-2 | 2W Analog Loop w/LNP Design/GA(%) | Diagnostic | | | 100.00% | 5 | | | | Diagnostic |
| B.2.11.13 | P-2 | 2W Analog Loop w/LNP Non-Design/GA(%) | Diagnostic | | | 63.64% | 44 | | | | Diagnostic |
| B.2.11.14 | P-2 | Other Design/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.2.11.15 | P-2 | Other Non-Design/GA(%) | Diagnostic | | | 100.00% | 1 | | | | Diagnostic |
| B.2.11.16 | P-2 | INP (Standalone)/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.2.11.17 | P-2 | LNP (Standalone)/GA(%) | Diagnostic | | | | | | | | Diagnostic |
| B.2.11.18 | P-2 | Digital Loop < DS1/GA(%) | Diagnostic | | | 100.00% | 19 | | | | Diagnostic |
| B.2.11.19 | P-2 | Digital Loop >= DS1/GA(%) | Diagnostic | | | 100.00% | 16 | | | | Diagnostic |
| | ***Coordinated Customers Conversions*** | | | | | | | | | | |
| B.2.12.1 | P-7 | Loops with INP/GA(%) | >= 95% w in 15 min | | | 100.00% | 1 | | | | YES |
| B.2.12.2 | P-7 | Loops with LNP/GA(%) | >= 95% w in 15 min | | | 99.72% | 2,522 | | | | YES |
| | ***% Hot Cuts > 15 minutes Early*** | | | | | | | | | | |
| B.2.13.1 | P-7A | Time-Specific SL1/GA(%) | <= 5% | | | 0.00% | 791 | | | | YES |
| B.2.13.2 | P-7A | Time-Specific SL2/GA(%) | <= 5% | | | 0.00% | 66 | | | | YES |
| B.2.13.3 | P-7A | Non-Time Specific SL1/GA(%) | <= 5% | | | 0.00% | 123 | | | | YES |
| B.2.13.4 | P-7A | Non-Time Specific SL2/GA(%) | <= 5% | | | 0.00% | 106 | | | | YES |
| | ***Hot Cut Timeliness*** | | | | | | | | | | |
| B.2.14.1 | P-7A | Time-Specific SL1/GA(%) | >= 95% w in 15 min | | | 99.49% | 791 | | | | YES |
| B.2.14.2 | P-7A | Time-Specific SL2/GA(%) | >= 95% w in 15 min | | | 100.00% | 66 | | | | YES |
| B.2.14.3 | P-7A | Non-Time Specific SL1/GA(%) | >= 95% w in 15 min | | | 100.00% | 123 | | | | YES |
| B.2.14.4 | P-7A | Non-Time Specific SL2/GA(%) | >= 95% w in 15 min | | | 100.00% | 106 | | | | YES |
| | ***% Hot Cuts > 15 minutes Late*** | | | | | | | | | | |
| B.2.15.1 | P-7A | Time-Specific SL1/GA(%) | <= 5% | | | 0.51% | 791 | | | | YES |
| B.2.15.2 | P-7A | Time-Specific SL2/GA(%) | <= 5% | | | 0.00% | 66 | | | | YES |
| B.2.15.3 | P-7A | Non-Time Specific SL1/GA(%) | <= 5% | | | 0.00% | 123 | | | | YES |
| B.2.15.4 | P-7A | Non-Time Specific SL2/GA(%) | <= 5% | | | 0.00% | 106 | | | | YES |
| | ***Average Recovery Time - CCC*** | | | | | | | | | | |
| B.2.16.1 | P-7B | Loops with INP/GA(minutes) | Diagnostic | | | | | | | | Diagnostic |
| B.2.16.2 | P-7B | Loops with LNP/GA(minutes) | Diagnostic | | | 1470.26 | 7 | | | | Diagnostic |
| | ***% Provisioning Troubles within 7 Days - Hot Cuts*** | | | | | | | | | | |
| B.2.17.1.1 | P-7C | UNE Loop Design/Dispatch/GA(%) | <= 5% | | | 2.81% | 463 | | | | YES |
| B.2.17.1.2 | P-7C | UNE Loop Design/Non-Dispatch/GA(%) | <= 5% | | | 0.00% | 1 | | | | YES |
| B.2.17.2.1 | P-7C | UNE Loop Non-Design/Dispatch/GA(%) | <= 5% | | | 1.53% | 1,441 | | | | YES |
| B.2.17.2.2 | P-7C | UNE Loop Non-Design/Non-Dispatch/GA(%) | <= 5% | | | 1.13% | 353 | | | | YES |
| | ***% Missed Installation Appointments*** | | | | | | | | | | |
| B.2.18.1.1.1 | P-3 | Switch Ports/<10 circuits/Dispatch/GA(%) | R&B (POTS) | 3.76% | 64,103 | | | | | | |
| B.2.18.1.1.2 | P-3 | Switch Ports/<10 circuits/Non-Dispatch/GA(%) | R&B (POTS) | 0.03% | 488,191 | | | | | | |
| B.2.18.1.2.1 | P-3 | Switch Ports/>=10 circuits/Dispatch/GA(%) | R&B (POTS) | 5.64% | 195 | | | | | | |
| B.2.18.1.2.2 | P-3 | Switch Ports/>=10 circuits/Non-Dispatch/GA(%) | R&B (POTS) | 0.00% | 15 | | | | | | |
| B.2.18.2.1.1 | P-3 | Local Interoffice Transport/<10 circuits/Dispatch/GA(%) | DS1/DS3 | 2.70% | 2,187 | 100.00% | 1 | | 0.16205 | -6.0043 | NO |
| B.2.18.2.1.2 | P-3 | Local Interoffice Transport/<10 circuits/Non-Dispatch/GA(%) | DS1/DS3 | | | | | | | | |
| B.2.18.2.2.1 | P-3 | Local Interoffice Transport/>=10 circuits/Dispatch/GA(%) | DS1/DS3 | | | | | | | | |
| B.2.18.2.2.2 | P-3 | Local Interoffice Transport/>=10 circuits/Non-Dispatch/GA(%) | DS1/DS3 | | | | | | | | |
| B.2.18.3.1.1 | P-3 | Loop + Port Combinations/<10 circuits/Dispatch/GA(%) | R&B | 3.75% | 65,720 | 3.02% | 1,158 | | 0.00563 | 1.2931 | YES |
| B.2.18.3.1.2 | P-3 | Loop + Port Combinations/<10 circuits/Non-Dispatch/GA(%) | R&B | 0.03% | 490,256 | 0.06% | 41,491 | | 0.00009 | -3.2844 | NO |
| B.2.18.3.1.3 | P-3 | Loop + Port Combinations/<10 circuits/Switch Based Orders/GA(%) | R&B | 0.00% | 268,108 | 0.00% | 10,856 | | 0.00002 | 0.1973 | YES |
| B.2.18.3.1.4 | P-3 | Loop + Port Combinations/<10 circuits/Dispatch In/GA(%) | R&B | 0.06% | 222,148 | 0.08% | 30,635 | | 0.00015 | -0.9362 | YES |
| B.2.18.3.2.1 | P-3 | Loop + Port Combinations/>=10 circuits/Dispatch/GA(%) | R&B | 6.13% | 261 | 12.50% | 8 | | 0.08610 | -0.7398 | YES |
| B.2.18.3.2.2 | P-3 | Loop + Port Combinations/>=10 circuits/Non-Dispatch/GA(%) | R&B | 0.00% | 177 | 0.00% | 1 | | 0.00000 | | YES |

## BellSouth Monthly State Summary
### *Georgia, October 2001*

| | | | Benchmark / Analog | BST Measure | BST Volume | CLEC Measure | CLEC Volume | Standard Deviation | Standard Error | ZScore | Equity |
|---|---|---|---|---|---|---|---|---|---|---|---|
| B.2.18.3.2.3 | P-3 | Loop + Port Combinations/>=10 circuits/Switch Based Orders/GA(%) | R&B | 0.00% | 43 | | | | | | |
| B.2.18.3.2.4 | P-3 | Loop + Port Combinations/>=10 circuits/Dispatch In/GA(%) | R&B | 0.00% | 134 | 0.00% | 1 | | 0.00000 | | YES |
| B.2.18.4.1.1 | P-3 | Combo Other/<10 circuits/Dispatch/GA(%) | R&B&D - Disp | 3.80% | 68,095 | 3.08% | 227 | | 0.01271 | 0.5639 | YES |
| B.2.18.4.1.4 | P-3 | Combo Other/<10 circuits/Dispatch In/GA(%) | R&B&D - Disp | 3.80% | 68,095 | | | | | | |
| B.2.18.4.2.1 | P-3 | Combo Other/>=10 circuits/Dispatch/GA(%) | R&B&D - Disp | 4.61% | 347 | | | | | | |
| B.2.18.4.2.4 | P-3 | Combo Other/>=10 circuits/Dispatch In/GA(%) | R&B&D - Disp | 4.61% | 347 | | | | | | |
| B.2.18.5.1.1 | P-3 | xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/GA(%) | ADSL to Retail | 8.41% | 8,613 | 0.00% | 164 | | 0.02187 | 3.8431 | YES |
| B.2.18.5.1.2 | P-3 | xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/GA(%) | ADSL to Retail | 0.00% | 3,379 | | | | | | |
| B.2.18.5.2.1 | P-3 | xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/GA(%) | ADSL to Retail | 6.25% | 16 | | | | | | |
| B.2.18.5.2.2 | P-3 | xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/GA(%) | ADSL to Retail | | | | | | | | |
| B.2.18.6.1.1 | P-3 | UNE ISDN/<10 circuits/Dispatch/GA(%) | ISDN - BRI | 5.76% | 486 | 2.59% | 193 | | 0.01983 | 1.5993 | YES |
| B.2.18.6.1.2 | P-3 | UNE ISDN/<10 circuits/Non-Dispatch/GA(%) | ISDN - BRI | 0.68% | 587 | | | | | | |
| B.2.18.6.2.1 | P-3 | UNE ISDN/>=10 circuits/Dispatch/GA(%) | ISDN - BRI | | | | | | | | |
| B.2.18.6.2.2 | P-3 | UNE ISDN/>=10 circuits/Non-Dispatch/GA(%) | ISDN - BRI | | | | | | | | |
| B.2.18.7.1.1 | P-3 | Line Sharing/<10 circuits/Dispatch/GA(%) | ADSL to Retail | 8.41% | 8,613 | | | | | | |
| B.2.18.7.1.2 | P-3 | Line Sharing/<10 circuits/Non-Dispatch/GA(%) | ADSL to Retail | 0.00% | 3,379 | 0.00% | 3 | | 0.00000 | | YES |
| B.2.18.7.2.1 | P-3 | Line Sharing/>=10 circuits/Dispatch/GA(%) | ADSL to Retail | 6.25% | 16 | | | | | | |
| B.2.18.7.2.2 | P-3 | Line Sharing/>=10 circuits/Non-Dispatch/GA(%) | ADSL to Retail | | | | | | | | |
| B.2.18.8.1.1 | P-3 | 2W Analog Loop Design/<10 circuits/Dispatch/GA(%) | R&B - Disp | 3.75% | 65,720 | 2.04% | 49 | | 0.02715 | 0.6297 | YES |
| B.2.18.8.1.2 | P-3 | 2W Analog Loop Design/<10 circuits/Non-Dispatch/GA(%) | R&B - Disp | 3.75% | 65,720 | | | | | | |
| B.2.18.8.2.1 | P-3 | 2W Analog Loop Design/>=10 circuits/Dispatch/GA(%) | R&B - Disp | 6.13% | 261 | 0.00% | 1 | | 0.24034 | 0.2551 | YES |
| B.2.18.8.2.2 | P-3 | 2W Analog Loop Design/>=10 circuits/Non-Dispatch/GA(%) | R&B - Disp | 6.13% | 261 | | | | | | |
| B.2.18.9.1.1 | P-3 | 2W Analog Loop Non-Design/<10 circuits/Dispatch/GA(%) | R&B (POTS) excl SB Or | 3.76% | 64,103 | 1.64% | 61 | | 0.02436 | 0.8692 | YES |
| B.2.18.9.1.4 | P-3 | 2W Analog Loop Non-Design/<10 circuits/Dispatch In/GA(%) | R&B (POTS) excl SB Or | 0.06% | 221,023 | | | | | | |
| B.2.18.9.2.1 | P-3 | 2W Analog Loop Non-Design/>=10 circuits/Dispatch/GA(%) | R&B (POTS) excl SB Or | 5.64% | 195 | 0.00% | 2 | | 0.16397 | 0.3440 | YES |
| B.2.18.9.2.4 | P-3 | 2W Analog Loop Non-Design/>=10 circuits/Dispatch In/GA(%) | R&B (POTS) excl SB Or | 0.00% | 7 | | | | | | |
| B.2.18.10.1.1 | P-3 | 2W Analog Loop w/INP Design/<10 circuits/Dispatch/GA(%) | R&B - Disp | 3.75% | 65,720 | | | | | | |
| B.2.18.10.1.2 | P-3 | 2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/GA(%) | R&B - Disp | 3.75% | 65,720 | | | | | | |
| B.2.18.10.2.1 | P-3 | 2W Analog Loop w/INP Design/>=10 circuits/Dispatch/GA(%) | R&B - Disp | 6.13% | 261 | | | | | | |
| B.2.18.10.2.2 | P-3 | 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/GA(%) | R&B - Disp | 6.13% | 261 | | | | | | |
| B.2.18.11.1.1 | P-3 | 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/GA(%) | R&B (POTS) excl SB Or | 3.76% | 64,103 | | | | | | |
| B.2.18.11.1.4 | P-3 | 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/GA(%) | R&B (POTS) excl SB Or | 0.06% | 221,023 | | | | | | |
| B.2.18.11.2.1 | P-3 | 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/GA(%) | R&B (POTS) excl SB Or | 5.64% | 195 | | | | | | |
| B.2.18.11.2.4 | P-3 | 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/GA(%) | R&B (POTS) excl SB Or | 0.00% | 7 | | | | | | |
| B.2.18.12.1.1 | P-12 | 2W Analog Loop w/LNP Design/<10 circuits/Dispatch/GA(%) | R&B - Disp | 3.75% | 65,720 | 0.00% | 176 | | 0.01434 | 2.6154 | YES |
| B.2.18.12.1.2 | P-12 | 2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/GA(%) | R&B - Disp | 3.75% | 65,720 | | | | | | |
| B.2.18.12.2.1 | P-12 | 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/GA(%) | R&B - Disp | 6.13% | 261 | 0.00% | 1 | | 0.24034 | 0.2551 | YES |
| B.2.18.12.2.2 | P-12 | 2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/GA(%) | R&B - Disp | 6.13% | 261 | | | | | | |
| B.2.18.13.1.1 | P-12 | 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/GA(%) | R&B (POTS) excl SB Or | 3.76% | 64,103 | 1.15% | 434 | | 0.00916 | 2.8461 | YES |
| B.2.18.13.1.4 | P-12 | 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/GA(%) | R&B (POTS) excl SB Or | 0.06% | 221,023 | | | | | | |
| B.2.18.13.2.1 | P-12 | 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/GA(%) | R&B (POTS) excl SB Or | 5.64% | 195 | 0.00% | 13 | | 0.06609 | 0.8536 | YES |
| B.2.18.13.2.4 | P-12 | 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/GA(%) | R&B (POTS) excl SB Or | 0.00% | 7 | | | | | | |
| B.2.18.14.1.1 | P-3 | Other Design/<10 circuits/Dispatch/GA(%) | Design | 5.18% | 2,375 | 0.00% | 141 | | 0.01921 | 2.6962 | YES |
| B.2.18.14.1.2 | P-3 | Other Design/<10 circuits/Non-Dispatch/GA(%) | Design | 3.43% | 525 | | | | | | |
| B.2.18.14.2.1 | P-3 | Other Design/>=10 circuits/Dispatch/GA(%) | Design | 0.00% | 86 | 0.00% | 1 | | 0.00000 | | YES |
| B.2.18.14.2.2 | P-3 | Other Design/>=10 circuits/Non-Dispatch/GA(%) | Design | 0.00% | 255 | | | | | | |
| B.2.18.15.1.1 | P-3 | Other Non-Design/<10 circuits/Dispatch/GA(%) | R&B | 3.75% | 65,720 | 2.05% | 244 | | 0.01219 | 1.3963 | YES |
| B.2.18.15.1.2 | P-3 | Other Non-Design/<10 circuits/Non-Dispatch/GA(%) | R&B | 0.03% | 490,256 | 0.00% | 9 | | 0.00569 | 0.0512 | YES |
| B.2.18.15.2.1 | P-3 | Other Non-Design/>=10 circuits/Dispatch/GA(%) | R&B | 6.13% | 261 | 0.00% | 9 | | 0.08133 | 0.7538 | YES |
| B.2.18.15.2.2 | P-3 | Other Non-Design/>=10 circuits/Non-Dispatch/GA(%) | R&B | 0.00% | 177 | 0.00% | 2 | | 0.00000 | | YES |
| B.2.18.16.1.1 | P-3 | INP (Standalone)/<10 circuits/Dispatch/GA(%) | R&B (POTS) | 3.76% | 64,103 | | | | | | |
| B.2.18.16.1.2 | P-3 | INP (Standalone)/<10 circuits/Non-Dispatch/GA(%) | R&B (POTS) | 0.03% | 488,191 | 0.00% | 6 | | 0.00689 | 0.0413 | YES |
| B.2.18.16.2.1 | P-3 | INP (Standalone)/>=10 circuits/Dispatch/GA(%) | R&B (POTS) | 5.64% | 195 | | | | | | |
| B.2.18.16.2.2 | P-3 | INP (Standalone)/>=10 circuits/Non-Dispatch/GA(%) | R&B (POTS) | 0.00% | 15 | | | | | | |
| B.2.18.17.1.1 | P-12 | LNP (Standalone)/<10 circuits/Dispatch/GA(%) | R&B (POTS) | 3.76% | 64,103 | 0.00% | 2 | | 0.13445 | 0.2794 | YES |
| B.2.18.17.1.2 | P-12 | LNP (Standalone)/<10 circuits/Non-Dispatch/GA(%) | R&B (POTS) | 0.03% | 488,191 | 0.03% | 3,017 | | 0.00031 | -0.0496 | YES |
| B.2.18.17.2.1 | P-12 | LNP (Standalone)/>=10 circuits/Dispatch/GA(%) | R&B (POTS) | 5.64% | 195 | | | | | | |
| B.2.18.17.2.2 | P-12 | LNP (Standalone)/>=10 circuits/Non-Dispatch/GA(%) | R&B (POTS) | 0.00% | 15 | 0.00% | 15 | | 0.00000 | | YES |
| B.2.18.18.1.1 | P-3 | Digital Loop < DS1/<10 circuits/Dispatch/GA(%) | Digital Loop < DS1 | 8.29% | 9,519 | 1.51% | 332 | | 0.01539 | 4.4063 | YES |
| B.2.18.18.1.2 | P-3 | Digital Loop < DS1/<10 circuits/Non-Dispatch/GA(%) | Digital Loop < DS1 | 0.09% | 4,285 | | | | | | |
| B.2.18.18.2.1 | P-3 | Digital Loop < DS1/>=10 circuits/Dispatch/GA(%) | Digital Loop < DS1 | 6.25% | 16 | | | | | | |

# BellSouth Monthly State Summary

## *Georgia, October 2001*

| | | | Benchmark / Analog | BST Measure | BST Volume | CLEC Measure | CLEC Volume | Standard Deviation | Standard Error | ZScore | Equity |
|---|---|---|---|---|---|---|---|---|---|---|---|
| B.2.18.18.2.2 | P-3 | Digital Loop < DS1/>=10 circuits/Non-Dispatch/GA(%) | Digital Loop < DS1 | 0.00% | 1 | | | | | | |
| B.2.18.19.1.1 | P-3 | Digital Loop >= DS1/<10 circuits/Dispatch/GA(%) | Digital Loop >= DS1 | 3.07% | 424 | 3.55% | 169 | | 0.01568 | -0.3088 | YES |
| B.2.18.19.1.2 | P-3 | Digital Loop >= DS1/<10 circuits/Non-Dispatch/GA(%) | Digital Loop >= DS1 | 1.35% | 443 | | | | | | |
| B.2.18.19.2.1 | P-3 | Digital Loop >= DS1/>=10 circuits/Dispatch/GA(%) | Digital Loop >= DS1 | 0.00% | 2 | | | | | | |
| B.2.18.19.2.2 | P-3 | Digital Loop >= DS1/>=10 circuits/Non-Dispatch/GA(%) | Digital Loop >= DS1 | 0.00% | 254 | | | | | | |
| | | ***% Provisioning Troubles within 30 Days*** | | | | | | | | | |
| B.2.19.1.1.1 | P-9 | Switch Ports/<10 circuits/Dispatch/GA(%) | R&B (POTS) | 7.35% | 55,846 | | | | | | |
| B.2.19.1.1.2 | P-9 | Switch Ports/<10 circuits/Non-Dispatch/GA(%) | R&B (POTS) | 4.41% | 391,329 | | | | | | |
| B.2.19.1.2.1 | P-9 | Switch Ports/>=10 circuits/Dispatch/GA(%) | R&B (POTS) | 10.11% | 178 | | | | | | |
| B.2.19.1.2.2 | P-9 | Switch Ports/>=10 circuits/Non-Dispatch/GA(%) | R&B (POTS) | 15.79% | 19 | | | | | | |
| B.2.19.2.1.1 | P-9 | Local Interoffice Transport/<10 circuits/Dispatch/GA(%) | DS1/DS3 | 7.19% | 1,557 | | | | | | |
| B.2.19.2.1.2 | P-9 | Local Interoffice Transport/<10 circuits/Non-Dispatch/GA(%) | DS1/DS3 | 0.00% | 1 | | | | | | |
| B.2.19.2.2.1 | P-9 | Local Interoffice Transport/>=10 circuits/Dispatch/GA(%) | DS1/DS3 | | | | | | | | |
| B.2.19.2.2.2 | P-9 | Local Interoffice Transport/>=10 circuits/Non-Dispatch/GA(%) | DS1/DS3 | | | | | | | | |
| B.2.19.3.1.1 | P-9 | Loop + Port Combinations/<10 circuits/Dispatch/GA(%) | R&B | 7.20% | 57,267 | 4.89% | 1,003 | | 0.00823 | 2.8110 | YES |
| B.2.19.3.1.2 | P-9 | Loop + Port Combinations/<10 circuits/Non-Dispatch/GA(%) | R&B | 4.40% | 392,924 | 1.90% | 35,686 | | 0.00113 | 22.0308 | YES |
| B.2.19.3.1.3 | P-9 | Loop + Port Combinations/<10 circuits/Switch Based Orders/GA(%) | R&B | 4.59% | 213,676 | 2.12% | 7,656 | | 0.00243 | 10.1699 | YES |
| B.2.19.3.1.4 | P-9 | Loop + Port Combinations/<10 circuits/Dispatch In/GA(%) | R&B | 4.17% | 179,249 | 1.84% | 28,030 | | 0.00128 | 18.1122 | YES |
| B.2.19.3.2.1 | P-9 | Loop + Port Combinations/>=10 circuits/Dispatch/GA(%) | R&B | 8.26% | 230 | 15.38% | 13 | | 0.07848 | -0.9077 | YES |
| B.2.19.3.2.2 | P-9 | Loop + Port Combinations/>=10 circuits/Non-Dispatch/GA(%) | R&B | 4.23% | 142 | 0.00% | 4 | | 0.10199 | 0.4143 | YES |
| B.2.19.3.2.3 | P-9 | Loop + Port Combinations/>=10 circuits/Switch Based Orders/GA(%) | R&B | 6.52% | 46 | | | | | | |
| B.2.19.3.2.4 | P-9 | Loop + Port Combinations/>=10 circuits/Dispatch In/GA(%) | R&B | 3.13% | 96 | 0.00% | 4 | | 0.08879 | 0.3520 | YES |
| B.2.19.4.1.1 | P-9 | Combo Other/<10 circuits/Dispatch/GA(%) | R&B&D - Disp | 7.09% | 59,041 | | | | | | |
| B.2.19.4.1.4 | P-9 | Combo Other/<10 circuits/Dispatch In/GA(%) | R&B&D - Disp | 7.09% | 59,041 | | | | | | |
| B.2.19.4.2.1 | P-9 | Combo Other/>=10 circuits/Dispatch/GA(%) | R&B&D - Disp | 4.99% | 381 | | | | | | |
| B.2.19.4.2.4 | P-9 | Combo Other/>=10 circuits/Dispatch In/GA(%) | R&B&D - Disp | 4.99% | 381 | | | | | | |
| B.2.19.5.1.1 | P-9 | xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/GA(%) | ADSL to Retail | 11.18% | 7,489 | 7.69% | 156 | | 0.02549 | 1.3670 | YES |
| B.2.19.5.1.2 | P-9 | xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/GA(%) | ADSL to Retail | 10.48% | 1,785 | | | | | | |
| B.2.19.5.2.1 | P-9 | xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/GA(%) | ADSL to Retail | 5.56% | 18 | | | | | | |
| B.2.19.5.2.2 | P-9 | xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/GA(%) | ADSL to Retail | | | | | | | | |
| B.2.19.6.1.1 | P-9 | UNE ISDN/<10 circuits/Dispatch/GA(%) | ISDN - BRI | 2.20% | 410 | 4.48% | 223 | | 0.01219 | -1.8776 | NO |
| B.2.19.6.1.2 | P-9 | UNE ISDN/<10 circuits/Non-Dispatch/GA(%) | ISDN - BRI | 0.91% | 330 | | | | | | |
| B.2.19.6.2.1 | P-9 | UNE ISDN/>=10 circuits/Dispatch/GA(%) | ISDN - BRI | | | | | | | | |
| B.2.19.6.2.2 | P-9 | UNE ISDN/>=10 circuits/Non-Dispatch/GA(%) | ISDN - BRI | 0.00% | 1 | | | | | | |
| B.2.19.7.1.1 | P-9 | Line Sharing/<10 circuits/Dispatch/GA(%) | ADSL to Retail | 11.18% | 7,489 | 33.33% | 3 | | 0.18195 | -1.2178 | YES |
| B.2.19.7.1.2 | P-9 | Line Sharing/<10 circuits/Non-Dispatch/GA(%) | ADSL to Retail | 10.48% | 1,785 | 30.77% | 13 | | 0.08525 | -2.3805 | NO |
| B.2.19.7.2.1 | P-9 | Line Sharing/>=10 circuits/Dispatch/GA(%) | ADSL to Retail | 5.56% | 18 | | | | | | |
| B.2.19.7.2.2 | P-9 | Line Sharing/>=10 circuits/Non-Dispatch/GA(%) | ADSL to Retail | | | | | | | | |
| B.2.19.8.1.1 | P-9 | 2W Analog Loop Design/<10 circuits/Dispatch/GA(%) | R&B - Disp | 7.20% | 57,267 | 7.63% | 118 | | 0.02382 | -0.1795 | YES |
| B.2.19.8.1.2 | P-9 | 2W Analog Loop Design/<10 circuits/Non-Dispatch/GA(%) | R&B - Disp | 7.20% | 57,267 | | | | | | |
| B.2.19.8.2.1 | P-9 | 2W Analog Loop Design/>=10 circuits/Dispatch/GA(%) | R&B - Disp | 8.26% | 230 | 0.00% | 1 | | 0.27589 | 0.2994 | YES |
| B.2.19.8.2.2 | P-9 | 2W Analog Loop Design/>=10 circuits/Non-Dispatch/GA(%) | R&B - Disp | 8.26% | 230 | | | | | | |
| B.2.19.9.1.1 | P-9 | 2W Analog Loop Non-Design/<10 circuits/Dispatch/GA(%) | R&B (POTS) excl SB Or | 7.35% | 55,845 | 0.00% | 245 | | 0.01671 | 4.3986 | YES |
| B.2.19.9.1.4 | P-9 | 2W Analog Loop Non-Design/<10 circuits/Dispatch In/GA(%) | R&B (POTS) excl SB Or | 4.18% | 178,195 | 0.00% | 4 | | 0.10007 | 0.4177 | YES |
| B.2.19.9.2.1 | P-9 | 2W Analog Loop Non-Design/>=10 circuits/Dispatch/GA(%) | R&B (POTS) excl SB Or | 10.11% | 178 | 0.00% | 8 | | 0.10896 | 0.9281 | YES |
| B.2.19.9.2.4 | P-9 | 2W Analog Loop Non-Design/>=10 circuits/Dispatch In/GA(%) | R&B (POTS) excl SB Or | 16.67% | 6 | | | | | | |
| B.2.19.10.1.1 | P-9 | 2W Analog Loop w/INP Design/<10 circuits/Dispatch/GA(%) | R&B - Disp | 7.20% | 57,267 | | | | | | |
| B.2.19.10.1.2 | P-9 | 2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/GA(%) | R&B - Disp | 7.20% | 57,267 | | | | | | |
| B.2.19.10.2.1 | P-9 | 2W Analog Loop w/INP Design/>=10 circuits/Dispatch/GA(%) | R&B - Disp | 8.26% | 230 | | | | | | |
| B.2.19.10.2.2 | P-9 | 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/GA(%) | R&B - Disp | 8.26% | 230 | | | | | | |
| B.2.19.11.1.1 | P-9 | 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/GA(%) | R&B (POTS) excl SB Or | 7.35% | 55,845 | 0.00% | 2 | | 0.18451 | 0.3983 | YES |
| B.2.19.11.1.4 | P-9 | 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/GA(%) | R&B (POTS) excl SB Or | 4.18% | 178,195 | | | | | | |
| B.2.19.11.2.1 | P-9 | 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/GA(%) | R&B (POTS) excl SB Or | 10.11% | 178 | | | | | | |
| B.2.19.11.2.4 | P-9 | 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/GA(%) | R&B (POTS) excl SB Or | 16.67% | 6 | | | | | | |
| B.2.19.12.1.1 | P-9 | 2W Analog Loop w/LNP Design/<10 circuits/Dispatch/GA(%) | R&B - Disp | 7.20% | 57,267 | 2.87% | 209 | | 0.01791 | 2.4171 | YES |
| B.2.19.12.1.2 | P-9 | 2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/GA(%) | R&B - Disp | 7.20% | 57,267 | | | | | | |
| B.2.19.12.2.1 | P-9 | 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/GA(%) | R&B - Disp | 8.26% | 230 | | | | | | |
| B.2.19.12.2.2 | P-9 | 2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/GA(%) | R&B - Disp | 8.26% | 230 | | | | | | |
| B.2.19.13.1.1 | P-9 | 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/GA(%) | R&B (POTS) excl SB Or | 7.35% | 55,845 | 0.00% | 538 | | 0.01130 | 6.5012 | YES |
| B.2.19.13.1.4 | P-9 | 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/GA(%) | R&B (POTS) excl SB Or | 4.18% | 178,195 | | | | | | |

## BellSouth Monthly State Summary
### *Georgia, October 2001*

| | | | Benchmark / Analog | BST Measure | BST Volume | CLEC Measure | CLEC Volume | Standard Deviation | Standard Error | ZScore | Equity |
|---|---|---|---|---|---|---|---|---|---|---|---|
| B.2.19.13.2.1 | P-9 | 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/GA(%) | R&B (POTS) excl SB Or | 10.11% | 178 | 0.00% | 27 | | 0.06227 | 1.6240 | YES |
| B.2.19.13.2.4 | P-9 | 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/GA(%) | R&B (POTS) excl SB Or | 16.67% | 6 | | | | | | |
| B.2.19.14.1.1 | P-9 | Other Design/<10 circuits/Dispatch/GA(%) | Design | 3.66% | 1,775 | 8.09% | 272 | | 0.01223 | -3.6191 | NO |
| B.2.19.14.1.2 | P-9 | Other Design/<10 circuits/Non-Dispatch/GA(%) | Design | 1.42% | 705 | | | | | | |
| B.2.19.14.2.1 | P-9 | Other Design/>=10 circuits/Dispatch/GA(%) | Design | 0.00% | 151 | 0.00% | 1 | | 0.00000 | | YES |
| B.2.19.14.2.2 | P-9 | Other Design/>=10 circuits/Non-Dispatch/GA(%) | Design | 0.00% | 122 | | | | | | |
| B.2.19.15.1.1 | P-9 | Other Non-Design/<10 circuits/Dispatch/GA(%) | R&B | 7.20% | 57,267 | 0.00% | 23 | | 0.05391 | 1.3355 | YES |
| B.2.19.15.1.2 | P-9 | Other Non-Design/<10 circuits/Non-Dispatch/GA(%) | R&B | 4.40% | 392,924 | 0.00% | 6 | | 0.08371 | 0.5253 | YES |
| B.2.19.15.2.1 | P-9 | Other Non-Design/>=10 circuits/Dispatch/GA(%) | R&B | 8.26% | 230 | 0.00% | 9 | | 0.09354 | 0.8831 | YES |
| B.2.19.15.2.2 | P-9 | Other Non-Design/>=10 circuits/Non-Dispatch/GA(%) | R&B | 4.23% | 142 | 0.00% | 1 | | 0.20187 | 0.2093 | YES |
| B.2.19.16.1.1 | P-9 | INP (Standalone)/<10 circuits/Dispatch/GA(%) | R&B (POTS) | 7.35% | 55,846 | | | | | | |
| B.2.19.16.1.2 | P-9 | INP (Standalone)/<10 circuits/Non-Dispatch/GA(%) | R&B (POTS) | 4.41% | 391,329 | 0.00% | 1 | | 0.20522 | 0.2147 | YES |
| B.2.19.16.2.1 | P-9 | INP (Standalone)/>=10 circuits/Dispatch/GA(%) | R&B (POTS) | 10.11% | 178 | | | | | | |
| B.2.19.16.2.2 | P-9 | INP (Standalone)/>=10 circuits/Non-Dispatch/GA(%) | R&B (POTS) | 15.79% | 19 | | | | | | |
| B.2.19.17.1.1 | P-9 | LNP (Standalone)/<10 circuits/Dispatch/GA(%) | R&B (POTS) | 7.35% | 55,846 | | | | | | |
| B.2.19.17.1.2 | P-9 | LNP (Standalone)/<10 circuits/Non-Dispatch/GA(%) | R&B (POTS) | 4.41% | 391,329 | | | | | | |
| B.2.19.17.2.1 | P-9 | LNP (Standalone)/>=10 circuits/Dispatch/GA(%) | R&B (POTS) | 10.11% | 178 | | | | | | |
| B.2.19.17.2.2 | P-9 | LNP (Standalone)/>=10 circuits/Non-Dispatch/GA(%) | R&B (POTS) | 15.79% | 19 | | | | | | |
| B.2.19.18.1.1 | P-9 | Digital Loop < DS1/<10 circuits/Dispatch/GA(%) | Digital Loop < DS1 | 10.23% | 8,305 | 5.48% | 365 | | 0.01621 | 2.9336 | YES |
| B.2.19.18.1.2 | P-9 | Digital Loop < DS1/<10 circuits/Non-Dispatch/GA(%) | Digital Loop < DS1 | 7.89% | 2,409 | | | | | | |
| B.2.19.18.2.1 | P-9 | Digital Loop < DS1/>=10 circuits/Dispatch/GA(%) | Digital Loop < DS1 | 5.56% | 18 | | | | | | |
| B.2.19.18.2.2 | P-9 | Digital Loop < DS1/>=10 circuits/Non-Dispatch/GA(%) | Digital Loop < DS1 | 0.00% | 1 | | | | | | |
| B.2.19.19.1.1 | P-9 | Digital Loop >= DS1/<10 circuits/Dispatch/GA(%) | Digital Loop >= DS1 | 2.38% | 336 | 10.34% | 145 | | 0.01515 | -5.2573 | NO |
| B.2.19.19.1.2 | P-9 | Digital Loop >= DS1/<10 circuits/Non-Dispatch/GA(%) | Digital Loop >= DS1 | 0.49% | 612 | | | | | | |
| B.2.19.19.2.1 | P-9 | Digital Loop >= DS1/>=10 circuits/Dispatch/GA(%) | Digital Loop >= DS1 | 0.00% | 38 | | | | | | |
| B.2.19.19.2.2 | P-9 | Digital Loop >= DS1/>=10 circuits/Non-Dispatch/GA(%) | Digital Loop >= DS1 | 0.00% | 114 | | | | | | |
| | ***Average Completion Notice Interval - Mechanized*** | | | | | | | | | | |
| B.2.21.1.1.1 | P-5 | Switch Ports/<10 circuits/Dispatch/GA(hours) | R&B (POTS) | 5.81 | 32,200 | | | 76.886 | | | |
| B.2.21.1.1.2 | P-5 | Switch Ports/<10 circuits/Non-Dispatch/GA(hours) | R&B (POTS) | 1.51 | 364,555 | | | 7.786 | | | |
| B.2.21.1.2.1 | P-5 | Switch Ports/>=10 circuits/Dispatch/GA(hours) | R&B (POTS) | 15.42 | 128 | | | 51.410 | | | |
| B.2.21.1.2.2 | P-5 | Switch Ports/>=10 circuits/Non-Dispatch/GA(hours) | R&B (POTS) | 41.81 | 13 | | | 143.160 | | | |
| B.2.21.2.1.1 | P-5 | Local Interoffice Transport/<10 circuits/Dispatch/GA(hours) | DS1/ DS3 - Interoffice | 96.82 | 1,600 | | | 244.079 | | | |
| B.2.21.2.1.2 | P-5 | Local Interoffice Transport/<10 circuits/Non-Dispatch/GA(hours) | DS1/ DS3 - Interoffice | | | | | | | | |
| B.2.21.2.2.1 | P-5 | Local Interoffice Transport/>=10 circuits/Dispatch/GA(hours) | DS1/ DS3 - Interoffice | | | | | | | | |
| B.2.21.2.2.2 | P-5 | Local Interoffice Transport/>=10 circuits/Non-Dispatch/GA(hours) | DS1/ DS3 - Interoffice | | | | | | | | |
| B.2.21.3.1.1 | P-5 | Loop + Port Combinations/<10 circuits/Dispatch/GA(hours) | R&B | 6.01 | 33,303 | 0.30 | 870 | 75.991 | 2.60978 | 2.1877 | YES |
| B.2.21.3.1.2 | P-5 | Loop + Port Combinations/<10 circuits/Non-Dispatch/GA(hours) | R&B | 1.52 | 365,949 | 1.00 | 33,077 | 7.879 | 0.04524 | 11.4759 | YES |
| B.2.21.3.1.3 | P-5 | Loop + Port Combinations/<10 circuits/Switch Based Orders/GA(hours) | R&B | 2.15 | 184,544 | 0.79 | 7,947 | 9.690 | 0.11101 | 12.2579 | YES |
| B.2.21.3.1.4 | P-5 | Loop + Port Combinations/<10 circuits/Dispatch In/GA(hours) | R&B | 0.88 | 181,405 | 1.07 | 25,130 | 5.376 | 0.03618 | -5.1846 | NO |
| B.2.21.3.2.1 | P-5 | Loop + Port Combinations/>=10 circuits/Dispatch/GA(hours) | R&B | 13.16 | 161 | 0.02 | 7 | 46.497 | 17.95224 | 0.7321 | YES |
| B.2.21.3.2.2 | P-5 | Loop + Port Combinations/>=10 circuits/Non-Dispatch/GA(hours) | R&B | 7.09 | 113 | | | 51.614 | | | |
| B.2.21.3.2.3 | P-5 | Loop + Port Combinations/>=10 circuits/Switch Based Orders/GA(hours) | R&B | 16.40 | 34 | | | 88.681 | | | |
| B.2.21.3.2.4 | P-5 | Loop + Port Combinations/>=10 circuits/Dispatch In/GA(hours) | R&B | 3.09 | 79 | | | 21.070 | | | |
| B.2.21.4.1.1 | P-5 | Combo Other/<10 circuits/Dispatch/GA(hours) | R&B&D - Disp | 15.41 | 34,727 | | | 144.012 | | | |
| B.2.21.4.1.4 | P-5 | Combo Other/<10 circuits/Dispatch In/GA(hours) | R&B&D - Disp | | | | | | | | |
| B.2.21.4.2.1 | P-5 | Combo Other/>=10 circuits/Dispatch/GA(hours) | R&B&D - Disp | 71.03 | 208 | | | 265.006 | | | |
| B.2.21.4.2.4 | P-5 | Combo Other/>=10 circuits/Dispatch In/GA(hours) | R&B&D - Disp | | | | | | | | |
| B.2.21.5.1.1 | P-5 | xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/GA(hours) | ADSL to Retail | 18.97 | 6,899 | 12.94 | 60 | 38.737 | 5.02260 | 1.2000 | YES |
| B.2.21.5.1.2 | P-5 | xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/GA(hours) | ADSL to Retail | 0.94 | 2,715 | | | 4.050 | | | |
| B.2.21.5.2.1 | P-5 | xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/GA(hours) | ADSL to Retail | 13.46 | 15 | | | 23.221 | | | |
| B.2.21.5.2.2 | P-5 | xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/GA(hours) | ADSL to Retail | | | | | | | | |
| B.2.21.6.1.1 | P-5 | UNE ISDN/<10 circuits/Dispatch/GA(hours) | ISDN - BRI | 17.96 | 316 | 0.02 | 1 | 67.792 | 67.89938 | 0.2642 | YES |
| B.2.21.6.1.2 | P-5 | UNE ISDN/<10 circuits/Non-Dispatch/GA(hours) | ISDN - BRI | 1.99 | 409 | | | 13.220 | | | |
| B.2.21.6.2.1 | P-5 | UNE ISDN/>=10 circuits/Dispatch/GA(hours) | ISDN - BRI | | | | | | | | |
| B.2.21.6.2.2 | P-5 | UNE ISDN/>=10 circuits/Non-Dispatch/GA(hours) | ISDN - BRI | | | | | | | | |
| B.2.21.7.1.1 | P-5 | Line Sharing/<10 circuits/Dispatch/GA(hours) | ADSL to Retail | 18.97 | 6,899 | | | 38.737 | | | |
| B.2.21.7.1.2 | P-5 | Line Sharing/<10 circuits/Non-Dispatch/GA(hours) | ADSL to Retail | 0.94 | 2,715 | | | 4.050 | | | |
| B.2.21.7.2.1 | P-5 | Line Sharing/>=10 circuits/Dispatch/GA(hours) | ADSL to Retail | 13.46 | 15 | | | 23.221 | | | |
| B.2.21.7.2.2 | P-5 | Line Sharing/>=10 circuits/Non-Dispatch/GA(hours) | ADSL to Retail | | | | | | | | |
| B.2.21.8.1.1 | P-5 | 2W Analog Loop Design/<10 circuits/Dispatch/GA(hours) | R&B - Disp | 6.01 | 33,303 | 17.24 | 35 | 75.991 | 12.85161 | -0.8737 | YES |

# BellSouth Monthly State Summary

## *Georgia, October 2001*

| | | | Benchmark / Analog | BST Measure | BST Volume | CLEC Measure | CLEC Volume | Standard Deviation | Standard Error | ZScore | Equity |
|---|---|---|---|---|---|---|---|---|---|---|---|
| B.2.21.8.1.2 | P-5 | 2W Analog Loop Design/<10 circuits/Non-Dispatch/GA(hours) | R&B - Disp | 6.01 | 33,303 | | | 75.991 | | | |
| B.2.21.8.2.1 | P-5 | 2W Analog Loop Design/>=10 circuits/Dispatch/GA(hours) | R&B - Disp | 13.16 | 161 | 62.30 | 1 | 46.497 | 46.64128 | -1.0535 | YES |
| B.2.21.8.2.2 | P-5 | 2W Analog Loop Design/>=10 circuits/Non-Dispatch/GA(hours) | R&B - Disp | 13.16 | 161 | | | 46.497 | | | |
| B.2.21.9.1.1 | P-5 | 2W Analog Loop Non-Design/<10 circuits/Dispatch/GA(hours) | R&B (POTS) excl SB Or | 5.81 | 32,200 | 0.05 | 28 | 76.886 | 14.53642 | 0.3964 | YES |
| B.2.21.9.1.4 | P-5 | 2W Analog Loop Non-Design/<10 circuits/Dispatch In/GA(hours) | R&B (POTS) excl SB Or | 0.87 | 180,525 | | | 5.353 | | | |
| B.2.21.9.2.1 | P-5 | 2W Analog Loop Non-Design/>=10 circuits/Dispatch/GA(hours) | R&B (POTS) excl SB Or | 15.42 | 128 | 0.02 | 1 | 51.410 | 51.61033 | 0.2983 | YES |
| B.2.21.9.2.4 | P-5 | 2W Analog Loop Non-Design/>=10 circuits/Dispatch In/GA(hours) | R&B (POTS) excl SB Or | 0.61 | 6 | | | 0.396 | | | |
| B.2.21.10.1.1 | P-5 | 2W Analog Loop w/INP Design/<10 circuits/Dispatch/GA(hours) | R&B - Disp | 6.01 | 33,303 | | | 75.991 | | | |
| B.2.21.10.1.2 | P-5 | 2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/GA(hours) | R&B - Disp | 6.01 | 33,303 | | | 75.991 | | | |
| B.2.21.10.2.1 | P-5 | 2W Analog Loop w/INP Design/>=10 circuits/Dispatch/GA(hours) | R&B - Disp | 13.16 | 161 | | | 46.497 | | | |
| B.2.21.10.2.2 | P-5 | 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/GA(hours) | R&B - Disp | 13.16 | 161 | | | 46.497 | | | |
| B.2.21.11.1.1 | P-5 | 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/GA(hours) | R&B (POTS) excl SB Or | 5.81 | 32,200 | | | 76.886 | | | |
| B.2.21.11.1.4 | P-5 | 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/GA(hours) | R&B (POTS) excl SB Or | 0.87 | 180,525 | | | 5.353 | | | |
| B.2.21.11.2.1 | P-5 | 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/GA(hours) | R&B (POTS) excl SB Or | 15.42 | 128 | | | 51.410 | | | |
| B.2.21.11.2.4 | P-5 | 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/GA(hours) | R&B (POTS) excl SB Or | 0.61 | 6 | | | 0.396 | | | |
| B.2.21.12.1.1 | P-5 | 2W Analog Loop w/LNP Design/<10 circuits/Dispatch/GA(hours) | R&B - Disp | 6.01 | 33,303 | 14.22 | 99 | 75.991 | 7.64875 | -1.0733 | YES |
| B.2.21.12.1.2 | P-5 | 2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/GA(hours) | R&B - Disp | 6.01 | 33,303 | | | 75.991 | | | |
| B.2.21.12.2.1 | P-5 | 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/GA(hours) | R&B - Disp | 13.16 | 161 | | | 46.497 | | | |
| B.2.21.12.2.2 | P-5 | 2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/GA(hours) | R&B - Disp | 13.16 | 161 | | | 46.497 | | | |
| B.2.21.13.1.1 | P-5 | 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/GA(hours) | R&B (POTS) excl SB Or | 5.81 | 32,200 | 3.85 | 310 | 76.886 | 4.38781 | 0.4462 | YES |
| B.2.21.13.1.4 | P-5 | 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/GA(hours) | R&B (POTS) excl SB Or | 0.87 | 180,525 | | | 5.353 | | | |
| B.2.21.13.2.1 | P-5 | 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/GA(hours) | R&B (POTS) excl SB Or | 15.42 | 128 | 5.57 | 8 | 51.410 | 18.73554 | 0.5256 | YES |
| B.2.21.13.2.4 | P-5 | 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/GA(hours) | R&B (POTS) excl SB Or | 0.61 | 6 | | | 0.396 | | | |
| B.2.21.14.1.1 | P-5 | Other Design/<10 circuits/Dispatch/GA(hours) | Design | 235.30 | 1,424 | 19.88 | 47 | 566.145 | 83.93241 | 2.5666 | YES |
| B.2.21.14.1.2 | P-5 | Other Design/<10 circuits/Non-Dispatch/GA(hours) | Design | 57.81 | 351 | | | 426.480 | | | |
| B.2.21.14.2.1 | P-5 | Other Design/>=10 circuits/Dispatch/GA(hours) | Design | 269.24 | 47 | | | 506.604 | | | |
| B.2.21.14.2.2 | P-5 | Other Design/>=10 circuits/Non-Dispatch/GA(hours) | Design | 4.96 | 225 | | | 42.675 | | | |
| B.2.21.15.1.1 | P-5 | Other Non-Design/<10 circuits/Dispatch/GA(hours) | R&B | 6.01 | 33,303 | 0.03 | 96 | 75.991 | 7.76699 | 0.7705 | YES |
| B.2.21.15.1.2 | P-5 | Other Non-Design/<10 circuits/Non-Dispatch/GA(hours) | R&B | 1.52 | 365,949 | | | 7.879 | | | |
| B.2.21.15.2.1 | P-5 | Other Non-Design/>=10 circuits/Dispatch/GA(hours) | R&B | 13.16 | 161 | 0.02 | 4 | 46.497 | 23.53558 | 0.5584 | YES |
| B.2.21.15.2.2 | P-5 | Other Non-Design/>=10 circuits/Non-Dispatch/GA(hours) | R&B | 7.09 | 113 | | | 51.614 | | | |
| B.2.21.16.1.1 | P-5 | INP (Standalone)/<10 circuits/Dispatch/GA(hours) | R&B (POTS) | 5.81 | 32,200 | | | 76.886 | | | |
| B.2.21.16.1.2 | P-5 | INP (Standalone)/<10 circuits/Non-Dispatch/GA(hours) | R&B (POTS) | 1.51 | 364,555 | | | 7.786 | | | |
| B.2.21.16.2.1 | P-5 | INP (Standalone)/>=10 circuits/Dispatch/GA(hours) | R&B (POTS) | 15.42 | 128 | | | 51.410 | | | |
| B.2.21.16.2.2 | P-5 | INP (Standalone)/>=10 circuits/Non-Dispatch/GA(hours) | R&B (POTS) | 41.81 | 13 | | | 143.160 | | | |
| B.2.21.17.1.1 | P-5 | LNP (Standalone)/<10 circuits/Dispatch/GA(hours) | R&B (POTS) | 5.81 | 32,200 | 27.48 | 21 | 76.886 | 16.78339 | -1.2913 | YES |
| B.2.21.17.1.2 | P-5 | LNP (Standalone)/<10 circuits/Non-Dispatch/GA(hours) | R&B (POTS) | 1.51 | 364,555 | 90.54 | 1,846 | 7.786 | 0.18168 | -490.0287 | NO |
| B.2.21.17.2.1 | P-5 | LNP (Standalone)/>=10 circuits/Dispatch/GA(hours) | R&B (POTS) | 15.42 | 128 | | | 51.410 | | | |
| B.2.21.17.2.2 | P-5 | LNP (Standalone)/>=10 circuits/Non-Dispatch/GA(hours) | R&B (POTS) | 41.81 | 13 | 494.95 | 1 | 143.160 | 148.56395 | -3.0502 | NO |
| B.2.21.18.1.1 | P-5 | Digital Loop < DS1/<10 circuits/Dispatch/GA(hours) | Digital Loop < DS1 | 29.95 | 7,480 | 0.02 | 1 | 2020.035 | 2020.16992 | 0.0148 | YES |
| B.2.21.18.1.2 | P-5 | Digital Loop < DS1/<10 circuits/Non-Dispatch/GA(hours) | Digital Loop < DS1 | 1.20 | 3,131 | | | 143.652 | | | |
| B.2.21.18.2.1 | P-5 | Digital Loop < DS1/>=10 circuits/Dispatch/GA(hours) | Digital Loop < DS1 | | | | | | | | |
| B.2.21.18.2.2 | P-5 | Digital Loop < DS1/>=10 circuits/Non-Dispatch/GA(hours) | Digital Loop < DS1 | | | | | | | | |
| B.2.21.19.1.1 | P-5 | Digital Loop >= DS1/<10 circuits/Dispatch/GA(hours) | Digital Loop >= DS1 | 561.95 | 188 | 66.56 | 28 | 590.800 | 119.67661 | 4.1394 | YES |
| B.2.21.19.1.2 | P-5 | Digital Loop >= DS1/<10 circuits/Non-Dispatch/GA(hours) | Digital Loop >= DS1 | 51.79 | 293 | | | 153.835 | | | |
| B.2.21.19.2.1 | P-5 | Digital Loop >= DS1/>=10 circuits/Dispatch/GA(hours) | Digital Loop >= DS1 | | | | | | | | |
| B.2.21.19.2.2 | P-5 | Digital Loop >= DS1/>=10 circuits/Non-Dispatch/GA(hours) | Digital Loop >= DS1 | | | | | | | | |
| | | ***Average Completion Notice Interval - Non-Mechanized*** | | | | | | | | | |
| B.2.22.1.1.1 | P-5 | Switch Ports/<10 circuits/Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.1.1.2 | P-5 | Switch Ports/<10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.1.2.1 | P-5 | Switch Ports/>=10 circuits/Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.1.2.2 | P-5 | Switch Ports/>=10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.2.1.1 | P-5 | Local Interoffice Transport/<10 circuits/Dispatch/GA(hours) | Diagnostic | | | 64.88 | 1 | | | | Diagnostic |
| B.2.22.2.1.2 | P-5 | Local Interoffice Transport/<10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.2.2.1 | P-5 | Local Interoffice Transport/>=10 circuits/Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.2.2.2 | P-5 | Local Interoffice Transport/>=10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.3.1.1 | P-5 | Loop + Port Combinations/<10 circuits/Dispatch/GA(hours) | Diagnostic | | | 19.64 | 66 | | | | Diagnostic |
| B.2.22.3.1.2 | P-5 | Loop + Port Combinations/<10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | 15.29 | 170 | | | | Diagnostic |
| B.2.22.3.1.3 | P-5 | Loop + Port Combinations/<10 circuits/Switch Based Orders/GA(hours) | Diagnostic | | | 13.81 | 57 | | | | Diagnostic |
| B.2.22.3.1.4 | P-5 | Loop + Port Combinations/<10 circuits/Dispatch In/GA(hours) | Diagnostic | | | 16.04 | 113 | | | | Diagnostic |

## BellSouth Monthly State Summary

*Georgia, October 2001*

| | | | Benchmark / Analog | BST Measure | BST Volume | CLEC Measure | CLEC Volume | Standard Deviation | Standard Error | ZScore | Equity |
|---|---|---|---|---|---|---|---|---|---|---|---|
| B.2.22.3.2.1 | P-5 | Loop + Port Combinations/>=10 circuits/Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.3.2.2 | P-5 | Loop + Port Combinations/>=10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | 14.00 | 1 | | | | Diagnostic |
| B.2.22.3.2.3 | P-5 | Loop + Port Combinations/>=10 circuits/Switch Based Orders/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.3.2.4 | P-5 | Loop + Port Combinations/>=10 circuits/Dispatch In/GA(hours) | Diagnostic | | | 14.00 | 1 | | | | Diagnostic |
| B.2.22.4.1.1 | P-5 | Combo Other/<10 circuits/Dispatch/GA(hours) | Diagnostic | | | 75.32 | 182 | | | | Diagnostic |
| B.2.22.4.1.4 | P-5 | Combo Other/<10 circuits/Dispatch In/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.4.2.1 | P-5 | Combo Other/>=10 circuits/Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.4.2.4 | P-5 | Combo Other/>=10 circuits/Dispatch In/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.5.1.1 | P-5 | xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/GA(hours) | Diagnostic | | | 32.54 | 140 | | | | Diagnostic |
| B.2.22.5.1.2 | P-5 | xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | 17.53 | 3 | | | | Diagnostic |
| B.2.22.5.2.1 | P-5 | xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.5.2.2 | P-5 | xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.6.1.1 | P-5 | UNE ISDN/<10 circuits/Dispatch/GA(hours) | Diagnostic | | | 39.14 | 145 | | | | Diagnostic |
| B.2.22.6.1.2 | P-5 | UNE ISDN/<10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.6.2.1 | P-5 | UNE ISDN/>=10 circuits/Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.6.2.2 | P-5 | UNE ISDN/>=10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.7.1.1 | P-5 | Line Sharing/<10 circuits/Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.7.1.2 | P-5 | Line Sharing/<10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | 14.00 | 1 | | | | Diagnostic |
| B.2.22.7.2.1 | P-5 | Line Sharing/>=10 circuits/Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.7.2.2 | P-5 | Line Sharing/>=10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.8.1.1 | P-5 | 2W Analog Loop Design/<10 circuits/Dispatch/GA(hours) | Diagnostic | | | 22.32 | 6 | | | | Diagnostic |
| B.2.22.8.1.2 | P-5 | 2W Analog Loop Design/<10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.8.2.1 | P-5 | 2W Analog Loop Design/>=10 circuits/Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.8.2.2 | P-5 | 2W Analog Loop Design/>=10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.9.1.1 | P-5 | 2W Analog Loop Non-Design/<10 circuits/Dispatch/GA(hours) | Diagnostic | | | 17.61 | 28 | | | | Diagnostic |
| B.2.22.9.1.4 | P-5 | 2W Analog Loop Non-Design/<10 circuits/Dispatch In/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.9.2.1 | P-5 | 2W Analog Loop Non-Design/>=10 circuits/Dispatch/GA(hours) | Diagnostic | | | 17.40 | 1 | | | | Diagnostic |
| B.2.22.9.2.4 | P-5 | 2W Analog Loop Non-Design/>=10 circuits/Dispatch In/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.10.1.1 | P-5 | 2W Analog Loop w/INP Design/<10 circuits/Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.10.1.2 | P-5 | 2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.10.2.1 | P-5 | 2W Analog Loop w/INP Design/>=10 circuits/Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.10.2.2 | P-5 | 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.11.1.1 | P-5 | 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.11.1.4 | P-5 | 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.11.2.1 | P-5 | 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.11.2.4 | P-5 | 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.12.1.1 | P-5 | 2W Analog Loop w/LNP Design/<10 circuits/Dispatch/GA(hours) | Diagnostic | | | 33.75 | 53 | | | | Diagnostic |
| B.2.22.12.1.2 | P-5 | 2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.12.2.1 | P-5 | 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.12.2.2 | P-5 | 2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.13.1.1 | P-5 | 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/GA(hours) | Diagnostic | | | 15.90 | 108 | | | | Diagnostic |
| B.2.22.13.1.4 | P-5 | 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.13.2.1 | P-5 | 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/GA(hours) | Diagnostic | | | 15.39 | 5 | | | | Diagnostic |
| B.2.22.13.2.4 | P-5 | 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.14.1.1 | P-5 | Other Design/<10 circuits/Dispatch/GA(hours) | Diagnostic | | | 32.03 | 21 | | | | Diagnostic |
| B.2.22.14.1.2 | P-5 | Other Design/<10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.14.2.1 | P-5 | Other Design/>=10 circuits/Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.14.2.2 | P-5 | Other Design/>=10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.15.1.1 | P-5 | Other Non-Design/<10 circuits/Dispatch/GA(hours) | Diagnostic | | | 18.37 | 52 | | | | Diagnostic |
| B.2.22.15.1.2 | P-5 | Other Non-Design/<10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | 16.67 | 2 | | | | Diagnostic |
| B.2.22.15.2.1 | P-5 | Other Non-Design/>=10 circuits/Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.15.2.2 | P-5 | Other Non-Design/>=10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.16.1.1 | P-5 | INP (Standalone)/<10 circuits/Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.16.1.2 | P-5 | INP (Standalone)/<10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | 14.00 | 1 | | | | Diagnostic |
| B.2.22.16.2.1 | P-5 | INP (Standalone)/>=10 circuits/Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.16.2.2 | P-5 | INP (Standalone)/>=10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.17.1.1 | P-5 | LNP (Standalone)/<10 circuits/Dispatch/GA(hours) | Diagnostic | | | 13.19 | 32 | | | | Diagnostic |
| B.2.22.17.1.2 | P-5 | LNP (Standalone)/<10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | 21.74 | 878 | | | | Diagnostic |
| B.2.22.17.2.1 | P-5 | LNP (Standalone)/>=10 circuits/Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.17.2.2 | P-5 | LNP (Standalone)/>=10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | 10.38 | 3 | | | | Diagnostic |
| B.2.22.18.1.1 | P-5 | Digital Loop < DS1/<10 circuits/Dispatch/GA(hours) | Diagnostic | | | 37.28 | 207 | | | | Diagnostic |

# BellSouth Monthly State Summary

*Georgia, October 2001*

| | | | Benchmark / Analog | BST Measure | BST Volume | CLEC Measure | CLEC Volume | Standard Deviation | Standard Error | ZScore | Equity |
|---|---|---|---|---|---|---|---|---|---|---|---|
| B.2.22.18.1.2 | P-5 | Digital Loop < DS1/<10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.18.2.1 | P-5 | Digital Loop < DS1/>=10 circuits/Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.18.2.2 | P-5 | Digital Loop < DS1/>=10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.19.1.1 | P-5 | Digital Loop >= DS1/<10 circuits/Dispatch/GA(hours) | Diagnostic | | | 62.49 | 92 | | | | Diagnostic |
| B.2.22.19.1.2 | P-5 | Digital Loop >= DS1/<10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.19.2.1 | P-5 | Digital Loop >= DS1/>=10 circuits/Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| B.2.22.19.2.2 | P-5 | Digital Loop >= DS1/>=10 circuits/Non-Dispatch/GA(hours) | Diagnostic | | | | | | | | Diagnostic |
| | ***Total Service Order Cycle Time - Mechanized*** | | | | | | | | | | |
| B.2.24.1.1.1 | P-10 | Switch Ports/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.1.1.2 | P-10 | Switch Ports/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.1.2.1 | P-10 | Switch Ports/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.1.2.2 | P-10 | Switch Ports/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.2.1.1 | P-10 | Local Interoffice Transport/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.2.1.2 | P-10 | Local Interoffice Transport/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.2.2.1 | P-10 | Local Interoffice Transport/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.2.2.2 | P-10 | Local Interoffice Transport/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.3.1.1 | P-10 | Loop + Port Combinations/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 5.74 | 408 | | | | Diagnostic |
| B.2.24.3.1.2 | P-10 | Loop + Port Combinations/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | 0.99 | 10,015 | | | | Diagnostic |
| B.2.24.3.2.1 | P-10 | Loop + Port Combinations/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | 4.00 | 4 | | | | Diagnostic |
| B.2.24.3.2.2 | P-10 | Loop + Port Combinations/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.4.1.1 | P-10 | Combo Other/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.4.1.2 | P-10 | Combo Other/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.4.2.1 | P-10 | Combo Other/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.4.2.2 | P-10 | Combo Other/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.5.1.1 | P-10 | xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 3.74 | 41 | | | | Diagnostic |
| B.2.24.5.1.2 | P-10 | xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.5.2.1 | P-10 | xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.5.2.2 | P-10 | xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.6.1.1 | P-10 | UNE ISDN/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.6.1.2 | P-10 | UNE ISDN/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.6.2.1 | P-10 | UNE ISDN/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.6.2.2 | P-10 | UNE ISDN/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.7.1.1 | P-10 | Line Sharing/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.7.1.2 | P-10 | Line Sharing/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.7.2.1 | P-10 | Line Sharing/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.7.2.2 | P-10 | Line Sharing/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.8.1.1 | P-10 | 2W Analog Loop Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 6.17 | 6 | | | | Diagnostic |
| B.2.24.8.1.2 | P-10 | 2W Analog Loop Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.8.2.1 | P-10 | 2W Analog Loop Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.8.2.2 | P-10 | 2W Analog Loop Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.9.1.1 | P-10 | 2W Analog Loop Non-Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 6.50 | 6 | | | | Diagnostic |
| B.2.24.9.1.2 | P-10 | 2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.9.2.1 | P-10 | 2W Analog Loop Non-Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.9.2.2 | P-10 | 2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.10.1.1 | P-10 | 2W Analog Loop w/INP Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.10.1.2 | P-10 | 2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.10.2.1 | P-10 | 2W Analog Loop w/INP Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.10.2.2 | P-10 | 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.11.1.1 | P-10 | 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.11.1.2 | P-10 | 2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.11.2.1 | P-10 | 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.11.2.2 | P-10 | 2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.12.1.1 | P-14 | 2W Analog Loop w/LNP Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 8.00 | 4 | | | | Diagnostic |
| B.2.24.12.1.2 | P-14 | 2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.12.2.1 | P-14 | 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.12.2.2 | P-14 | 2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.13.1.1 | P-14 | 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.13.1.2 | P-14 | 2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | 5.00 | 1 | | | | Diagnostic |
| B.2.24.13.2.1 | P-14 | 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.13.2.2 | P-14 | 2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |

# BellSouth Monthly State Summary

## *Georgia, October 2001*

| | | | Benchmark / Analog | BST Measure | BST Volume | CLEC Measure | CLEC Volume | Standard Deviation | Standard Error | ZScore | Equity |
|---|---|---|---|---|---|---|---|---|---|---|---|
| B.2.24.14.1.1 | P-10 | Other Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 5.93 | 14 | | | | Diagnostic |
| B.2.24.14.1.2 | P-10 | Other Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.14.2.1 | P-10 | Other Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.14.2.2 | P-10 | Other Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.15.1.1 | P-10 | Other Non-Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 5.53 | 15 | | | | Diagnostic |
| B.2.24.15.1.2 | P-10 | Other Non-Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.15.2.1 | P-10 | Other Non-Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.15.2.2 | P-10 | Other Non-Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.16.1.1 | P-10 | INP (Standalone)/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.16.1.2 | P-10 | INP (Standalone)/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.16.2.1 | P-10 | INP (Standalone)/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.16.2.2 | P-10 | INP (Standalone)/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.17.1.1 | P-14 | LNP (Standalone)/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.17.1.2 | P-14 | LNP (Standalone)/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | 1.91 | 1,780 | | | | Diagnostic |
| B.2.24.17.2.1 | P-14 | LNP (Standalone)/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.17.2.2 | P-14 | LNP (Standalone)/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.18.1.1 | P-10 | Digital Loop < DS1/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.18.1.2 | P-10 | Digital Loop < DS1/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.18.2.1 | P-10 | Digital Loop < DS1/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.18.2.2 | P-10 | Digital Loop < DS1/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.19.1.1 | P-10 | Digital Loop >= DS1/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 6.17 | 24 | | | | Diagnostic |
| B.2.24.19.1.2 | P-10 | Digital Loop >= DS1/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.19.2.1 | P-10 | Digital Loop >= DS1/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.24.19.2.2 | P-10 | Digital Loop >= DS1/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| | ***Total Service Order Cycle Time - Partially Mechanized*** | | | | | | | | | | |
| B.2.25.1.1.1 | P-10 | Switch Ports/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.1.1.2 | P-10 | Switch Ports/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.1.2.1 | P-10 | Switch Ports/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.1.2.2 | P-10 | Switch Ports/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.2.1.1 | P-10 | Local Interoffice Transport/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.2.1.2 | P-10 | Local Interoffice Transport/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.2.2.1 | P-10 | Local Interoffice Transport/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.2.2.2 | P-10 | Local Interoffice Transport/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.3.1.1 | P-10 | Loop + Port Combinations/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 5.15 | 175 | | | | Diagnostic |
| B.2.25.3.1.2 | P-10 | Loop + Port Combinations/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | 1.58 | 4,162 | | | | Diagnostic |
| B.2.25.3.2.1 | P-10 | Loop + Port Combinations/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | 7.33 | 3 | | | | Diagnostic |
| B.2.25.3.2.2 | P-10 | Loop + Port Combinations/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.4.1.1 | P-10 | Combo Other/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.4.1.2 | P-10 | Combo Other/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.4.2.1 | P-10 | Combo Other/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.4.2.2 | P-10 | Combo Other/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.5.1.1 | P-10 | xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.5.1.2 | P-10 | xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.5.2.1 | P-10 | xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.5.2.2 | P-10 | xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.6.1.1 | P-10 | UNE ISDN/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 8.50 | 2 | | | | Diagnostic |
| B.2.25.6.1.2 | P-10 | UNE ISDN/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.6.2.1 | P-10 | UNE ISDN/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.6.2.2 | P-10 | UNE ISDN/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.7.1.1 | P-10 | Line Sharing/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.7.1.2 | P-10 | Line Sharing/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.7.2.1 | P-10 | Line Sharing/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.7.2.2 | P-10 | Line Sharing/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.8.1.1 | P-10 | 2W Analog Loop Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 6.89 | 9 | | | | Diagnostic |
| B.2.25.8.1.2 | P-10 | 2W Analog Loop Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.8.2.1 | P-10 | 2W Analog Loop Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.8.2.2 | P-10 | 2W Analog Loop Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.9.1.1 | P-10 | 2W Analog Loop Non-Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 6.00 | 2 | | | | Diagnostic |
| B.2.25.9.1.2 | P-10 | 2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.9.2.1 | P-10 | 2W Analog Loop Non-Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |

# BellSouth Monthly State Summary

## *Georgia, October 2001*

| | | | Benchmark / Analog | BST Measure | BST Volume | CLEC Measure | CLEC Volume | Standard Deviation | Standard Error | ZScore | Equity |
|---|---|---|---|---|---|---|---|---|---|---|---|
| B.2.25.9.2.2 | P-10 | 2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.10.1.1 | P-10 | 2W Analog Loop w/INP Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.10.1.2 | P-10 | 2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.10.2.1 | P-10 | 2W Analog Loop w/INP Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.10.2.2 | P-10 | 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.11.1.1 | P-10 | 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.11.1.2 | P-10 | 2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.11.2.1 | P-10 | 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.11.2.2 | P-10 | 2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.12.1.1 | P-14 | 2W Analog Loop w/LNP Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 7.12 | 33 | | | | Diagnostic |
| B.2.25.12.1.2 | P-14 | 2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.12.2.1 | P-14 | 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.12.2.2 | P-14 | 2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.13.1.1 | P-14 | 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 6.67 | 52 | | | | Diagnostic |
| B.2.25.13.1.2 | P-14 | 2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | 5.86 | 36 | | | | Diagnostic |
| B.2.25.13.2.1 | P-14 | 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | 8.00 | 1 | | | | Diagnostic |
| B.2.25.13.2.2 | P-14 | 2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | 7.00 | 2 | | | | Diagnostic |
| B.2.25.14.1.1 | P-10 | Other Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 5.78 | 27 | | | | Diagnostic |
| B.2.25.14.1.2 | P-10 | Other Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.14.2.1 | P-10 | Other Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.14.2.2 | P-10 | Other Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.15.1.1 | P-10 | Other Non-Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 5.79 | 14 | | | | Diagnostic |
| B.2.25.15.1.2 | P-10 | Other Non-Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.15.2.1 | P-10 | Other Non-Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | 6.50 | 2 | | | | Diagnostic |
| B.2.25.15.2.2 | P-10 | Other Non-Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.16.1.1 | P-10 | INP (Standalone)/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.16.1.2 | P-10 | INP (Standalone)/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.16.2.1 | P-10 | INP (Standalone)/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.16.2.2 | P-10 | INP (Standalone)/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.17.1.1 | P-14 | LNP (Standalone)/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 21.00 | 1 | | | | Diagnostic |
| B.2.25.17.1.2 | P-14 | LNP (Standalone)/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | 5.01 | 644 | | | | Diagnostic |
| B.2.25.17.2.1 | P-14 | LNP (Standalone)/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.17.2.2 | P-14 | LNP (Standalone)/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | 2.00 | 2 | | | | Diagnostic |
| B.2.25.18.1.1 | P-10 | Digital Loop < DS1/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 8.50 | 2 | | | | Diagnostic |
| B.2.25.18.1.2 | P-10 | Digital Loop < DS1/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.18.2.1 | P-10 | Digital Loop < DS1/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.18.2.2 | P-10 | Digital Loop < DS1/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.19.1.1 | P-10 | Digital Loop >= DS1/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 5.33 | 3 | | | | Diagnostic |
| B.2.25.19.1.2 | P-10 | Digital Loop >= DS1/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.19.2.1 | P-10 | Digital Loop >= DS1/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.25.19.2.2 | P-10 | Digital Loop >= DS1/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| | ***Total Service Order Cycle Time - Non-Mechanized*** | | | | | | | | | | |
| B.2.26.1.1.1 | P-10 | Switch Ports/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.1.1.2 | P-10 | Switch Ports/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.1.2.1 | P-10 | Switch Ports/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.1.2.2 | P-10 | Switch Ports/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.2.1.1 | P-10 | Local Interoffice Transport/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 17.00 | 1 | | | | Diagnostic |
| B.2.26.2.1.2 | P-10 | Local Interoffice Transport/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.2.2.1 | P-10 | Local Interoffice Transport/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.2.2.2 | P-10 | Local Interoffice Transport/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.3.1.1 | P-10 | Loop + Port Combinations/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 5.80 | 45 | | | | Diagnostic |
| B.2.26.3.1.2 | P-10 | Loop + Port Combinations/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | 3.34 | 121 | | | | Diagnostic |
| B.2.26.3.2.1 | P-10 | Loop + Port Combinations/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.3.2.2 | P-10 | Loop + Port Combinations/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | 7.00 | 1 | | | | Diagnostic |
| B.2.26.4.1.1 | P-10 | Combo Other/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 11.73 | 176 | | | | Diagnostic |
| B.2.26.4.1.2 | P-10 | Combo Other/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.4.2.1 | P-10 | Combo Other/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.4.2.2 | P-10 | Combo Other/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.5.1.1 | P-10 | xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 8.18 | 50 | | | | Diagnostic |
| B.2.26.5.1.2 | P-10 | xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |

# BellSouth Monthly State Summary

## *Georgia, October 2001*

| | | | Benchmark / Analog | BST Measure | BST Volume | CLEC Measure | CLEC Volume | Standard Deviation | Standard Error | ZScore | Equity |
|---|---|---|---|---|---|---|---|---|---|---|---|
| B.2.26.5.2.1 | P-10 | xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.5.2.2 | P-10 | xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.6.1.1 | P-10 | UNE ISDN/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 12.41 | 138 | | | | Diagnostic |
| B.2.26.6.1.2 | P-10 | UNE ISDN/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.6.2.1 | P-10 | UNE ISDN/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.6.2.2 | P-10 | UNE ISDN/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.7.1.1 | P-10 | Line Sharing/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.7.1.2 | P-10 | Line Sharing/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.7.2.1 | P-10 | Line Sharing/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.7.2.2 | P-10 | Line Sharing/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.8.1.1 | P-10 | 2W Analog Loop Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 7.25 | 4 | | | | Diagnostic |
| B.2.26.8.1.2 | P-10 | 2W Analog Loop Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.8.2.1 | P-10 | 2W Analog Loop Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.8.2.2 | P-10 | 2W Analog Loop Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.9.1.1 | P-10 | 2W Analog Loop Non-Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 9.00 | 18 | | | | Diagnostic |
| B.2.26.9.1.2 | P-10 | 2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.9.2.1 | P-10 | 2W Analog Loop Non-Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | 8.00 | 1 | | | | Diagnostic |
| B.2.26.9.2.2 | P-10 | 2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.10.1.1 | P-10 | 2W Analog Loop w/INP Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.10.1.2 | P-10 | 2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.10.2.1 | P-10 | 2W Analog Loop w/INP Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.10.2.2 | P-10 | 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.11.1.1 | P-10 | 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.11.1.2 | P-10 | 2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.11.2.1 | P-10 | 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.11.2.2 | P-10 | 2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.12.1.1 | P-14 | 2W Analog Loop w/LNP Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 7.17 | 6 | | | | Diagnostic |
| B.2.26.12.1.2 | P-14 | 2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.12.2.1 | P-14 | 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.12.2.2 | P-14 | 2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.13.1.1 | P-14 | 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 7.36 | 44 | | | | Diagnostic |
| B.2.26.13.1.2 | P-14 | 2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | 6.71 | 35 | | | | Diagnostic |
| B.2.26.13.2.1 | P-14 | 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | 11.00 | 1 | | | | Diagnostic |
| B.2.26.13.2.2 | P-14 | 2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.14.1.1 | P-10 | Other Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 7.63 | 16 | | | | Diagnostic |
| B.2.26.14.1.2 | P-10 | Other Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.14.2.1 | P-10 | Other Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.14.2.2 | P-10 | Other Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.15.1.1 | P-10 | Other Non-Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 7.60 | 35 | | | | Diagnostic |
| B.2.26.15.1.2 | P-10 | Other Non-Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | 7.00 | 1 | | | | Diagnostic |
| B.2.26.15.2.1 | P-10 | Other Non-Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.15.2.2 | P-10 | Other Non-Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.16.1.1 | P-10 | INP (Standalone)/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.16.1.2 | P-10 | INP (Standalone)/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.16.2.1 | P-10 | INP (Standalone)/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.16.2.2 | P-10 | INP (Standalone)/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.17.1.1 | P-14 | LNP (Standalone)/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 4.00 | 1 | | | | Diagnostic |
| B.2.26.17.1.2 | P-14 | LNP (Standalone)/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | 1.67 | 393 | | | | Diagnostic |
| B.2.26.17.2.1 | P-14 | LNP (Standalone)/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.17.2.2 | P-14 | LNP (Standalone)/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | 1.67 | 7 | | | | Diagnostic |
| B.2.26.18.1.1 | P-10 | Digital Loop < DS1/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 10.78 | 194 | | | | Diagnostic |
| B.2.26.18.1.2 | P-10 | Digital Loop < DS1/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.18.2.1 | P-10 | Digital Loop < DS1/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.18.2.2 | P-10 | Digital Loop < DS1/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.19.1.1 | P-10 | Digital Loop >= DS1/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 7.42 | 62 | | | | Diagnostic |
| B.2.26.19.1.2 | P-10 | Digital Loop >= DS1/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.19.2.1 | P-10 | Digital Loop >= DS1/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.26.19.2.2 | P-10 | Digital Loop >= DS1/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| | | ***Total Service Order Cycle Time (offered) - Mechanized*** | | | | | | | | | |
| B.2.28.1.1.1 | P-10 | Switch Ports/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |

# BellSouth Monthly State Summary

## *Georgia, October 2001*

| | | | Benchmark / Analog | BST Measure | BST Volume | CLEC Measure | CLEC Volume | Standard Deviation | Standard Error | ZScore | Equity |
|---|---|---|---|---|---|---|---|---|---|---|---|
| B.2.28.1.1.2 | P-10 | Switch Ports/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.1.2.1 | P-10 | Switch Ports/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.1.2.2 | P-10 | Switch Ports/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.2.1.1 | P-10 | Local Interoffice Transport/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.2.1.2 | P-10 | Local Interoffice Transport/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.2.2.1 | P-10 | Local Interoffice Transport/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.2.2.2 | P-10 | Local Interoffice Transport/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.3.1.1 | P-10 | Loop + Port Combinations/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 5.64 | 378 | | | | Diagnostic |
| B.2.28.3.1.2 | P-10 | Loop + Port Combinations/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | 1.27 | 5,686 | | | | Diagnostic |
| B.2.28.3.2.1 | P-10 | Loop + Port Combinations/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | 4.00 | 4 | | | | Diagnostic |
| B.2.28.3.2.2 | P-10 | Loop + Port Combinations/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.4.1.1 | P-10 | Combo Other/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.4.1.2 | P-10 | Combo Other/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.4.2.1 | P-10 | Combo Other/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.4.2.2 | P-10 | Combo Other/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.5.1.1 | P-10 | xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 3.74 | 41 | | | | Diagnostic |
| B.2.28.5.1.2 | P-10 | xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.5.2.1 | P-10 | xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.5.2.2 | P-10 | xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.6.1.1 | P-10 | UNE ISDN/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.6.1.2 | P-10 | UNE ISDN/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.6.2.1 | P-10 | UNE ISDN/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.6.2.2 | P-10 | UNE ISDN/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.7.1.1 | P-10 | Line Sharing/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.7.1.2 | P-10 | Line Sharing/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.7.2.1 | P-10 | Line Sharing/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.7.2.2 | P-10 | Line Sharing/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.8.1.1 | P-10 | 2W Analog Loop Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 6.17 | 6 | | | | Diagnostic |
| B.2.28.8.1.2 | P-10 | 2W Analog Loop Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.8.2.1 | P-10 | 2W Analog Loop Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.8.2.2 | P-10 | 2W Analog Loop Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.9.1.1 | P-10 | 2W Analog Loop Non-Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 6.50 | 6 | | | | Diagnostic |
| B.2.28.9.1.2 | P-10 | 2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.9.2.1 | P-10 | 2W Analog Loop Non-Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.9.2.2 | P-10 | 2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.10.1.1 | P-10 | 2W Analog Loop w/INP Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.10.1.2 | P-10 | 2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.10.2.1 | P-10 | 2W Analog Loop w/INP Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.10.2.2 | P-10 | 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.11.1.1 | P-10 | 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.11.1.2 | P-10 | 2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.11.2.1 | P-10 | 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.11.2.2 | P-10 | 2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.12.1.1 | P-14 | 2W Analog Loop w/LNP Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 8.00 | 4 | | | | Diagnostic |
| B.2.28.12.1.2 | P-14 | 2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.12.2.1 | P-14 | 2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.12.2.2 | P-14 | 2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.13.1.1 | P-14 | 2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.13.1.2 | P-14 | 2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | 5.00 | 1 | | | | Diagnostic |
| B.2.28.13.2.1 | P-14 | 2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.13.2.2 | P-14 | 2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.14.1.1 | P-10 | Other Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 6.00 | 12 | | | | Diagnostic |
| B.2.28.14.1.2 | P-10 | Other Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.14.2.1 | P-10 | Other Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.14.2.2 | P-10 | Other Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.15.1.1 | P-10 | Other Non-Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 5.53 | 15 | | | | Diagnostic |
| B.2.28.15.1.2 | P-10 | Other Non-Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.15.2.1 | P-10 | Other Non-Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.15.2.2 | P-10 | Other Non-Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.16.1.1 | P-10 | INP (Standalone)/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.16.1.2 | P-10 | INP (Standalone)/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |

# BellSouth Monthly State Summary

*Georgia, October 2001*

| | | | Benchmark / Analog | BST Measure | BST Volume | CLEC Measure | CLEC Volume | Standard Deviation | Standard Error | ZScore | Equity |
|---|---|---|---|---|---|---|---|---|---|---|---|
| B.2.28.16.2.1 | P-10 | INP (Standalone)/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.16.2.2 | P-10 | INP (Standalone)/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.17.1.1 | P-14 | LNP (Standalone)/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.17.1.2 | P-14 | LNP (Standalone)/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | 1.91 | 1,780 | | | | Diagnostic |
| B.2.28.17.2.1 | P-14 | LNP (Standalone)/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.17.2.2 | P-14 | LNP (Standalone)/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.18.1.1 | P-10 | Digital Loop < DS1/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.18.1.2 | P-10 | Digital Loop < DS1/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.18.2.1 | P-10 | Digital Loop < DS1/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.18.2.2 | P-10 | Digital Loop < DS1/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.19.1.1 | P-10 | Digital Loop >= DS1/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 6.17 | 24 | | | | Diagnostic |
| B.2.28.19.1.2 | P-10 | Digital Loop >= DS1/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.19.2.1 | P-10 | Digital Loop >= DS1/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.28.19.2.2 | P-10 | Digital Loop >= DS1/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| | ***Total Service Order Cycle Time (offered) - Partially Mechanized*** | | | | | | | | | | |
| B.2.29.1.1.1 | P-10 | Switch Ports/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.1.1.2 | P-10 | Switch Ports/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.1.2.1 | P-10 | Switch Ports/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.1.2.2 | P-10 | Switch Ports/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.2.1.1 | P-10 | Local Interoffice Transport/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.2.1.2 | P-10 | Local Interoffice Transport/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.2.2.1 | P-10 | Local Interoffice Transport/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.2.2.2 | P-10 | Local Interoffice Transport/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.3.1.1 | P-10 | Loop + Port Combinations/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 5.12 | 155 | | | | Diagnostic |
| B.2.29.3.1.2 | P-10 | Loop + Port Combinations/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | 1.53 | 2,848 | | | | Diagnostic |
| B.2.29.3.2.1 | P-10 | Loop + Port Combinations/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | 7.33 | 3 | | | | Diagnostic |
| B.2.29.3.2.2 | P-10 | Loop + Port Combinations/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.4.1.1 | P-10 | Combo Other/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.4.1.2 | P-10 | Combo Other/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.4.2.1 | P-10 | Combo Other/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.4.2.2 | P-10 | Combo Other/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.5.1.1 | P-10 | xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.5.1.2 | P-10 | xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.5.2.1 | P-10 | xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.5.2.2 | P-10 | xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.6.1.1 | P-10 | UNE ISDN/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 8.50 | 2 | | | | Diagnostic |
| B.2.29.6.1.2 | P-10 | UNE ISDN/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.6.2.1 | P-10 | UNE ISDN/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.6.2.2 | P-10 | UNE ISDN/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.7.1.1 | P-10 | Line Sharing/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.7.1.2 | P-10 | Line Sharing/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.7.2.1 | P-10 | Line Sharing/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.7.2.2 | P-10 | Line Sharing/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.8.1.1 | P-10 | 2W Analog Loop Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 6.89 | 9 | | | | Diagnostic |
| B.2.29.8.1.2 | P-10 | 2W Analog Loop Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.8.2.1 | P-10 | 2W Analog Loop Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.8.2.2 | P-10 | 2W Analog Loop Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.9.1.1 | P-10 | 2W Analog Loop Non-Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 5.00 | 1 | | | | Diagnostic |
| B.2.29.9.1.2 | P-10 | 2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.9.2.1 | P-10 | 2W Analog Loop Non-Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.9.2.2 | P-10 | 2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.10.1.1 | P-10 | 2W Analog Loop w/INP Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.10.1.2 | P-10 | 2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.10.2.1 | P-10 | 2W Analog Loop w/INP Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.10.2.2 | P-10 | 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.11.1.1 | P-10 | 2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.11.1.2 | P-10 | 2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.11.2.1 | P-10 | 2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.11.2.2 | P-10 | 2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/GA(days) | Diagnostic | | | | | | | | Diagnostic |
| B.2.29.12.1.1 | P-14 | 2W Analog Loop w/LNP Design/<10 circuits/Dispatch/GA(days) | Diagnostic | | | 7.26 | 31 | | | | Diagnostic |