BellSouth Monthly Performance Summary

Florida Interim, August 2002 - May 2002

			Benchmark/ Analog	August BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
	Unbundled Network Elements - Ordering										
	% Rejected Service Requests - Mechanized										
B.1.1.1	O-7	Switch Ports/FL (%)	Diagnostic								Diagnostic
B.1.1.2	O-7	Local Interoffice Transport/FL (%)	Diagnostic								Diagnostic
B.1.1.3	O-7	Loop + Port Combinations/FL (%)	Diagnostic			16.23%	81,764				Diagnostic
B.1.1.4	O-7	Combo Other/FL (%)	Diagnostic			38.46%	13				Diagnostic
B.1.1.5	O-7	xDSL (ADSL, HDSL and UCL)/FL (%)	Diagnostic			25.42%	708				Diagnostic
B.1.1.6	O-7	ISDN Loop (UDN, UDC)/FL (%)	Diagnostic			5.94%	219				Diagnostic
B.1.1.7	O-7	Line Sharing/FL (%)	Diagnostic			30.23%	258				Diagnostic
B.1.1.8	O-7	2W Analog Loop Design/FL (%)	Diagnostic			19.67%	1,037				Diagnostic
B.1.1.9	O-7	2W Analog Loop Non-Design/FL (%)	Diagnostic			14.19%	1,008				Diagnostic
B.1.1.10	O-7	2W Analog Loop w/INP Design/FL (%)	Diagnostic								Diagnostic
B.1.1.11	O-7	2W Analog Loop w/INP Non-Design/FL (%)	Diagnostic								Diagnostic
B.1.1.12	O-13	2W Analog Loop w/LNP Design/FL (%)	Diagnostic			62.86%	35				Diagnostic
B.1.1.13	O-13	2W Analog Loop w/LNP Non-Design/FL (%)	Diagnostic			71.94%	139				Diagnostic
B.1.1.14	O-7	Other Design/FL (%)	Diagnostic			26.59%	346				Diagnostic
B.1.1.15	O-7	Other Non-Design/FL (%)	Diagnostic			29.55%	15,960				Diagnostic
B.1.1.16	O-7	INP Standalone/FL (%)	Diagnostic			100.00%	1				Diagnostic
B.1.1.17	O-13	LNP Standalone/FL (%)	Diagnostic			9.84%	3,618				Diagnostic
	% Rejected Service Requests - Partially Mechanized										
B.1.2.1	O-7	Switch Ports/FL (%)	Diagnostic								Diagnostic
B.1.2.2	O-7	Local Interoffice Transport/FL (%)	Diagnostic								Diagnostic
B.1.2.3	O-7	Loop + Port Combinations/FL (%)	Diagnostic			31.03%	15,887				Diagnostic
B.1.2.4	O-7	Combo Other/FL (%)	Diagnostic			16.95%	59				Diagnostic
B.1.2.5	O-7	xDSL (ADSL, HDSL and UCL)/FL (%)	Diagnostic			13.33%	30				Diagnostic
B.1.2.6	O-7	ISDN Loop (UDN, UDC)/FL (%)	Diagnostic			0.00%	23				Diagnostic
B.1.2.7	O-7	Line Sharing/FL (%)	Diagnostic			44.00%	75				Diagnostic
B.1.2.8	O-7	2W Analog Loop Design/FL (%)	Diagnostic			24.79%	238				Diagnostic
B.1.2.9	O-7	2W Analog Loop Non-Design/FL (%)	Diagnostic			11.10%	1,693				Diagnostic
B.1.2.10	O-7	2W Analog Loop w/INP Design/FL (%)	Diagnostic								Diagnostic
B.1.2.11	O-7	2W Analog Loop w/INP Non-Design/FL (%)	Diagnostic								Diagnostic
B.1.2.12	O-13	2W Analog Loop w/LNP Design/FL (%)	Diagnostic			31.03%	203				Diagnostic
B.1.2.13	O-13	2W Analog Loop w/LNP Non-Design/FL (%)	Diagnostic			21.44%	1,138				Diagnostic
B.1.2.14	O-7	Other Design/FL (%)	Diagnostic			25.94%	347				Diagnostic
B.1.2.15	O-7	Other Non-Design/FL (%)	Diagnostic			37.51%	7,917				Diagnostic
B.1.2.16	O-7	INP Standalone/FL (%)	Diagnostic								Diagnostic
B.1.2.17	O-13	LNP Standalone/FL (%)	Diagnostic			34.77%	1,504				Diagnostic
	% Rejected Service Requests - Non-Mechanized										
B.1.3.1	O-7	Switch Ports/FL (%)	Diagnostic								Diagnostic
B.1.3.2	O-7	Local Interoffice Transport/FL (%)	Diagnostic			61.54%	39				Diagnostic
B.1.3.3	O-7	Loop + Port Combinations/FL (%)	Diagnostic			38.75%	3,004				Diagnostic
B.1.3.4	O-7	Combo Other/FL (%)	Diagnostic			45.03%	191				Diagnostic
B.1.3.5	O-7	xDSL (ADSL, HDSL and UCL)/FL (%)	Diagnostic			25.00%	88				Diagnostic
B.1.3.6	O-7	ISDN Loop (UDN, UDC)/FL (%)	Diagnostic			21.28%	296				Diagnostic
B.1.3.7	O-7	Line Sharing/FL (%)	Diagnostic			50.00%	28				Diagnostic
B.1.3.8	O-7	2W Analog Loop Design/FL (%)	Diagnostic			42.53%	174				Diagnostic
B.1.3.9	O-7	2W Analog Loop Non-Design/FL (%)	Diagnostic			37.50%	472				Diagnostic
B.1.3.10	O-7	2W Analog Loop w/INP Design/FL (%)	Diagnostic			0.00%	6				Diagnostic
B.1.3.11	O-7	2W Analog Loop w/INP Non-Design/FL (%)	Diagnostic			0.00%	4				Diagnostic
B.1.3.12	O-13	2W Analog Loop w/LNP Design/FL (%)	Diagnostic			64.10%	39				Diagnostic
B.1.3.13	O-13	2W Analog Loop w/LNP Non-Design/FL (%)	Diagnostic			46.94%	49				Diagnostic
B.1.3.14	O-7	Other Design/FL (%)	Diagnostic			45.64%	401				Diagnostic
B.1.3.15	O-7	Other Non-Design/FL (%)	Diagnostic			30.34%	1,381				Diagnostic
B.1.3.16	O-7	INP Standalone/FL (%)	Diagnostic			36.36%	44				Diagnostic
B.1.3.17	O-13	LNP Standalone/FL (%)	Diagnostic			33.48%	914				Diagnostic
	Reject Interval - Mechanized										
B.1.4.1	O-8	Switch Ports/FL (%)	>= 97% w in 1 hr								
B.1.4.2	O-8	Local Interoffice Transport/FL (%)	>= 97% w in 1 hr								
B.1.4.3	O-8	Loop + Port Combinations/FL (%)	>= 97% w in 1 hr			96.39%	13,335				NO
B.1.4.4	O-8	Combo Other/FL (%)	>= 97% w in 1 hr			80.00%	5				NO
B.1.4.5	O-8	xDSL (ADSL, HDSL and UCL)/FL (%)	>= 97% w in 1 hr			97.78%	180				YES
B.1.4.6	O-8	ISDN Loop (UDN, UDC)/FL (%)	>= 97% w in 1 hr			76.92%	13				NO
B.1.4.7	O-8	Line Sharing/FL (%)	>= 97% w in 1 hr			56.96%	79				NO

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - May 2002

			July								June				
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation
	Unbundled Network Elements - Ordering														
	% Rejected Service Requests - Mechanized														
B.1.1.1	O-7	Switch Ports/FL (%)								Diagnostic					
B.1.1.2	O-7	Local Interoffice Transport/FL (%)								Diagnostic					
B.1.1.3	O-7	Loop + Port Combinations/FL (%)			11.93%	76,239				Diagnostic			15.14%	56,934	
B.1.1.4	O-7	Combo Other/FL (%)			100.00%	2				Diagnostic			21.82%	55	
B.1.1.5	O-7	xDSL (ADSL, HDSL and UCL)/FL (%)			20.63%	795				Diagnostic			14.51%	1,006	
B.1.1.6	O-7	ISDN Loop (UDN, UDC)/FL (%)			9.74%	349				Diagnostic			9.86%	71	
B.1.1.7	O-7	Line Sharing/FL (%)			23.66%	224				Diagnostic			22.76%	268	
B.1.1.8	O-7	2W Analog Loop Design/FL (%)			15.67%	970				Diagnostic			22.65%	830	
B.1.1.9	O-7	2W Analog Loop Non-Design/FL (%)			10.15%	1,399				Diagnostic			16.07%	952	
B.1.1.10	O-7	2W Analog Loop w/INP Design/FL (%)								Diagnostic					
B.1.1.11	O-7	2W Analog Loop w/INP Non-Design/FL (%)								Diagnostic					
B.1.1.12	O-13	2W Analog Loop w/LNP Design/FL (%)			69.57%	23				Diagnostic			44.19%	43	
B.1.1.13	O-13	2W Analog Loop w/LNP Non-Design/FL (%)			98.97%	97				Diagnostic			97.09%	103	
B.1.1.14	O-7	Other Design/FL (%)			27.52%	258				Diagnostic			16.39%	299	
B.1.1.15	O-7	Other Non-Design/FL (%)			42.77%	15,881				Diagnostic			48.16%	12,756	
B.1.1.16	O-7	INP Standalone/FL (%)			50.00%	2				Diagnostic			100.00%	1	
B.1.1.17	O-13	LNP Standalone/FL (%)			9.55%	3,309				Diagnostic			9.14%	3,029	
	% Rejected Service Requests - Partially Mechanized														
B.1.2.1	O-7	Switch Ports/FL (%)								Diagnostic					
B.1.2.2	O-7	Local Interoffice Transport/FL (%)								Diagnostic					
B.1.2.3	O-7	Loop + Port Combinations/FL (%)			28.06%	15,130				Diagnostic			21.52%	17,473	
B.1.2.4	O-7	Combo Other/FL (%)			5.00%	40				Diagnostic			33.33%	6	
B.1.2.5	O-7	xDSL (ADSL, HDSL and UCL)/FL (%)			3.92%	51				Diagnostic			10.34%	29	
B.1.2.6	O-7	ISDN Loop (UDN, UDC)/FL (%)			2.56%	39				Diagnostic			26.67%	30	
B.1.2.7	O-7	Line Sharing/FL (%)			30.30%	66				Diagnostic			40.00%	75	
B.1.2.8	O-7	2W Analog Loop Design/FL (%)			23.05%	347				Diagnostic			22.63%	433	
B.1.2.9	O-7	2W Analog Loop Non-Design/FL (%)			13.02%	1,574				Diagnostic			16.80%	1,393	
B.1.2.10	O-7	2W Analog Loop w/INP Design/FL (%)								Diagnostic					
B.1.2.11	O-7	2W Analog Loop w/INP Non-Design/FL (%)								Diagnostic					
B.1.2.12	O-13	2W Analog Loop w/LNP Design/FL (%)			35.28%	309				Diagnostic			32.98%	376	
B.1.2.13	O-13	2W Analog Loop w/LNP Non-Design/FL (%)			22.45%	1,096				Diagnostic			23.26%	1,419	
B.1.2.14	O-7	Other Design/FL (%)			24.60%	374				Diagnostic			16.78%	286	
B.1.2.15	O-7	Other Non-Design/FL (%)			37.28%	10,040				Diagnostic			41.21%	7,238	
B.1.2.16	O-7	INP Standalone/FL (%)								Diagnostic					
B.1.2.17	O-13	LNP Standalone/FL (%)			30.95%	1,596				Diagnostic			29.47%	1,707	
	% Rejected Service Requests - Non-Mechanized														
B.1.3.1	O-7	Switch Ports/FL (%)								Diagnostic			0.00%	1	
B.1.3.2	O-7	Local Interoffice Transport/FL (%)			57.58%	33				Diagnostic			11.11%	9	
B.1.3.3	O-7	Loop + Port Combinations/FL (%)			38.57%	3,023				Diagnostic			41.32%	2,287	
B.1.3.4	O-7	Combo Other/FL (%)			47.27%	110				Diagnostic			51.20%	125	
B.1.3.5	O-7	xDSL (ADSL, HDSL and UCL)/FL (%)			20.00%	100				Diagnostic			25.91%	274	
B.1.3.6	O-7	ISDN Loop (UDN, UDC)/FL (%)			20.96%	439				Diagnostic			19.19%	344	
B.1.3.7	O-7	Line Sharing/FL (%)			25.00%	48				Diagnostic			35.85%	53	
B.1.3.8	O-7	2W Analog Loop Design/FL (%)			37.82%	156				Diagnostic			27.38%	168	
B.1.3.9	O-7	2W Analog Loop Non-Design/FL (%)			39.12%	340				Diagnostic			25.93%	671	
B.1.3.10	O-7	2W Analog Loop w/INP Design/FL (%)			69.23%	13				Diagnostic					
B.1.3.11	O-7	2W Analog Loop w/INP Non-Design/FL (%)			25.00%	12				Diagnostic			44.44%	9	
B.1.3.12	O-13	2W Analog Loop w/LNP Design/FL (%)			52.94%	17				Diagnostic			62.16%	37	
B.1.3.13	O-13	2W Analog Loop w/LNP Non-Design/FL (%)			49.09%	55				Diagnostic			43.18%	44	
B.1.3.14	O-7	Other Design/FL (%)			38.81%	353				Diagnostic			35.35%	396	
B.1.3.15	O-7	Other Non-Design/FL (%)			28.52%	1,676				Diagnostic			31.56%	1,752	
B.1.3.16	O-7	INP Standalone/FL (%)			26.67%	60				Diagnostic			35.62%	73	
B.1.3.17	O-13	LNP Standalone/FL (%)			30.32%	818				Diagnostic			40.32%	930	
	Reject Interval - Mechanized														
B.1.4.1	O-8	Switch Ports/FL (%)													
B.1.4.2	O-8	Local Interoffice Transport/FL (%)													
B.1.4.3	O-8	Loop + Port Combinations/FL (%)			93.13%	9,200				NO			95.49%	8,651	
B.1.4.4	O-8	Combo Other/FL (%)			100.00%	2				YES			83.33%	12	
B.1.4.5	O-8	xDSL (ADSL, HDSL and UCL)/FL (%)			98.78%	164				YES			98.63%	146	
B.1.4.6	O-8	ISDN Loop (UDN, UDC)/FL (%)			85.71%	35				NO			55.56%	9	
B.1.4.7	O-8	Line Sharing/FL (%)			79.25%	53				NO			69.35%	62	

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - May 2002

			Standard Error	Zscore	Equity	May BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
	Unbundled Network Elements - Ordering												
	% Rejected Service Requests - Mechanized												
B.1.1.1	O-7	Switch Ports/FL (%)			Diagnostic								Diagnostic
B.1.1.2	O-7	Local Interoffice Transport/FL (%)			Diagnostic								Diagnostic
B.1.1.3	O-7	Loop + Port Combinations/FL (%)			Diagnostic			17.65%	44,624				Diagnostic
B.1.1.4	O-7	Combo Other/FL (%)			Diagnostic								Diagnostic
B.1.1.5	O-7	xDSL (ADSL, HDSL and UCL)/FL (%)			Diagnostic			15.72%	598				Diagnostic
B.1.1.6	O-7	ISDN Loop (UDN, UDC)/FL (%)			Diagnostic			13.33%	75				Diagnostic
B.1.1.7	O-7	Line Sharing/FL (%)			Diagnostic			34.69%	245				Diagnostic
B.1.1.8	O-7	2W Analog Loop Design/FL (%)			Diagnostic			22.90%	930				Diagnostic
B.1.1.9	O-7	2W Analog Loop Non-Design/FL (%)			Diagnostic			17.82%	943				Diagnostic
B.1.1.10	O-7	2W Analog Loop w/INP Design/FL (%)			Diagnostic								Diagnostic
B.1.1.11	O-7	2W Analog Loop w/INP Non-Design/FL (%)			Diagnostic			100.00%	3				Diagnostic
B.1.1.12	O-13	2W Analog Loop w/LNP Design/FL (%)			Diagnostic			55.96%	109				Diagnostic
B.1.1.13	O-13	2W Analog Loop w/LNP Non-Design/FL (%)			Diagnostic			59.14%	257				Diagnostic
B.1.1.14	O-7	Other Design/FL (%)			Diagnostic			22.39%	335				Diagnostic
B.1.1.15	O-7	Other Non-Design/FL (%)			Diagnostic			48.24%	14,806				Diagnostic
B.1.1.16	O-7	INP Standalone/FL (%)			Diagnostic			100.00%	1				Diagnostic
B.1.1.17	O-13	LNP Standalone/FL (%)			Diagnostic			8.75%	3,796				Diagnostic
	% Rejected Service Requests - Partially Mechanized												
B.1.2.1	O-7	Switch Ports/FL (%)			Diagnostic								Diagnostic
B.1.2.2	O-7	Local Interoffice Transport/FL (%)			Diagnostic								Diagnostic
B.1.2.3	O-7	Loop + Port Combinations/FL (%)			Diagnostic			28.09%	17,723				Diagnostic
B.1.2.4	O-7	Combo Other/FL (%)			Diagnostic								Diagnostic
B.1.2.5	O-7	xDSL (ADSL, HDSL and UCL)/FL (%)			Diagnostic			6.98%	86				Diagnostic
B.1.2.6	O-7	ISDN Loop (UDN, UDC)/FL (%)			Diagnostic			11.94%	268				Diagnostic
B.1.2.7	O-7	Line Sharing/FL (%)			Diagnostic			37.07%	232				Diagnostic
B.1.2.8	O-7	2W Analog Loop Design/FL (%)			Diagnostic			28.28%	290				Diagnostic
B.1.2.9	O-7	2W Analog Loop Non-Design/FL (%)			Diagnostic			16.28%	1,167				Diagnostic
B.1.2.10	O-7	2W Analog Loop w/INP Design/FL (%)			Diagnostic								Diagnostic
B.1.2.11	O-7	2W Analog Loop w/INP Non-Design/FL (%)			Diagnostic								Diagnostic
B.1.2.12	O-13	2W Analog Loop w/LNP Design/FL (%)			Diagnostic			37.52%	757				Diagnostic
B.1.2.13	O-13	2W Analog Loop w/LNP Non-Design/FL (%)			Diagnostic			24.97%	2,211				Diagnostic
B.1.2.14	O-7	Other Design/FL (%)			Diagnostic			25.71%	245				Diagnostic
B.1.2.15	O-7	Other Non-Design/FL (%)			Diagnostic			29.32%	7,661				Diagnostic
B.1.2.16	O-7	INP Standalone/FL (%)			Diagnostic								Diagnostic
B.1.2.17	O-13	LNP Standalone/FL (%)			Diagnostic			31.30%	1,901				Diagnostic
	% Rejected Service Requests - Non-Mechanized												
B.1.3.1	O-7	Switch Ports/FL (%)			Diagnostic			50.00%	4				Diagnostic
B.1.3.2	O-7	Local Interoffice Transport/FL (%)			Diagnostic			46.67%	45				Diagnostic
B.1.3.3	O-7	Loop + Port Combinations/FL (%)			Diagnostic			43.23%	2,084				Diagnostic
B.1.3.4	O-7	Combo Other/FL (%)			Diagnostic			62.79%	43				Diagnostic
B.1.3.5	O-7	xDSL (ADSL, HDSL and UCL)/FL (%)			Diagnostic			26.86%	283				Diagnostic
B.1.3.6	O-7	ISDN Loop (UDN, UDC)/FL (%)			Diagnostic			16.21%	475				Diagnostic
B.1.3.7	O-7	Line Sharing/FL (%)			Diagnostic			35.09%	114				Diagnostic
B.1.3.8	O-7	2W Analog Loop Design/FL (%)			Diagnostic			33.84%	263				Diagnostic
B.1.3.9	O-7	2W Analog Loop Non-Design/FL (%)			Diagnostic			25.83%	906				Diagnostic
B.1.3.10	O-7	2W Analog Loop w/INP Design/FL (%)			Diagnostic			33.33%	6				Diagnostic
B.1.3.11	O-7	2W Analog Loop w/INP Non-Design/FL (%)			Diagnostic			20.00%	5				Diagnostic
B.1.3.12	O-13	2W Analog Loop w/LNP Design/FL (%)			Diagnostic			61.32%	106				Diagnostic
B.1.3.13	O-13	2W Analog Loop w/LNP Non-Design/FL (%)			Diagnostic			51.76%	85				Diagnostic
B.1.3.14	O-7	Other Design/FL (%)			Diagnostic			29.30%	587				Diagnostic
B.1.3.15	O-7	Other Non-Design/FL (%)			Diagnostic			38.26%	1,882				Diagnostic
B.1.3.16	O-7	INP Standalone/FL (%)			Diagnostic			40.91%	88				Diagnostic
B.1.3.17	O-13	LNP Standalone/FL (%)			Diagnostic			35.78%	911				Diagnostic
	Reject Interval - Mechanized												
B.1.4.1	O-8	Switch Ports/FL (%)											
B.1.4.2	O-8	Local Interoffice Transport/FL (%)											
B.1.4.3	O-8	Loop + Port Combinations/FL (%)			NO			93.80%	7,901				NO
B.1.4.4	O-8	Combo Other/FL (%)			NO								
B.1.4.5	O-8	xDSL (ADSL, HDSL and UCL)/FL (%)			YES			98.94%	94				YES
B.1.4.6	O-8	ISDN Loop (UDN, UDC)/FL (%)			NO			80.00%	10				NO
B.1.4.7	O-8	Line Sharing/FL (%)			NO			83.91%	87				NO

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			Benchmark/ Analog	August BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.1.4.8	O-8	2W Analog Loop Design/FL (%)	>= 97% w in 1 hr			74.88%	207				NO
B.1.4.9	O-8	2W Analog Loop Non-Design/FL (%)	>= 97% w in 1 hr			64.14%	145				NO
B.1.4.10	O-8	2W Analog Loop w/INP Design/FL (%)	>= 97% w in 1 hr								
B.1.4.11	O-8	2W Analog Loop w/INP Non-Design/FL (%)	>= 97% w in 1 hr								
B.1.4.12	O-14	2W Analog Loop w/LNP Design/FL (%)	>= 97% w in 1 hr			90.91%	22				NO
B.1.4.13	O-14	2W Analog Loop w/LNP Non-Design/FL (%)	>= 97% w in 1 hr			98.00%	100				YES
B.1.4.14	O-8	Other Design/FL (%)	>= 97% w in 1 hr			54.17%	96				NO
B.1.4.15	O-8	Other Non-Design/FL (%)	>= 97% w in 1 hr			56.80%	5,049				NO
B.1.4.16	O-8	INP Standalone/FL (%)	>= 97% w in 1 hr			0.00%	1				NO
B.1.4.17	O-14	LNP Standalone/FL (%)	>= 97% w in 1 hr			98.32%	357				YES
		Reject Interval - Partially Mechanized - 10 hours									
B.1.7.1	O-8	Switch Ports/FL (%)	>= 85% w in 10 hrs								
B.1.7.2	O-8	Local Interoffice Transport/FL (%)	>= 85% w in 10 hrs								
B.1.7.3	O-8	Loop + Port Combinations/FL (%)	>= 85% w in 10 hrs			96.64%	5,033				YES
B.1.7.4	O-8	Combo Other/FL (%)	>= 85% w in 10 hrs			90.00%	10				YES
B.1.7.5	O-8	xDSL (ADSL, HDSL and UCL)/FL (%)	>= 85% w in 10 hrs			50.00%	4				NO
B.1.7.6	O-8	ISDN Loop (UDN, UDC)/FL (%)	>= 85% w in 10 hrs								
B.1.7.7	O-8	Line Sharing/FL (%)	>= 85% w in 10 hrs			88.89%	36				YES
B.1.7.8	O-8	2W Analog Loop Design/FL (%)	>= 85% w in 10 hrs			88.89%	63				YES
B.1.7.9	O-8	2W Analog Loop Non-Design/FL (%)	>= 85% w in 10 hrs			78.61%	201				NO
B.1.7.10	O-8	2W Analog Loop w/INP Design/FL (%)	>= 85% w in 10 hrs								
B.1.7.11	O-8	2W Analog Loop w/INP Non-Design/FL (%)	>= 85% w in 10 hrs								
B.1.7.12	O-14	2W Analog Loop w/LNP Design/FL (%)	>= 85% w in 10 hrs			92.31%	65				YES
B.1.7.13	O-14	2W Analog Loop w/LNP Non-Design/FL (%)	>= 85% w in 10 hrs			88.14%	253				YES
B.1.7.14	O-8	Other Design/FL (%)	>= 85% w in 10 hrs			88.66%	97				YES
B.1.7.15	O-8	Other Non-Design/FL (%)	>= 85% w in 10 hrs			98.84%	3,022				YES
B.1.7.16	O-8	INP Standalone/FL (%)	>= 85% w in 10 hrs								
B.1.7.17	O-14	LNP Standalone/FL (%)	>= 85% w in 10 hrs			96.62%	532				YES
		Reject Interval - Non-Mechanized									
B.1.8.1	O-8	Switch Ports/FL (%)	>= 85% w in 24 hrs								
B.1.8.2	O-8	Local Interoffice Transport/FL (%)	>= 85% w in 24 hrs			100.00%	25				YES
B.1.8.3	O-8	Loop + Port Combinations/FL (%)	>= 85% w in 24 hrs			97.57%	1,195				YES
B.1.8.4	O-8	Combo Other/FL (%)	>= 85% w in 24 hrs			95.40%	87				YES
B.1.8.5	O-8	xDSL (ADSL, HDSL and UCL)/FL (%)	>= 85% w in 24 hrs			100.00%	22				YES
B.1.8.6	O-8	ISDN Loop (UDN, UDC)/FL (%)	>= 85% w in 24 hrs			100.00%	64				YES
B.1.8.7	O-8	Line Sharing/FL (%)	>= 85% w in 24 hrs			100.00%	14				YES
B.1.8.8	O-8	2W Analog Loop Design/FL (%)	>= 85% w in 24 hrs			98.68%	76				YES
B.1.8.9	O-8	2W Analog Loop Non-Design/FL (%)	>= 85% w in 24 hrs			100.00%	183				YES
B.1.8.10	O-8	2W Analog Loop w/INP Design/FL (%)	>= 85% w in 24 hrs								
B.1.8.11	O-8	2W Analog Loop w/INP Non-Design/FL (%)	>= 85% w in 24 hrs								
B.1.8.12	O-14	2W Analog Loop w/LNP Design/FL (%)	>= 85% w in 24 hrs			100.00%	25				YES
B.1.8.13	O-14	2W Analog Loop w/LNP Non-Design/FL (%)	>= 85% w in 24 hrs			100.00%	24				YES
B.1.8.14	O-8	Other Design/FL (%)	>= 85% w in 24 hrs			97.87%	188				YES
B.1.8.15	O-8	Other Non-Design/FL (%)	>= 85% w in 24 hrs			98.42%	442				YES
B.1.8.16	O-8	INP Standalone/FL (%)	>= 85% w in 24 hrs			100.00%	16				YES
B.1.8.17	O-14	LNP Standalone/FL (%)	>= 85% w in 24 hrs			99.03%	310				YES
		FOC Timeliness - Mechanized									
B.1.9.1	O-9	Switch Ports/FL (%)	>= 95% w in 3 hrs								
B.1.9.2	O-9	Local Interoffice Transport/FL (%)	>= 95% w in 3 hrs								
B.1.9.3	O-9	Loop + Port Combinations/FL (%)	>= 95% w in 3 hrs			98.67%	68,436				YES
B.1.9.4	O-9	Combo Other/FL (%)	>= 95% w in 3 hrs			66.67%	3				NO
B.1.9.5	O-9	xDSL (ADSL, HDSL and UCL)/FL (%)	>= 95% w in 3 hrs			98.14%	485				YES
B.1.9.6	O-9	ISDN Loop (UDN, UDC)/FL (%)	>= 95% w in 3 hrs			76.56%	128				NO
B.1.9.7	O-9	Line Sharing/FL (%)	>= 95% w in 3 hrs			97.98%	198				YES
B.1.9.8	O-9	2W Analog Loop Design/FL (%)	>= 95% w in 3 hrs			93.38%	831				NO
B.1.9.9	O-9	2W Analog Loop Non-Design/FL (%)	>= 95% w in 3 hrs			99.03%	829				YES
B.1.9.10	O-9	2W Analog Loop w/INP Design/FL (%)	>= 95% w in 3 hrs								
B.1.9.11	O-9	2W Analog Loop w/INP Non-Design/FL (%)	>= 95% w in 3 hrs								
B.1.9.12	O-15	2W Analog Loop w/LNP Design/FL (%)	>= 95% w in 3 hrs			66.67%	9				NO
B.1.9.13	O-15	2W Analog Loop w/LNP Non-Design/FL (%)	>= 95% w in 3 hrs			96.55%	29				YES
B.1.9.14	O-9	Other Design/FL (%)	>= 95% w in 3 hrs			97.27%	256				YES
B.1.9.15	O-9	Other Non-Design/FL (%)	>= 95% w in 3 hrs			96.37%	12,269				YES
B.1.9.16	O-9	INP Standalone/FL (%)	>= 95% w in 3 hrs								
B.1.9.17	O-15	LNP Standalone/FL (%)	>= 95% w in 3 hrs			99.38%	3,240				YES
		FOC Timeliness - Partially Mechanized - 10 hours									
B.1.12.1	O-9	Switch Ports/FL (%)	>= 85% w in 10 hrs								

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - May 2002

			July								June				
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation
B.1.4.8	O-8	2W Analog Loop Design/FL (%)			72.55%	153				NO			77.60%	192	
B.1.4.9	O-8	2W Analog Loop Non-Design/FL (%)			52.03%	148				NO			64.29%	154	
B.1.4.10	O-8	2W Analog Loop w/INP Design/FL (%)													
B.1.4.11	O-8	2W Analog Loop w/INP Non-Design/FL (%)													
B.1.4.12	O-14	2W Analog Loop w/LNP Design/FL (%)			81.25%	16				NO			94.74%	19	
B.1.4.13	O-14	2W Analog Loop w/LNP Non-Design/FL (%)			86.14%	101				NO			96.00%	100	
B.1.4.14	O-8	Other Design/FL (%)			55.56%	72				NO			61.22%	49	
B.1.4.15	O-8	Other Non-Design/FL (%)			66.61%	7,170				NO			73.90%	6,226	
B.1.4.16	O-8	INP Standalone/FL (%)			100.00%	1				YES			100.00%	1	
B.1.4.17	O-14	LNP Standalone/FL (%)			98.42%	317				YES			96.06%	279	
	Reject Interval - Partially Mechanized - 10 hours														
B.1.7.1	O-8	Switch Ports/FL (%)													
B.1.7.2	O-8	Local Interoffice Transport/FL (%)													
B.1.7.3	O-8	Loop + Port Combinations/FL (%)			96.93%	4,339				YES			96.49%	3,983	
B.1.7.4	O-8	Combo Other/FL (%)			100.00%	2				YES			100.00%	2	
B.1.7.5	O-8	xDSL (ADSL, HDSL and UCL)/FL (%)			100.00%	2				YES			100.00%	3	
B.1.7.6	O-8	ISDN Loop (UDN, UDC)/FL (%)			100.00%	1				YES			46.67%	15	
B.1.7.7	O-8	Line Sharing/FL (%)			95.00%	20				YES			84.85%	33	
B.1.7.8	O-8	2W Analog Loop Design/FL (%)			90.24%	82				YES			87.96%	108	
B.1.7.9	O-8	2W Analog Loop Non-Design/FL (%)			76.78%	211				NO			78.24%	239	
B.1.7.10	O-8	2W Analog Loop w/INP Design/FL (%)													
B.1.7.11	O-8	2W Analog Loop w/INP Non-Design/FL (%)													
B.1.7.12	O-14	2W Analog Loop w/LNP Design/FL (%)			72.80%	125				NO			75.18%	137	
B.1.7.13	O-14	2W Analog Loop w/LNP Non-Design/FL (%)			72.66%	289				NO			85.30%	347	
B.1.7.14	O-8	Other Design/FL (%)			89.25%	93				YES			88.00%	50	
B.1.7.15	O-8	Other Non-Design/FL (%)			99.04%	3,866				YES			98.67%	3,004	
B.1.7.16	O-8	INP Standalone/FL (%)													
B.1.7.17	O-14	LNP Standalone/FL (%)			89.06%	530				YES			91.38%	522	
	Reject Interval - Non-Mechanized														
B.1.8.1	O-8	Switch Ports/FL (%)													
B.1.8.2	O-8	Local Interoffice Transport/FL (%)			94.74%	19				YES			100.00%	1	
B.1.8.3	O-8	Loop + Port Combinations/FL (%)			98.22%	1,179				YES			98.50%	998	
B.1.8.4	O-8	Combo Other/FL (%)			100.00%	52				YES			98.48%	66	
B.1.8.5	O-8	xDSL (ADSL, HDSL and UCL)/FL (%)			100.00%	23				YES			100.00%	74	
B.1.8.6	O-8	ISDN Loop (UDN, UDC)/FL (%)			100.00%	94				YES			100.00%	72	
B.1.8.7	O-8	Line Sharing/FL (%)			100.00%	12				YES			100.00%	19	
B.1.8.8	O-8	2W Analog Loop Design/FL (%)			100.00%	60				YES			100.00%	46	
B.1.8.9	O-8	2W Analog Loop Non-Design/FL (%)			100.00%	137				YES			100.00%	184	
B.1.8.10	O-8	2W Analog Loop w/INP Design/FL (%)			100.00%	9				YES					
B.1.8.11	O-8	2W Analog Loop w/INP Non-Design/FL (%)			100.00%	3				YES			100.00%	4	
B.1.8.12	O-14	2W Analog Loop w/LNP Design/FL (%)			90.00%	10				YES			95.83%	24	
B.1.8.13	O-14	2W Analog Loop w/LNP Non-Design/FL (%)			100.00%	28				YES			100.00%	21	
B.1.8.14	O-8	Other Design/FL (%)			98.59%	142				YES			99.30%	142	
B.1.8.15	O-8	Other Non-Design/FL (%)			99.17%	484				YES			98.94%	567	
B.1.8.16	O-8	INP Standalone/FL (%)			100.00%	16				YES			96.15%	26	
B.1.8.17	O-14	LNP Standalone/FL (%)			99.60%	252				YES			98.44%	384	
	FOC Timeliness - Mechanized														
B.1.9.1	O-9	Switch Ports/FL (%)													
B.1.9.2	O-9	Local Interoffice Transport/FL (%)													
B.1.9.3	O-9	Loop + Port Combinations/FL (%)			98.70%	67,227				YES			98.62%	48,246	
B.1.9.4	O-9	Combo Other/FL (%)											60.00%	15	
B.1.9.5	O-9	xDSL (ADSL, HDSL and UCL)/FL (%)			98.96%	579				YES			92.54%	657	
B.1.9.6	O-9	ISDN Loop (UDN, UDC)/FL (%)			76.28%	156				NO			96.88%	64	
B.1.9.7	O-9	Line Sharing/FL (%)			97.75%	178				YES			97.74%	221	
B.1.9.8	O-9	2W Analog Loop Design/FL (%)			94.60%	815				NO			94.23%	641	
B.1.9.9	O-9	2W Analog Loop Non-Design/FL (%)			98.42%	1,269				YES			99.00%	803	
B.1.9.10	O-9	2W Analog Loop w/INP Design/FL (%)													
B.1.9.11	O-9	2W Analog Loop w/INP Non-Design/FL (%)													
B.1.9.12	O-15	2W Analog Loop w/LNP Design/FL (%)			80.00%	5				NO			91.30%	23	
B.1.9.13	O-15	2W Analog Loop w/LNP Non-Design/FL (%)											0.00%	3	
B.1.9.14	O-9	Other Design/FL (%)			83.24%	185				NO			83.61%	244	
B.1.9.15	O-9	Other Non-Design/FL (%)			96.30%	9,109				YES			94.85%	6,757	
B.1.9.16	O-9	INP Standalone/FL (%)			100.00%	1				YES					
B.1.9.17	O-15	LNP Standalone/FL (%)			98.27%	3,002				YES			96.73%	2,815	
	FOC Timeliness - Partially Mechanized - 10 hours														
B.1.12.1	O-9	Switch Ports/FL (%)													

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - May 2002

						May							
			Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.1.4.8	O-8	2W Analog Loop Design/FL (%)			NO			79.26%	217				NO
B.1.4.9	O-8	2W Analog Loop Non-Design/FL (%)			NO			61.27%	173				NO
B.1.4.10	O-8	2W Analog Loop w/INP Design/FL (%)											
B.1.4.11	O-8	2W Analog Loop w/INP Non-Design/FL (%)						100.00%	3				YES
B.1.4.12	O-14	2W Analog Loop w/LNP Design/FL (%)			NO			98.36%	61				YES
B.1.4.13	O-14	2W Analog Loop w/LNP Non-Design/FL (%)			NO			92.11%	152				NO
B.1.4.14	O-8	Other Design/FL (%)			NO			58.97%	78				NO
B.1.4.15	O-8	Other Non-Design/FL (%)			NO			77.92%	7,270				NO
B.1.4.16	O-8	INP Standalone/FL (%)			YES			0.00%	1				NO
B.1.4.17	O-14	LNP Standalone/FL (%)			NO			96.40%	333				NO
	Reject Interval - Partially Mechanized - 10 hours												
B.1.7.1	O-8	Switch Ports/FL (%)											
B.1.7.2	O-8	Local Interoffice Transport/FL (%)											
B.1.7.3	O-8	Loop + Port Combinations/FL (%)			YES			86.71%	5,058				YES
B.1.7.4	O-8	Combo Other/FL (%)			YES								
B.1.7.5	O-8	xDSL (ADSL, HDSL and UCL)/FL (%)			YES			83.33%	6				NO
B.1.7.6	O-8	ISDN Loop (UDN, UDC)/FL (%)			NO			60.00%	35				NO
B.1.7.7	O-8	Line Sharing/FL (%)			NO			75.28%	89				NO
B.1.7.8	O-8	2W Analog Loop Design/FL (%)			YES			84.52%	84				NO
B.1.7.9	O-8	2W Analog Loop Non-Design/FL (%)			NO			64.71%	204				NO
B.1.7.10	O-8	2W Analog Loop w/INP Design/FL (%)											
B.1.7.11	O-8	2W Analog Loop w/INP Non-Design/FL (%)											
B.1.7.12	O-14	2W Analog Loop w/LNP Design/FL (%)			NO			74.23%	291				NO
B.1.7.13	O-14	2W Analog Loop w/LNP Non-Design/FL (%)			YES			84.13%	586				NO
B.1.7.14	O-8	Other Design/FL (%)			YES			86.36%	66				YES
B.1.7.15	O-8	Other Non-Design/FL (%)			YES			97.46%	2,324				YES
B.1.7.16	O-8	INP Standalone/FL (%)											
B.1.7.17	O-14	LNP Standalone/FL (%)			YES			91.00%	611				YES
	Reject Interval - Non-Mechanized												
B.1.8.1	O-8	Switch Ports/FL (%)						100.00%	2				YES
B.1.8.2	O-8	Local Interoffice Transport/FL (%)			YES			100.00%	21				YES
B.1.8.3	O-8	Loop + Port Combinations/FL (%)			YES			98.38%	925				YES
B.1.8.4	O-8	Combo Other/FL (%)			YES			100.00%	28				YES
B.1.8.5	O-8	xDSL (ADSL, HDSL and UCL)/FL (%)			YES			97.37%	76				YES
B.1.8.6	O-8	ISDN Loop (UDN, UDC)/FL (%)			YES			100.00%	81				YES
B.1.8.7	O-8	Line Sharing/FL (%)			YES			100.00%	43				YES
B.1.8.8	O-8	2W Analog Loop Design/FL (%)			YES			98.90%	91				YES
B.1.8.9	O-8	2W Analog Loop Non-Design/FL (%)			YES			100.00%	241				YES
B.1.8.10	O-8	2W Analog Loop w/INP Design/FL (%)						100.00%	2				YES
B.1.8.11	O-8	2W Analog Loop w/INP Non-Design/FL (%)			YES			100.00%	1				YES
B.1.8.12	O-14	2W Analog Loop w/LNP Design/FL (%)			YES			97.14%	70				YES
B.1.8.13	O-14	2W Analog Loop w/LNP Non-Design/FL (%)			YES			97.83%	46				YES
B.1.8.14	O-8	Other Design/FL (%)			YES			99.43%	175				YES
B.1.8.15	O-8	Other Non-Design/FL (%)			YES			98.91%	737				YES
B.1.8.16	O-8	INP Standalone/FL (%)			YES			100.00%	36				YES
B.1.8.17	O-14	LNP Standalone/FL (%)			YES			98.82%	338				YES
	FOC Timeliness - Mechanized												
B.1.9.1	O-9	Switch Ports/FL (%)											
B.1.9.2	O-9	Local Interoffice Transport/FL (%)											
B.1.9.3	O-9	Loop + Port Combinations/FL (%)			YES			98.97%	35,367				YES
B.1.9.4	O-9	Combo Other/FL (%)			NO								
B.1.9.5	O-9	xDSL (ADSL, HDSL and UCL)/FL (%)			NO			97.67%	472				YES
B.1.9.6	O-9	ISDN Loop (UDN, UDC)/FL (%)			YES			97.92%	48				YES
B.1.9.7	O-9	Line Sharing/FL (%)			YES			99.34%	152				YES
B.1.9.8	O-9	2W Analog Loop Design/FL (%)			NO			97.88%	708				YES
B.1.9.9	O-9	2W Analog Loop Non-Design/FL (%)			YES			99.21%	757				YES
B.1.9.10	O-9	2W Analog Loop w/INP Design/FL (%)											
B.1.9.11	O-9	2W Analog Loop w/INP Non-Design/FL (%)											
B.1.9.12	O-15	2W Analog Loop w/LNP Design/FL (%)			NO			100.00%	34				YES
B.1.9.13	O-15	2W Analog Loop w/LNP Non-Design/FL (%)			NO			100.00%	76				YES
B.1.9.14	O-9	Other Design/FL (%)			NO			96.05%	253				YES
B.1.9.15	O-9	Other Non-Design/FL (%)			NO			93.88%	7,584				NO
B.1.9.16	O-9	INP Standalone/FL (%)											
B.1.9.17	O-15	LNP Standalone/FL (%)			YES			95.95%	3,434				YES
	FOC Timeliness - Partially Mechanized - 10 hours												
B.1.12.1	O-9	Switch Ports/FL (%)											

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - May 2002

			Benchmark/ Analog	August							
				BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.1.12.2	O-9	Local Interoffice Transport/FL (%)	>= 85% w in 10 hrs								
B.1.12.3	O-9	Loop + Port Combinations/FL (%)	>= 85% w in 10 hrs			91.83%	12,339				YES
B.1.12.4	O-9	Combo Other/FL (%)	>= 85% w in 10 hrs			71.43%	35				NO
B.1.12.5	O-9	xDSL (ADSL, HDSL and UCL)/FL (%)	>= 85% w in 10 hrs			81.82%	22				NO
B.1.12.6	O-9	ISDN Loop (UDN, UDC)/FL (%)	>= 85% w in 10 hrs			80.95%	21				NO
B.1.12.7	O-9	Line Sharing/FL (%)	>= 85% w in 10 hrs			96.36%	55				YES
B.1.12.8	O-9	2W Analog Loop Design/FL (%)	>= 85% w in 10 hrs			91.33%	196				YES
B.1.12.9	O-9	2W Analog Loop Non-Design/FL (%)	>= 85% w in 10 hrs			95.09%	1,609				YES
B.1.12.10	O-9	2W Analog Loop w/INP Design/FL (%)	>= 85% w in 10 hrs								
B.1.12.11	O-9	2W Analog Loop w/INP Non-Design/FL (%)	>= 85% w in 10 hrs								
B.1.12.12	O-15	2W Analog Loop w/LNP Design/FL (%)	>= 85% w in 10 hrs			69.80%	149				NO
B.1.12.13	O-15	2W Analog Loop w/LNP Non-Design/FL (%)	>= 85% w in 10 hrs			84.03%	952				NO
B.1.12.14	O-9	Other Design/FL (%)	>= 85% w in 10 hrs			91.07%	280				YES
B.1.12.15	O-9	Other Non-Design/FL (%)	>= 85% w in 10 hrs			96.47%	5,206				YES
B.1.12.16	O-9	INP Standalone/FL (%)	>= 85% w in 10 hrs								
B.1.12.17	O-15	LNP Standalone/FL (%)	>= 85% w in 10 hrs			95.23%	1,027				YES
		FOC Timeliness - Non-Mechanized									
B.1.13.1	O-9	Switch Ports/FL (%)	>= 85% w in 36 hrs								
B.1.13.2	O-9	Local Interoffice Transport/FL (%)	>= 85% w in 36 hrs			88.24%	17				YES
B.1.13.3	O-9	Loop + Port Combinations/FL (%)	>= 85% w in 36 hrs			97.41%	1,779				YES
B.1.13.4	O-9	Combo Other/FL (%)	>= 85% w in 36 hrs			99.06%	106				YES
B.1.13.5	O-9	xDSL (ADSL, HDSL and UCL)/FL (%)	>= 85% w in 36 hrs			96.92%	65				YES
B.1.13.6	O-9	ISDN Loop (UDN, UDC)/FL (%)	>= 85% w in 36 hrs			98.67%	226				YES
B.1.13.7	O-9	Line Sharing/FL (%)	>= 85% w in 36 hrs			100.00%	15				YES
B.1.13.8	O-9	2W Analog Loop Design/FL (%)	>= 85% w in 36 hrs			96.84%	95				YES
B.1.13.9	O-9	2W Analog Loop Non-Design/FL (%)	>= 85% w in 36 hrs			98.98%	295				YES
B.1.13.10	O-9	2W Analog Loop w/INP Design/FL (%)	>= 85% w in 36 hrs			100.00%	3				YES
B.1.13.11	O-9	2W Analog Loop w/INP Non-Design/FL (%)	>= 85% w in 36 hrs			100.00%	4				YES
B.1.13.12	O-15	2W Analog Loop w/LNP Design/FL (%)	>= 85% w in 36 hrs			100.00%	14				YES
B.1.13.13	O-15	2W Analog Loop w/LNP Non-Design/FL (%)	>= 85% w in 36 hrs			100.00%	26				YES
B.1.13.14	O-9	Other Design/FL (%)	>= 85% w in 36 hrs			100.00%	231				YES
B.1.13.15	O-9	Other Non-Design/FL (%)	>= 85% w in 36 hrs			99.80%	981				YES
B.1.13.16	O-9	INP Standalone/FL (%)	>= 85% w in 36 hrs			100.00%	26				YES
B.1.13.17	O-15	LNP Standalone/FL (%)	>= 85% w in 36 hrs			99.67%	600				YES
		FOC & Reject Response Completeness - Mechanized									
B.1.14.1.1	O-11	Switch Ports/EDI/FL (%)	>= 95%								
B.1.14.1.2	O-11	Switch Ports/TAG/FL (%)	>= 95%								
B.1.14.2.1	O-11	Local Interoffice Transport/EDI/FL (%)	>= 95%								
B.1.14.2.2	O-11	Local Interoffice Transport/TAG/FL (%)	>= 95%								
B.1.14.3.1	O-11	Loop + Port Combinations/EDI/FL (%)	>= 95%			99.42%	30,685				YES
B.1.14.3.2	O-11	Loop + Port Combinations/TAG/FL (%)	>= 95%			99.86%	51,079				YES
B.1.14.4.1	O-11	Combo Other/EDI/FL (%)	>= 95%			50.00%	10				NO
B.1.14.4.2	O-11	Combo Other/TAG/FL (%)	>= 95%			66.67%	3				NO
B.1.14.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/FL (%)	>= 95%			98.90%	363				YES
B.1.14.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/FL (%)	>= 95%			88.41%	345				NO
B.1.14.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/FL (%)	>= 95%			90.91%	11				NO
B.1.14.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/FL (%)	>= 95%			62.02%	208				NO
B.1.14.7.1	O-11	Line Sharing/EDI/FL (%)	>= 95%			99.06%	212				YES
B.1.14.7.2	O-11	Line Sharing/TAG/FL (%)	>= 95%			97.83%	46				YES
B.1.14.8.1	O-11	2W Analog Loop Design/EDI/FL (%)	>= 95%			92.66%	218				NO
B.1.14.8.2	O-11	2W Analog Loop Design/TAG/FL (%)	>= 95%			99.63%	819				YES
B.1.14.9.1	O-11	2W Analog Loop Non-Design/EDI/FL (%)	>= 95%			100.00%	4				YES
B.1.14.9.2	O-11	2W Analog Loop Non-Design/TAG/FL (%)	>= 95%			95.62%	1,004				YES
B.1.14.10.1	O-11	2W Analog Loop w/INP Design/EDI/FL (%)	>= 95%								
B.1.14.10.2	O-11	2W Analog Loop w/INP Design/TAG/FL (%)	>= 95%								
B.1.14.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/FL (%)	>= 95%								
B.1.14.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/FL (%)	>= 95%								
B.1.14.12.1	O-11	2W Analog Loop w/LNP Design/EDI/FL (%)	>= 95%			87.50%	32				NO
B.1.14.12.2	O-11	2W Analog Loop w/LNP Design/TAG/FL (%)	>= 95%			66.67%	3				NO
B.1.14.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/FL (%)	>= 95%			100.00%	6				YES
B.1.14.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/FL (%)	>= 95%			92.48%	133				NO
B.1.14.14.1	O-11	Other Design/EDI/FL (%)	>= 95%			90.98%	122				NO
B.1.14.14.2	O-11	Other Design/TAG/FL (%)	>= 95%			99.11%	224				YES
B.1.14.15.1	O-11	Other Non-Design/EDI/FL (%)	>= 95%			99.24%	14,280				YES
B.1.14.15.2	O-11	Other Non-Design/TAG/FL (%)	>= 95%			93.81%	1,680				NO
B.1.14.16.1	O-11	INP Standalone/EDI/FL (%)	>= 95%			100.00%	1				YES
B.1.14.16.2	O-11	INP Standalone/TAG/FL (%)	>= 95%								

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - May 2002

			July								June				
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation
B.1.12.2	O-9	Local Interoffice Transport/FL (%)													
B.1.12.3	O-9	Loop + Port Combinations/FL (%)			94.67%	11,745				YES			92.68%	14,712	
B.1.12.4	O-9	Combo Other/FL (%)			58.82%	17				NO			0.00%	1	
B.1.12.5	O-9	xDSL (ADSL, HDSL and UCL)/FL (%)			89.19%	37				YES			88.89%	18	
B.1.12.6	O-9	ISDN Loop (UDN, UDC)/FL (%)			70.83%	24				NO			82.05%	39	
B.1.12.7	O-9	Line Sharing/FL (%)			100.00%	42				YES			88.89%	54	
B.1.12.8	O-9	2W Analog Loop Design/FL (%)			91.70%	289				YES			91.67%	348	
B.1.12.9	O-9	2W Analog Loop Non-Design/FL (%)			94.11%	1,426				YES			91.53%	1,204	
B.1.12.10	O-9	2W Analog Loop w/INP Design/FL (%)													
B.1.12.11	O-9	2W Analog Loop w/INP Non-Design/FL (%)													
B.1.12.12	O-15	2W Analog Loop w/LNP Design/FL (%)			74.04%	208				NO			65.57%	273	
B.1.12.13	O-15	2W Analog Loop w/LNP Non-Design/FL (%)			65.43%	969				NO			80.58%	1,107	
B.1.12.14	O-9	Other Design/FL (%)			83.78%	296				NO			84.84%	244	
B.1.12.15	O-9	Other Non-Design/FL (%)			97.44%	6,416				YES			96.82%	4,121	
B.1.12.16	O-9	INP Standalone/FL (%)													
B.1.12.17	O-15	LNP Standalone/FL (%)			92.63%	1,139				YES			94.08%	1,182	
	FOC Timeliness - Non-Mechanized														
B.1.13.1	O-9	Switch Ports/FL (%)											100.00%	1	
B.1.13.2	O-9	Local Interoffice Transport/FL (%)			100.00%	15				YES			100.00%	8	
B.1.13.3	O-9	Loop + Port Combinations/FL (%)			98.11%	1,691				YES			98.23%	1,246	
B.1.13.4	O-9	Combo Other/FL (%)			100.00%	57				YES			100.00%	65	
B.1.13.5	O-9	xDSL (ADSL, HDSL and UCL)/FL (%)			96.47%	85				YES			99.50%	200	
B.1.13.6	O-9	ISDN Loop (UDN, UDC)/FL (%)			100.00%	348				YES			97.89%	285	
B.1.13.7	O-9	Line Sharing/FL (%)			100.00%	35				YES			100.00%	35	
B.1.13.8	O-9	2W Analog Loop Design/FL (%)			96.97%	99				YES			98.41%	126	
B.1.13.9	O-9	2W Analog Loop Non-Design/FL (%)			98.58%	211				YES			99.21%	504	
B.1.13.10	O-9	2W Analog Loop w/INP Design/FL (%)													
B.1.13.11	O-9	2W Analog Loop w/INP Non-Design/FL (%)			100.00%	6				YES			100.00%	3	
B.1.13.12	O-15	2W Analog Loop w/LNP Design/FL (%)			100.00%	7				YES			100.00%	14	
B.1.13.13	O-15	2W Analog Loop w/LNP Non-Design/FL (%)			100.00%	25				YES			100.00%	23	
B.1.13.14	O-9	Other Design/FL (%)			99.53%	213				YES			98.81%	253	
B.1.13.15	O-9	Other Non-Design/FL (%)			98.99%	1,187				YES			98.94%	1,221	
B.1.13.16	O-9	INP Standalone/FL (%)			100.00%	34				YES			100.00%	39	
B.1.13.17	O-15	LNP Standalone/FL (%)			99.48%	573				YES			99.81%	540	
	FOC & Reject Response Completeness - Mechanized														
B.1.14.1.1	O-11	Switch Ports/EDI/FL (%)													
B.1.14.1.2	O-11	Switch Ports/TAG/FL (%)													
B.1.14.2.1	O-11	Local Interoffice Transport/EDI/FL (%)													
B.1.14.2.2	O-11	Local Interoffice Transport/TAG/FL (%)													
B.1.14.3.1	O-11	Loop + Port Combinations/EDI/FL (%)			99.88%	29,319				YES			99.53%	20,966	
B.1.14.3.2	O-11	Loop + Port Combinations/TAG/FL (%)			99.97%	46,920				YES			99.97%	35,968	
B.1.14.4.1	O-11	Combo Other/EDI/FL (%)			100.00%	2				YES			62.50%	8	
B.1.14.4.2	O-11	Combo Other/TAG/FL (%)											46.81%	47	
B.1.14.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/FL (%)			99.07%	430				YES			98.09%	472	
B.1.14.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/FL (%)			85.75%	365				NO			64.04%	534	
B.1.14.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/FL (%)			90.91%	22				NO			100.00%	13	
B.1.14.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/FL (%)			51.68%	327				NO			100.00%	58	
B.1.14.7.1	O-11	Line Sharing/EDI/FL (%)			100.00%	182				YES			100.00%	182	
B.1.14.7.2	O-11	Line Sharing/TAG/FL (%)			100.00%	42				YES			100.00%	86	
B.1.14.8.1	O-11	2W Analog Loop Design/EDI/FL (%)			93.36%	226				NO			97.35%	340	
B.1.14.8.2	O-11	2W Analog Loop Design/TAG/FL (%)			99.46%	744				YES			98.78%	490	
B.1.14.9.1	O-11	2W Analog Loop Non-Design/EDI/FL (%)			100.00%	2				YES					
B.1.14.9.2	O-11	2W Analog Loop Non-Design/TAG/FL (%)			99.36%	1,397				YES			99.47%	952	
B.1.14.10.1	O-11	2W Analog Loop w/INP Design/EDI/FL (%)													
B.1.14.10.2	O-11	2W Analog Loop w/INP Design/TAG/FL (%)													
B.1.14.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/FL (%)													
B.1.14.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/FL (%)													
B.1.14.12.1	O-11	2W Analog Loop w/LNP Design/EDI/FL (%)			96.24%	21				YES			96.97%	33	
B.1.14.12.2	O-11	2W Analog Loop w/LNP Design/TAG/FL (%)			100.00%	2				YES			100.00%	10	
B.1.14.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/FL (%)													
B.1.14.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/FL (%)			98.97%	97				YES			100.00%	103	
B.1.14.14.1	O-11	Other Design/EDI/FL (%)			89.80%	98				NO			93.10%	116	
B.1.14.14.2	O-11	Other Design/TAG/FL (%)			99.38%	160				YES			98.91%	183	
B.1.14.15.1	O-11	Other Non-Design/EDI/FL (%)			98.01%	14,536				YES			98.19%	11,850	
B.1.14.15.2	O-11	Other Non-Design/TAG/FL (%)			90.71%	1,345				NO			89.29%	906	
B.1.14.16.1	O-11	INP Standalone/EDI/FL (%)													
B.1.14.16.2	O-11	INP Standalone/TAG/FL (%)			100.00%	2				YES			100.00%	1	

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - May 2002

			Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
						May							
B.1.12.2	O-9	Local Interoffice Transport/FL (%)											
B.1.12.3	O-9	Loop + Port Combinations/FL (%)			YES			80.73%	13,549				NO
B.1.12.4	O-9	Combo Other/FL (%)			NO								
B.1.12.5	O-9	xDSL (ADSL, HDSL and UCL)/FL (%)			YES			95.24%	63				YES
B.1.12.6	O-9	ISDN Loop (UDN, UDC)/FL (%)			NO			94.01%	267				YES
B.1.12.7	O-9	Line Sharing/FL (%)			YES			87.50%	168				YES
B.1.12.8	O-9	2W Analog Loop Design/FL (%)			YES			83.64%	214				NO
B.1.12.9	O-9	2W Analog Loop Non-Design/FL (%)			YES			93.01%	1,102				YES
B.1.12.10	O-9	2W Analog Loop w/INP Design/FL (%)											
B.1.12.11	O-9	2W Analog Loop w/INP Non-Design/FL (%)											
B.1.12.12	O-15	2W Analog Loop w/LNP Design/FL (%)			NO			77.96%	490				NO
B.1.12.13	O-15	2W Analog Loop w/LNP Non-Design/FL (%)			NO			92.92%	1,694				YES
B.1.12.14	O-9	Other Design/FL (%)			NO			84.34%	198				NO
B.1.12.15	O-9	Other Non-Design/FL (%)			YES			95.70%	5,302				YES
B.1.12.16	O-9	INP Standalone/FL (%)											
B.1.12.17	O-15	LNP Standalone/FL (%)			YES			93.08%	1,330				YES
	FOC Timeliness - Non-Mechanized												
B.1.13.1	O-9	Switch Ports/FL (%)			YES			100.00%	2				YES
B.1.13.2	O-9	Local Interoffice Transport/FL (%)			YES			96.67%	30				YES
B.1.13.3	O-9	Loop + Port Combinations/FL (%)			YES			98.14%	1,022				YES
B.1.13.4	O-9	Combo Other/FL (%)			YES			94.12%	17				YES
B.1.13.5	O-9	xDSL (ADSL, HDSL and UCL)/FL (%)			YES			99.49%	195				YES
B.1.13.6	O-9	ISDN Loop (UDN, UDC)/FL (%)			YES			99.48%	382				YES
B.1.13.7	O-9	Line Sharing/FL (%)			YES			98.73%	79				YES
B.1.13.8	O-9	2W Analog Loop Design/FL (%)			YES			100.00%	170				YES
B.1.13.9	O-9	2W Analog Loop Non-Design/FL (%)			YES			99.04%	625				YES
B.1.13.10	O-9	2W Analog Loop w/INP Design/FL (%)						100.00%	2				YES
B.1.13.11	O-9	2W Analog Loop w/INP Non-Design/FL (%)			YES			100.00%	3				YES
B.1.13.12	O-15	2W Analog Loop w/LNP Design/FL (%)			YES			100.00%	39				YES
B.1.13.13	O-15	2W Analog Loop w/LNP Non-Design/FL (%)			YES			97.30%	37				YES
B.1.13.14	O-9	Other Design/FL (%)			YES			99.52%	414				YES
B.1.13.15	O-9	Other Non-Design/FL (%)			YES			99.31%	1,161				YES
B.1.13.16	O-9	INP Standalone/FL (%)			YES			100.00%	42				YES
B.1.13.17	O-15	LNP Standalone/FL (%)			YES			99.61%	512				YES
	FOC & Reject Response Completeness - Mechanized												
B.1.14.1.1	O-11	Switch Ports/EDI/FL (%)											
B.1.14.1.2	O-11	Switch Ports/TAG/FL (%)											
B.1.14.2.1	O-11	Local Interoffice Transport/EDI/FL (%)											
B.1.14.2.2	O-11	Local Interoffice Transport/TAG/FL (%)											
B.1.14.3.1	O-11	Loop + Port Combinations/EDI/FL (%)			YES			96.35%	13,232				YES
B.1.14.3.2	O-11	Loop + Port Combinations/TAG/FL (%)			YES			96.45%	31,392				YES
B.1.14.4.1	O-11	Combo Other/EDI/FL (%)			NO								
B.1.14.4.2	O-11	Combo Other/TAG/FL (%)			NO								
B.1.14.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/FL (%)			YES			97.00%	367				YES
B.1.14.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/FL (%)			NO			86.15%	231				NO
B.1.14.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/FL (%)			YES			60.00%	5				NO
B.1.14.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/FL (%)			YES			77.14%	70				NO
B.1.14.7.1	O-11	Line Sharing/EDI/FL (%)			YES			95.21%	167				YES
B.1.14.7.2	O-11	Line Sharing/TAG/FL (%)			YES			87.18%	78				NO
B.1.14.8.1	O-11	2W Analog Loop Design/EDI/FL (%)			YES			91.77%	328				NO
B.1.14.8.2	O-11	2W Analog Loop Design/TAG/FL (%)			YES			98.50%	602				YES
B.1.14.9.1	O-11	2W Analog Loop Non-Design/EDI/FL (%)											
B.1.14.9.2	O-11	2W Analog Loop Non-Design/TAG/FL (%)			YES			96.61%	943				YES
B.1.14.10.1	O-11	2W Analog Loop w/INP Design/EDI/FL (%)											
B.1.14.10.2	O-11	2W Analog Loop w/INP Design/TAG/FL (%)											
B.1.14.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/FL (%)											
B.1.14.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/FL (%)						100.00%	3				YES
B.1.14.12.1	O-11	2W Analog Loop w/LNP Design/EDI/FL (%)			YES			86.46%	96				NO
B.1.14.12.2	O-11	2W Analog Loop w/LNP Design/TAG/FL (%)			YES			92.31%	13				NO
B.1.14.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/FL (%)											
B.1.14.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/FL (%)			YES			88.72%	257				NO
B.1.14.14.1	O-11	Other Design/EDI/FL (%)			NO			89.86%	138				NO
B.1.14.14.2	O-11	Other Design/TAG/FL (%)			YES			98.48%	197				YES
B.1.14.15.1	O-11	Other Non-Design/EDI/FL (%)			YES			97.02%	13,524				YES
B.1.14.15.2	O-11	Other Non-Design/TAG/FL (%)			NO			87.13%	1,282				NO
B.1.14.16.1	O-11	INP Standalone/EDI/FL (%)						100.00%	1				YES
B.1.14.16.2	O-11	INP Standalone/TAG/FL (%)			YES								

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - M ay 2002

			Benchmark/ Analog	August BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.1.14.17.1	O-11	LNP Standalone/EDI/FL (%)	>= 95%			99.17%	3,384				YES
B.1.14.17.2	O-11	LNP Standalone/TAG/FL (%)	>= 95%			96.58%	234				YES
		FOC & Reject Response Completeness - Partially Mechanized									
B.1.15.1.1	O-11	Switch Ports/EDI/FL (%)	>= 95%								
B.1.15.1.2	O-11	Switch Ports/TAG/FL (%)	>= 95%								
B.1.15.2.1	O-11	Local Interoffice Transport/EDI/FL (%)	>= 95%								
B.1.15.2.2	O-11	Local Interoffice Transport/TAG/FL (%)	>= 95%								
B.1.15.3.1	O-11	Loop + Port Combinations/EDI/FL (%)	>= 95%			99.75%	3,994				YES
B.1.15.3.2	O-11	Loop + Port Combinations/TAG/FL (%)	>= 95%			99.82%	11,893				YES
B.1.15.4.1	O-11	Combo Other/EDI/FL (%)	>= 95%			83.78%	37				NO
B.1.15.4.2	O-11	Combo Other/TAG/FL (%)	>= 95%			59.09%	22				NO
B.1.15.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/FL (%)	>= 95%			93.33%	15				NO
B.1.15.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/FL (%)	>= 95%			60.00%	15				NO
B.1.15.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/FL (%)	>= 95%			100.00%	1				YES
B.1.15.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/FL (%)	>= 95%			81.82%	22				NO
B.1.15.7.1	O-11	Line Sharing/EDI/FL (%)	>= 95%			100.00%	54				YES
B.1.15.7.2	O-11	Line Sharing/TAG/FL (%)	>= 95%			100.00%	21				YES
B.1.15.8.1	O-11	2W Analog Loop Design/EDI/FL (%)	>= 95%			99.32%	148				YES
B.1.15.8.2	O-11	2W Analog Loop Design/TAG/FL (%)	>= 95%			98.89%	90				YES
B.1.15.9.1	O-11	2W Analog Loop Non-Design/EDI/FL (%)	>= 95%			100.00%	10				YES
B.1.15.9.2	O-11	2W Analog Loop Non-Design/TAG/FL (%)	>= 95%			99.70%	1,683				YES
B.1.15.10.1	O-11	2W Analog Loop w/INP Design/EDI/FL (%)	>= 95%								
B.1.15.10.2	O-11	2W Analog Loop w/INP Design/TAG/FL (%)	>= 95%								
B.1.15.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/FL (%)	>= 95%								
B.1.15.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/FL (%)	>= 95%								
B.1.15.12.1	O-11	2W Analog Loop w/LNP Design/EDI/FL (%)	>= 95%			100.00%	162				YES
B.1.15.12.2	O-11	2W Analog Loop w/LNP Design/TAG/FL (%)	>= 95%			100.00%	41				YES
B.1.15.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/FL (%)	>= 95%			100.00%	13				YES
B.1.15.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/FL (%)	>= 95%			99.91%	1,125				YES
B.1.15.14.1	O-11	Other Design/EDI/FL (%)	>= 95%			99.61%	254				YES
B.1.15.14.2	O-11	Other Design/TAG/FL (%)	>= 95%			98.92%	93				YES
B.1.15.15.1	O-11	Other Non-Design/EDI/FL (%)	>= 95%			99.82%	7,191				YES
B.1.15.15.2	O-11	Other Non-Design/TAG/FL (%)	>= 95%			98.21%	726				YES
B.1.15.16.1	O-11	INP Standalone/EDI/FL (%)	>= 95%								
B.1.15.16.2	O-11	INP Standalone/TAG/FL (%)	>= 95%								
B.1.15.17.1	O-11	LNP Standalone/EDI/FL (%)	>= 95%			99.84%	1,287				YES
B.1.15.17.2	O-11	LNP Standalone/TAG/FL (%)	>= 95%			99.54%	217				YES
		FOC & Reject Response Completeness - Non-Mechanized									
B.1.16.1	O-11	Switch Ports/FL (%)	>= 95%								
B.1.16.2	O-11	Local Interoffice Transport/FL (%)	>= 95%			97.44%	39				YES
B.1.16.3	O-11	Loop + Port Combinations/FL (%)	>= 95%			93.28%	3,004				NO
B.1.16.4	O-11	Combo Other/FL (%)	>= 95%			95.29%	191				YES
B.1.16.5	O-11	xDSL (ADSL, HDSL and UCL)/FL (%)	>= 95%			95.45%	88				YES
B.1.16.6	O-11	ISDN Loop (UDN, UDC)/FL (%)	>= 95%			93.58%	296				NO
B.1.16.7	O-11	Line Sharing/FL (%)	>= 95%			100.00%	28				YES
B.1.16.8	O-11	2W Analog Loop Design/FL (%)	>= 95%			95.40%	174				YES
B.1.16.9	O-11	2W Analog Loop Non-Design/FL (%)	>= 95%			95.13%	472				YES
B.1.16.10	O-11	2W Analog Loop w/INP Design/FL (%)	>= 95%			50.00%	6				NO
B.1.16.11	O-11	2W Analog Loop w/INP Non-Design/FL (%)	>= 95%			100.00%	4				YES
B.1.16.12	O-11	2W Analog Loop w/LNP Design/FL (%)	>= 95%			97.44%	39				YES
B.1.16.13	O-11	2W Analog Loop w/LNP Non-Design/FL (%)	>= 95%			97.96%	49				YES
B.1.16.14	O-11	Other Design/FL (%)	>= 95%			97.76%	401				YES
B.1.16.15	O-11	Other Non-Design/FL (%)	>= 95%			98.04%	1,381				YES
B.1.16.16	O-11	INP Standalone/FL (%)	>= 95%			90.91%	44				NO
B.1.16.17	O-11	LNP Standalone/FL (%)	>= 95%			96.50%	914				YES
		FOC & Reject Response Completeness (Multiple Responses) - Mechanized									
B.1.17.1.1	O-11	Switch Ports/EDI/FL (%)	>= 95%								
B.1.17.1.2	O-11	Switch Ports/TAG/FL (%)	>= 95%								
B.1.17.2.1	O-11	Local Interoffice Transport/EDI/FL (%)	>= 95%								
B.1.17.2.2	O-11	Local Interoffice Transport/TAG/FL (%)	>= 95%								
B.1.17.3.1	O-11	Loop + Port Combinations/EDI/FL (%)	>= 95%			98.74%	30,506				YES
B.1.17.3.2	O-11	Loop + Port Combinations/TAG/FL (%)	>= 95%			99.77%	51,006				YES
B.1.17.4.1	O-11	Combo Other/EDI/FL (%)	>= 95%			60.00%	5				NO
B.1.17.4.2	O-11	Combo Other/TAG/FL (%)	>= 95%			100.00%	2				YES
B.1.17.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/FL (%)	>= 95%			95.82%	359				YES
B.1.17.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/FL (%)	>= 95%			85.25%	305				NO

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - M ay 2002

			July								June				
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation
B.1.14.17.1	O-11	LNP Standalone/EDI/FL (%)			99.46%	3,123				YES			99.65%	2,861	
B.1.14.17.2	O-11	LNP Standalone/TAG/FL (%)			99.46%	186				YES			97.62%	168	
		FOC & Reject Response Completeness - Partially Mechanized													
B.1.15.1.1	O-11	Switch Ports/EDI/FL (%)													
B.1.15.1.2	O-11	Switch Ports/TAG/FL (%)													
B.1.15.2.1	O-11	Local Interoffice Transport/EDI/FL (%)													
B.1.15.2.2	O-11	Local Interoffice Transport/TAG/FL (%)													
B.1.15.3.1	O-11	Loop + Port Combinations/EDI/FL (%)			99.94%	5,399				YES			99.44%	6,469	
B.1.15.3.2	O-11	Loop + Port Combinations/TAG/FL (%)			99.77%	9,731				YES			99.74%	11,004	
B.1.15.4.1	O-11	Combo Other/EDI/FL (%)			57.89%	19				NO			100.00%	2	
B.1.15.4.2	O-11	Combo Other/TAG/FL (%)			33.33%	21				NO			25.00%	4	
B.1.15.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/FL (%)			100.00%	25				YES			75.00%	12	
B.1.15.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/FL (%)			46.15%	26				NO			64.71%	17	
B.1.15.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/FL (%)											50.00%	2	
B.1.15.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/FL (%)			66.67%	39				NO			100.00%	28	
B.1.15.7.1	O-11	Line Sharing/EDI/FL (%)			100.00%	48				YES			100.00%	52	
B.1.15.7.2	O-11	Line Sharing/TAG/FL (%)			100.00%	18				YES			95.65%	23	
B.1.15.8.1	O-11	2W Analog Loop Design/EDI/FL (%)			99.54%	217				YES			99.72%	355	
B.1.15.8.2	O-11	2W Analog Loop Design/TAG/FL (%)			100.00%	130				YES			98.72%	78	
B.1.15.9.1	O-11	2W Analog Loop Non-Design/EDI/FL (%)			100.00%	6				YES					
B.1.15.9.2	O-11	2W Analog Loop Non-Design/TAG/FL (%)			99.74%	1,568				YES			99.71%	1,393	
B.1.15.10.1	O-11	2W Analog Loop w/INP Design/EDI/FL (%)													
B.1.15.10.2	O-11	2W Analog Loop w/INP Design/TAG/FL (%)													
B.1.15.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/FL (%)													
B.1.15.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/FL (%)													
B.1.15.12.1	O-11	2W Analog Loop w/LNP Design/EDI/FL (%)			99.58%	237				YES			99.65%	282	
B.1.15.12.2	O-11	2W Analog Loop w/LNP Design/TAG/FL (%)			100.00%	72				YES			100.00%	94	
B.1.15.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/FL (%)			100.00%	2				YES					
B.1.15.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/FL (%)			99.91%	1,094				YES			100.00%	1,419	
B.1.15.14.1	O-11	Other Design/EDI/FL (%)			99.28%	279				YES			99.12%	227	
B.1.15.14.2	O-11	Other Design/TAG/FL (%)			100.00%	95				YES			98.31%	59	
B.1.15.15.1	O-11	Other Non-Design/EDI/FL (%)			99.83%	9,442				YES			99.87%	6,956	
B.1.15.15.2	O-11	Other Non-Design/TAG/FL (%)			95.99%	598				YES			96.10%	282	
B.1.15.16.1	O-11	INP Standalone/EDI/FL (%)													
B.1.15.16.2	O-11	INP Standalone/TAG/FL (%)													
B.1.15.17.1	O-11	LNP Standalone/EDI/FL (%)			100.00%	1,389				YES			99.52%	1,451	
B.1.15.17.2	O-11	LNP Standalone/TAG/FL (%)			100.00%	207				YES			100.00%	256	
		FOC & Reject Response Completeness - Non-Mechanized													
B.1.16.1	O-11	Switch Ports/FL (%)											100.00%	1	
B.1.16.2	O-11	Local Interoffice Transport/FL (%)			96.97%	33				YES			100.00%	9	
B.1.16.3	O-11	Loop + Port Combinations/FL (%)			92.69%	3,023				NO			92.61%	2,287	
B.1.16.4	O-11	Combo Other/FL (%)			94.55%	110				NO			96.80%	125	
B.1.16.5	O-11	xDSL (ADSL, HDSL and UCL)/FL (%)			96.00%	100				YES			97.08%	274	
B.1.16.6	O-11	ISDN Loop (UDN, UDC)/FL (%)			96.81%	439				YES			97.67%	344	
B.1.16.7	O-11	Line Sharing/FL (%)			97.92%	48				YES			100.00%	53	
B.1.16.8	O-11	2W Analog Loop Design/FL (%)			95.51%	156				YES			98.21%	168	
B.1.16.9	O-11	2W Analog Loop Non-Design/FL (%)			96.18%	340				YES			97.02%	671	
B.1.16.10	O-11	2W Analog Loop w/INP Design/FL (%)			69.23%	13				NO					
B.1.16.11	O-11	2W Analog Loop w/INP Non-Design/FL (%)			75.00%	12				NO			77.78%	9	
B.1.16.12	O-11	2W Analog Loop w/LNP Design/FL (%)			94.12%	17				NO			89.19%	37	
B.1.16.13	O-11	2W Analog Loop w/LNP Non-Design/FL (%)			94.55%	55				NO			95.45%	44	
B.1.16.14	O-11	Other Design/FL (%)			96.32%	353				YES			98.23%	396	
B.1.16.15	O-11	Other Non-Design/FL (%)			98.21%	1,676				YES			98.29%	1,752	
B.1.16.16	O-11	INP Standalone/FL (%)			83.33%	60				NO			86.30%	73	
B.1.16.17	O-11	LNP Standalone/FL (%)			98.29%	818				YES			97.20%	930	
		FOC & Reject Response Completeness (Multiple Responses) - Mechanized													
B.1.17.1.1	O-11	Switch Ports/EDI/FL (%)													
B.1.17.1.2	O-11	Switch Ports/TAG/FL (%)													
B.1.17.2.1	O-11	Local Interoffice Transport/EDI/FL (%)													
B.1.17.2.2	O-11	Local Interoffice Transport/TAG/FL (%)													
B.1.17.3.1	O-11	Loop + Port Combinations/EDI/FL (%)			98.38%	29,285				YES			99.51%	20,867	
B.1.17.3.2	O-11	Loop + Port Combinations/TAG/FL (%)			99.58%	46,907				YES			99.89%	35,958	
B.1.17.4.1	O-11	Combo Other/EDI/FL (%)			100.00%	2				YES			60.00%	5	
B.1.17.4.2	O-11	Combo Other/TAG/FL (%)											95.45%	22	
B.1.17.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/FL (%)			99.30%	426				YES			98.70%	463	
B.1.17.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/FL (%)			94.57%	313				NO			90.94%	342	

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - May 2002

						May							
			Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.1.14.17.1	O-11	LNP Standalone/EDI/FL (%)			YES			99.31%	3,486				YES
B.1.14.17.2	O-11	LNP Standalone/TAG/FL (%)			YES			97.10%	310				YES
		FOC & Reject Response Completeness - Partially Mechanized											
B.1.15.1.1	O-11	Switch Ports/EDI/FL (%)											
B.1.15.1.2	O-11	Switch Ports/TAG/FL (%)											
B.1.15.2.1	O-11	Local Interoffice Transport/EDI/FL (%)											
B.1.15.2.2	O-11	Local Interoffice Transport/TAG/FL (%)											
B.1.15.3.1	O-11	Loop + Port Combinations/EDI/FL (%)			YES			95.98%	3,755				YES
B.1.15.3.2	O-11	Loop + Port Combinations/TAG/FL (%)			YES			99.83%	13,968				YES
B.1.15.4.1	O-11	Combo Other/EDI/FL (%)			YES								
B.1.15.4.2	O-11	Combo Other/TAG/FL (%)			NO								
B.1.15.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/FL (%)			NO			73.58%	53				NO
B.1.15.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/FL (%)			NO			78.79%	33				NO
B.1.15.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/FL (%)			NO			100.00%	18				YES
B.1.15.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/FL (%)			YES			99.60%	250				YES
B.1.15.7.1	O-11	Line Sharing/EDI/FL (%)			YES			97.58%	124				YES
B.1.15.7.2	O-11	Line Sharing/TAG/FL (%)			YES			98.15%	108				YES
B.1.15.8.1	O-11	2W Analog Loop Design/EDI/FL (%)			YES			95.41%	196				YES
B.1.15.8.2	O-11	2W Analog Loop Design/TAG/FL (%)			YES			100.00%	94				YES
B.1.15.9.1	O-11	2W Analog Loop Non-Design/EDI/FL (%)											
B.1.15.9.2	O-11	2W Analog Loop Non-Design/TAG/FL (%)			YES			99.91%	1,167				YES
B.1.15.10.1	O-11	2W Analog Loop w/INP Design/EDI/FL (%)											
B.1.15.10.2	O-11	2W Analog Loop w/INP Design/TAG/FL (%)											
B.1.15.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/FL (%)											
B.1.15.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/FL (%)											
B.1.15.12.1	O-11	2W Analog Loop w/LNP Design/EDI/FL (%)			YES			100.00%	605				YES
B.1.15.12.2	O-11	2W Analog Loop w/LNP Design/TAG/FL (%)			YES			100.00%	152				YES
B.1.15.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/FL (%)											
B.1.15.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/FL (%)			YES			100.00%	2,211				YES
B.1.15.14.1	O-11	Other Design/EDI/FL (%)			YES			93.08%	159				NO
B.1.15.14.2	O-11	Other Design/TAG/FL (%)			YES			100.00%	86				YES
B.1.15.15.1	O-11	Other Non-Design/EDI/FL (%)			YES			94.81%	7,193				NO
B.1.15.15.2	O-11	Other Non-Design/TAG/FL (%)			YES			96.37%	468				YES
B.1.15.16.1	O-11	INP Standalone/EDI/FL (%)											
B.1.15.16.2	O-11	INP Standalone/TAG/FL (%)											
B.1.15.17.1	O-11	LNP Standalone/EDI/FL (%)			YES			99.87%	1,511				YES
B.1.15.17.2	O-11	LNP Standalone/TAG/FL (%)			YES			99.49%	390				YES
		FOC & Reject Response Completeness - Non-Mechanized											
B.1.16.1	O-11	Switch Ports/FL (%)			YES			100.00%	4				YES
B.1.16.2	O-11	Local Interoffice Transport/FL (%)			YES			100.00%	45				YES
B.1.16.3	O-11	Loop + Port Combinations/FL (%)			NO			91.41%	2,084				NO
B.1.16.4	O-11	Combo Other/FL (%)			YES			95.35%	43				YES
B.1.16.5	O-11	xDSL (ADSL, HDSL and UCL)/FL (%)			YES			94.70%	283				NO
B.1.16.6	O-11	ISDN Loop (UDN, UDC)/FL (%)			YES			93.47%	475				NO
B.1.16.7	O-11	Line Sharing/FL (%)			YES			99.12%	114				YES
B.1.16.8	O-11	2W Analog Loop Design/FL (%)			YES			95.06%	263				YES
B.1.16.9	O-11	2W Analog Loop Non-Design/FL (%)			YES			91.72%	906				NO
B.1.16.10	O-11	2W Analog Loop w/INP Design/FL (%)						66.67%	6				NO
B.1.16.11	O-11	2W Analog Loop w/INP Non-Design/FL (%)			NO			80.00%	5				NO
B.1.16.12	O-11	2W Analog Loop w/LNP Design/FL (%)			NO			95.28%	106				YES
B.1.16.13	O-11	2W Analog Loop w/LNP Non-Design/FL (%)			YES			90.59%	85				NO
B.1.16.14	O-11	Other Design/FL (%)			YES			95.40%	587				YES
B.1.16.15	O-11	Other Non-Design/FL (%)			YES			96.71%	1,882				YES
B.1.16.16	O-11	INP Standalone/FL (%)			NO			86.36%	88				NO
B.1.16.17	O-11	LNP Standalone/FL (%)			YES			88.91%	911				NO
		FOC & Reject Response Completeness (Multiple Responses) - Mechanized											
B.1.17.1.1	O-11	Switch Ports/EDI/FL (%)											
B.1.17.1.2	O-11	Switch Ports/TAG/FL (%)											
B.1.17.2.1	O-11	Local Interoffice Transport/EDI/FL (%)											
B.1.17.2.2	O-11	Local Interoffice Transport/TAG/FL (%)											
B.1.17.3.1	O-11	Loop + Port Combinations/EDI/FL (%)			YES			99.79%	12,749				YES
B.1.17.3.2	O-11	Loop + Port Combinations/TAG/FL (%)			YES			99.54%	30,278				YES
B.1.17.4.1	O-11	Combo Other/EDI/FL (%)			NO								
B.1.17.4.2	O-11	Combo Other/TAG/FL (%)			YES								
B.1.17.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/FL (%)			YES			98.03%	356				YES
B.1.17.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/FL (%)			NO			95.48%	199				YES

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - May 2002

			Benchmark/ Analog	August							
				BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.1.17.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/FL (%)	>= 95%			100.00%	10				YES
B.1.17.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/FL (%)	>= 95%			90.70%	129				NO
B.1.17.7.1	O-11	Line Sharing/EDI/FL (%)	>= 95%			89.52%	210				NO
B.1.17.7.2	O-11	Line Sharing/TAG/FL (%)	>= 95%			95.56%	45				YES
B.1.17.8.1	O-11	2W Analog Loop Design/EDI/FL (%)	>= 95%			96.53%	202				YES
B.1.17.8.2	O-11	2W Analog Loop Design/TAG/FL (%)	>= 95%			98.53%	816				YES
B.1.17.9.1	O-11	2W Analog Loop Non-Design/EDI/FL (%)	>= 95%			100.00%	4				YES
B.1.17.9.2	O-11	2W Analog Loop Non-Design/TAG/FL (%)	>= 95%			99.17%	960				YES
B.1.17.10.1	O-11	2W Analog Loop w/INP Design/EDI/FL (%)	>= 95%								
B.1.17.10.2	O-11	2W Analog Loop w/INP Design/TAG/FL (%)	>= 95%								
B.1.17.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/FL (%)	>= 95%								
B.1.17.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/FL (%)	>= 95%								
B.1.17.12.1	O-11	2W Analog Loop w/LNP Design/EDI/FL (%)	>= 95%			78.57%	28				NO
B.1.17.12.2	O-11	2W Analog Loop w/LNP Design/TAG/FL (%)	>= 95%			100.00%	2				YES
B.1.17.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/FL (%)	>= 95%			100.00%	6				YES
B.1.17.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/FL (%)	>= 95%			100.00%	123				YES
B.1.17.14.1	O-11	Other Design/EDI/FL (%)	>= 95%			85.59%	111				NO
B.1.17.14.2	O-11	Other Design/TAG/FL (%)	>= 95%			96.85%	222				YES
B.1.17.15.1	O-11	Other Non-Design/EDI/FL (%)	>= 95%			92.00%	14,171				NO
B.1.17.15.2	O-11	Other Non-Design/TAG/FL (%)	>= 95%			99.81%	1,576				YES
B.1.17.16.1	O-11	INP Standalone/EDI/FL (%)	>= 95%			0.00%	1				NO
B.1.17.16.2	O-11	INP Standalone/TAG/FL (%)	>= 95%								
B.1.17.17.1	O-11	LNP Standalone/EDI/FL (%)	>= 95%			68.56%	3,356				NO
B.1.17.17.2	O-11	LNP Standalone/TAG/FL (%)	>= 95%			62.83%	226				NO
		FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized									
B.1.18.1.1	O-11	Switch Ports/EDI/FL (%)	>= 95%								
B.1.18.1.2	O-11	Switch Ports/TAG/FL (%)	>= 95%								
B.1.18.2.1	O-11	Local Interoffice Transport/EDI/FL (%)	>= 95%								
B.1.18.2.2	O-11	Local Interoffice Transport/TAG/FL (%)	>= 95%								
B.1.18.3.1	O-11	Loop + Port Combinations/EDI/FL (%)	>= 95%			93.00%	3,984				NO
B.1.18.3.2	O-11	Loop + Port Combinations/TAG/FL (%)	>= 95%			92.46%	11,871				NO
B.1.18.4.1	O-11	Combo Other/EDI/FL (%)	>= 95%			80.65%	31				NO
B.1.18.4.2	O-11	Combo Other/TAG/FL (%)	>= 95%			84.62%	13				NO
B.1.18.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/FL (%)	>= 95%			85.71%	14				NO
B.1.18.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/FL (%)	>= 95%			66.67%	9				NO
B.1.18.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/FL (%)	>= 95%			100.00%	1				YES
B.1.18.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/FL (%)	>= 95%			83.33%	18				NO
B.1.18.7.1	O-11	Line Sharing/EDI/FL (%)	>= 95%			88.89%	54				NO
B.1.18.7.2	O-11	Line Sharing/TAG/FL (%)	>= 95%			80.95%	21				NO
B.1.18.8.1	O-11	2W Analog Loop Design/EDI/FL (%)	>= 95%			90.48%	147				NO
B.1.18.8.2	O-11	2W Analog Loop Design/TAG/FL (%)	>= 95%			92.13%	89				NO
B.1.18.9.1	O-11	2W Analog Loop Non-Design/EDI/FL (%)	>= 95%			90.00%	10				NO
B.1.18.9.2	O-11	2W Analog Loop Non-Design/TAG/FL (%)	>= 95%			96.31%	1,678				YES
B.1.18.10.1	O-11	2W Analog Loop w/INP Design/EDI/FL (%)	>= 95%								
B.1.18.10.2	O-11	2W Analog Loop w/INP Design/TAG/FL (%)	>= 95%								
B.1.18.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/FL (%)	>= 95%								
B.1.18.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/FL (%)	>= 95%								
B.1.18.12.1	O-11	2W Analog Loop w/LNP Design/EDI/FL (%)	>= 95%			55.56%	162				NO
B.1.18.12.2	O-11	2W Analog Loop w/LNP Design/TAG/FL (%)	>= 95%			80.49%	41				NO
B.1.18.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/FL (%)	>= 95%			53.85%	13				NO
B.1.18.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/FL (%)	>= 95%			69.84%	1,124				NO
B.1.18.14.1	O-11	Other Design/EDI/FL (%)	>= 95%			91.70%	253				NO
B.1.18.14.2	O-11	Other Design/TAG/FL (%)	>= 95%			90.22%	92				NO
B.1.18.15.1	O-11	Other Non-Design/EDI/FL (%)	>= 95%			98.82%	7,178				YES
B.1.18.15.2	O-11	Other Non-Design/TAG/FL (%)	>= 95%			96.21%	713				YES
B.1.18.16.1	O-11	INP Standalone/EDI/FL (%)	>= 95%								
B.1.18.16.2	O-11	INP Standalone/TAG/FL (%)	>= 95%								
B.1.18.17.1	O-11	LNP Standalone/EDI/FL (%)	>= 95%			85.29%	1,285				NO
B.1.18.17.2	O-11	LNP Standalone/TAG/FL (%)	>= 95%			76.85%	216				NO
		FOC & Reject Response Completeness (Multiple Responses) - Non-Mechanized									
B.1.19.1	O-11	Switch Ports/FL (%)	>= 95%								
B.1.19.2	O-11	Local Interoffice Transport/FL (%)	>= 95%			89.47%	38				NO
B.1.19.3	O-11	Loop + Port Combinations/FL (%)	>= 95%			92.15%	2,802				NO
B.1.19.4	O-11	Combo Other/FL (%)	>= 95%			90.11%	182				NO
B.1.19.5	O-11	xDSL (ADSL, HDSL and UCL)/FL (%)	>= 95%			84.52%	84				NO
B.1.19.6	O-11	ISDN Loop (UDN, UDC)/FL (%)	>= 95%			93.50%	277				NO
B.1.19.7	O-11	Line Sharing/FL (%)	>= 95%			92.86%	28				NO

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - May 2002

			July								June				
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation
B.1.17.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/FL (%)			100.00%	20				YES			100.00%	13	
B.1.17.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/FL (%)			97.04%	169				YES			98.28%	58	
B.1.17.7.1	O-11	Line Sharing/EDI/FL (%)			96.70%	182				YES			94.51%	182	
B.1.17.7.2	O-11	Line Sharing/TAG/FL (%)			97.62%	42				YES			95.35%	86	
B.1.17.8.1	O-11	2W Analog Loop Design/EDI/FL (%)			97.16%	211				YES			92.75%	331	
B.1.17.8.2	O-11	2W Analog Loop Design/TAG/FL (%)			98.92%	740				YES			98.35%	484	
B.1.17.9.1	O-11	2W Analog Loop Non-Design/EDI/FL (%)			50.00%	2				NO					
B.1.17.9.2	O-11	2W Analog Loop Non-Design/TAG/FL (%)			98.63%	1,388				YES			98.63%	947	
B.1.17.10.1	O-11	2W Analog Loop w/INP Design/EDI/FL (%)													
B.1.17.10.2	O-11	2W Analog Loop w/INP Design/TAG/FL (%)													
B.1.17.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/FL (%)													
B.1.17.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/FL (%)													
B.1.17.12.1	O-11	2W Analog Loop w/LNP Design/EDI/FL (%)			75.00%	20				NO			96.88%	32	
B.1.17.12.2	O-11	2W Analog Loop w/LNP Design/TAG/FL (%)			100.00%	2				YES			90.00%	10	
B.1.17.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/FL (%)													
B.1.17.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/FL (%)			100.00%	96				YES			99.03%	103	
B.1.17.14.1	O-11	Other Design/EDI/FL (%)			93.18%	88				NO			93.52%	108	
B.1.17.14.2	O-11	Other Design/TAG/FL (%)			97.48%	159				YES			96.13%	181	
B.1.17.15.1	O-11	Other Non-Design/EDI/FL (%)			92.91%	14,247				NO			95.87%	11,636	
B.1.17.15.2	O-11	Other Non-Design/TAG/FL (%)			99.92%	1,220				YES			98.39%	809	
B.1.17.16.1	O-11	INP Standalone/EDI/FL (%)													
B.1.17.16.2	O-11	INP Standalone/TAG/FL (%)			100.00%	2				YES			100.00%	1	
B.1.17.17.1	O-11	LNP Standalone/EDI/FL (%)			65.04%	3,106				NO			66.82%	2,851	
B.1.17.17.2	O-11	LNP Standalone/TAG/FL (%)			57.84%	185				NO			69.51%	164	
		FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized													
B.1.18.1.1	O-11	Switch Ports/EDI/FL (%)													
B.1.18.1.2	O-11	Switch Ports/TAG/FL (%)													
B.1.18.2.1	O-11	Local Interoffice Transport/EDI/FL (%)													
B.1.18.2.2	O-11	Local Interoffice Transport/TAG/FL (%)													
B.1.18.3.1	O-11	Loop + Port Combinations/EDI/FL (%)			95.27%	5,396				YES			97.75%	6,433	
B.1.18.3.2	O-11	Loop + Port Combinations/TAG/FL (%)			94.37%	9,709				NO			94.80%	10,975	
B.1.18.4.1	O-11	Combo Other/EDI/FL (%)			90.91%	11				NO			100.00%	2	
B.1.18.4.2	O-11	Combo Other/TAG/FL (%)			85.71%	7				NO			0.00%	1	
B.1.18.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/FL (%)			88.00%	25				NO			77.78%	9	
B.1.18.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/FL (%)			58.33%	12				NO			45.45%	11	
B.1.18.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/FL (%)											100.00%	1	
B.1.18.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/FL (%)			88.46%	26				NO			92.86%	28	
B.1.18.7.1	O-11	Line Sharing/EDI/FL (%)			95.83%	48				YES			94.23%	52	
B.1.18.7.2	O-11	Line Sharing/TAG/FL (%)			100.00%	18				YES			95.45%	22	
B.1.18.8.1	O-11	2W Analog Loop Design/EDI/FL (%)			87.96%	216				NO			93.50%	354	
B.1.18.8.2	O-11	2W Analog Loop Design/TAG/FL (%)			90.77%	130				NO			97.40%	77	
B.1.18.9.1	O-11	2W Analog Loop Non-Design/EDI/FL (%)			83.33%	6				NO					
B.1.18.9.2	O-11	2W Analog Loop Non-Design/TAG/FL (%)			95.01%	1,564				YES			95.82%	1,389	
B.1.18.10.1	O-11	2W Analog Loop w/INP Design/EDI/FL (%)													
B.1.18.10.2	O-11	2W Analog Loop w/INP Design/TAG/FL (%)													
B.1.18.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/FL (%)													
B.1.18.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/FL (%)													
B.1.18.12.1	O-11	2W Analog Loop w/LNP Design/EDI/FL (%)			58.90%	236				NO			54.09%	281	
B.1.18.12.2	O-11	2W Analog Loop w/LNP Design/TAG/FL (%)			65.28%	72				NO			84.04%	94	
B.1.18.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/FL (%)			50.00%	2				NO					
B.1.18.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/FL (%)			58.65%	1,093				NO			73.43%	1,419	
B.1.18.14.1	O-11	Other Design/EDI/FL (%)			85.20%	277				NO			85.78%	225	
B.1.18.14.2	O-11	Other Design/TAG/FL (%)			86.32%	95				NO			91.38%	58	
B.1.18.15.1	O-11	Other Non-Design/EDI/FL (%)			98.84%	9,426				YES			99.05%	6,947	
B.1.18.15.2	O-11	Other Non-Design/TAG/FL (%)			94.25%	574				NO			97.42%	271	
B.1.18.16.1	O-11	INP Standalone/EDI/FL (%)													
B.1.18.16.2	O-11	INP Standalone/TAG/FL (%)													
B.1.18.17.1	O-11	LNP Standalone/EDI/FL (%)			81.79%	1,389				NO			81.93%	1,444	
B.1.18.17.2	O-11	LNP Standalone/TAG/FL (%)			69.08%	207				NO			77.34%	256	
		FOC & Reject Response Completeness (Multiple Responses) - Non-Mechanized													
B.1.19.1	O-11	Switch Ports/FL (%)											100.00%	1	
B.1.19.2	O-11	Local Interoffice Transport/FL (%)			87.50%	32				NO			100.00%	9	
B.1.19.3	O-11	Loop + Port Combinations/FL (%)			90.36%	2,802				NO			89.94%	2,118	
B.1.19.4	O-11	Combo Other/FL (%)			83.65%	104				NO			83.47%	121	
B.1.19.5	O-11	xDSL (ADSL, HDSL and UCL)/FL (%)			89.58%	96				NO			93.98%	266	
B.1.19.6	O-11	ISDN Loop (UDN, UDC)/FL (%)			93.41%	425				NO			92.86%	336	
B.1.19.7	O-11	Line Sharing/FL (%)			97.87%	47				YES			92.45%	53	

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - May 2002

						May							
			Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.1.17.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/FL (%)			YES			100.00%	3				YES
B.1.17.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/FL (%)			YES			92.59%	54				NO
B.1.17.7.1	O-11	Line Sharing/EDI/FL (%)			NO			96.23%	159				YES
B.1.17.7.2	O-11	Line Sharing/TAG/FL (%)			YES			95.59%	68				YES
B.1.17.8.1	O-11	2W Analog Loop Design/EDI/FL (%)			NO			96.68%	301				YES
B.1.17.8.2	O-11	2W Analog Loop Design/TAG/FL (%)			YES			97.13%	593				YES
B.1.17.9.1	O-11	2W Analog Loop Non-Design/EDI/FL (%)											
B.1.17.9.2	O-11	2W Analog Loop Non-Design/TAG/FL (%)			YES			98.46%	911				YES
B.1.17.10.1	O-11	2W Analog Loop w/INP Design/EDI/FL (%)											
B.1.17.10.2	O-11	2W Analog Loop w/INP Design/TAG/FL (%)											
B.1.17.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/FL (%)											
B.1.17.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/FL (%)						100.00%	3				YES
B.1.17.12.1	O-11	2W Analog Loop w/LNP Design/EDI/FL (%)			YES			100.00%	83				YES
B.1.17.12.2	O-11	2W Analog Loop w/LNP Design/TAG/FL (%)			NO			91.67%	12				NO
B.1.17.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/FL (%)											
B.1.17.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/FL (%)			YES			99.12%	228				YES
B.1.17.14.1	O-11	Other Design/EDI/FL (%)			NO			98.39%	124				YES
B.1.17.14.2	O-11	Other Design/TAG/FL (%)			YES			95.88%	194				YES
B.1.17.15.1	O-11	Other Non-Design/EDI/FL (%)			YES			97.25%	13,121				YES
B.1.17.15.2	O-11	Other Non-Design/TAG/FL (%)			YES			97.31%	1,117				YES
B.1.17.16.1	O-11	INP Standalone/EDI/FL (%)						100.00%	1				YES
B.1.17.16.2	O-11	INP Standalone/TAG/FL (%)			YES								
B.1.17.17.1	O-11	LNP Standalone/EDI/FL (%)			NO			72.67%	3,462				NO
B.1.17.17.2	O-11	LNP Standalone/TAG/FL (%)			NO			70.43%	301				NO
	FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized												
B.1.18.1.1	O-11	Switch Ports/EDI/FL (%)											
B.1.18.1.2	O-11	Switch Ports/TAG/FL (%)											
B.1.18.2.1	O-11	Local Interoffice Transport/EDI/FL (%)											
B.1.18.2.2	O-11	Local Interoffice Transport/TAG/FL (%)											
B.1.18.3.1	O-11	Loop + Port Combinations/EDI/FL (%)			YES			97.81%	3,604				YES
B.1.18.3.2	O-11	Loop + Port Combinations/TAG/FL (%)			NO			95.60%	13,944				YES
B.1.18.4.1	O-11	Combo Other/EDI/FL (%)			YES								
B.1.18.4.2	O-11	Combo Other/TAG/FL (%)			NO								
B.1.18.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/FL (%)			NO			97.44%	39				YES
B.1.18.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/FL (%)			NO			92.31%	26				NO
B.1.18.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/FL (%)			YES			100.00%	18				YES
B.1.18.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/FL (%)			NO			92.77%	249				NO
B.1.18.7.1	O-11	Line Sharing/EDI/FL (%)			NO			93.39%	121				NO
B.1.18.7.2	O-11	Line Sharing/TAG/FL (%)			YES			86.79%	106				NO
B.1.18.8.1	O-11	2W Analog Loop Design/EDI/FL (%)			NO			97.33%	187				YES
B.1.18.8.2	O-11	2W Analog Loop Design/TAG/FL (%)			YES			94.68%	94				NO
B.1.18.9.1	O-11	2W Analog Loop Non-Design/EDI/FL (%)											
B.1.18.9.2	O-11	2W Analog Loop Non-Design/TAG/FL (%)			YES			92.62%	1,166				NO
B.1.18.10.1	O-11	2W Analog Loop w/INP Design/EDI/FL (%)											
B.1.18.10.2	O-11	2W Analog Loop w/INP Design/TAG/FL (%)											
B.1.18.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/FL (%)											
B.1.18.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/FL (%)											
B.1.18.12.1	O-11	2W Analog Loop w/LNP Design/EDI/FL (%)			NO			64.63%	605				NO
B.1.18.12.2	O-11	2W Analog Loop w/LNP Design/TAG/FL (%)			NO			80.92%	152				NO
B.1.18.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/FL (%)											
B.1.18.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/FL (%)			NO			66.03%	2,211				NO
B.1.18.14.1	O-11	Other Design/EDI/FL (%)			NO			98.65%	148				YES
B.1.18.14.2	O-11	Other Design/TAG/FL (%)			NO			82.56%	86				NO
B.1.18.15.1	O-11	Other Non-Design/EDI/FL (%)			YES			99.25%	6,820				YES
B.1.18.15.2	O-11	Other Non-Design/TAG/FL (%)			YES			96.23%	451				YES
B.1.18.16.1	O-11	INP Standalone/EDI/FL (%)											
B.1.18.16.2	O-11	INP Standalone/TAG/FL (%)											
B.1.18.17.1	O-11	LNP Standalone/EDI/FL (%)			NO			84.96%	1,509				NO
B.1.18.17.2	O-11	LNP Standalone/TAG/FL (%)			NO			78.35%	388				NO
	FOC & Reject Response Completeness (Multiple Responses) - Non-Mechanized												
B.1.19.1	O-11	Switch Ports/FL (%)			YES			100.00%	4				YES
B.1.19.2	O-11	Local Interoffice Transport/FL (%)			YES			77.78%	45				NO
B.1.19.3	O-11	Loop + Port Combinations/FL (%)			NO			88.92%	1,905				NO
B.1.19.4	O-11	Combo Other/FL (%)			NO			85.37%	41				NO
B.1.19.5	O-11	xDSL (ADSL, HDSL and UCL)/FL (%)			NO			98.13%	268				YES
B.1.19.6	O-11	ISDN Loop (UDN, UDC)/FL (%)			NO			89.86%	444				NO
B.1.19.7	O-11	Line Sharing/FL (%)			NO			91.15%	113				NO

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - May 2002

			Benchmark/ Analog	August BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.1.19.8	O-11	2W Analog Loop Design/FL (%)	>= 95%			91.57%	166				NO
B.1.19.9	O-11	2W Analog Loop Non-Design/FL (%)	>= 95%			94.21%	449				NO
B.1.19.10	O-11	2W Analog Loop w/INP Design/FL (%)	>= 95%			66.67%	3				NO
B.1.19.11	O-11	2W Analog Loop w/INP Non-Design/FL (%)	>= 95%			100.00%	4				YES
B.1.19.12	O-11	2W Analog Loop w/LNP Design/FL (%)	>= 95%			89.47%	38				NO
B.1.19.13	O-11	2W Analog Loop w/LNP Non-Design/FL (%)	>= 95%			93.75%	48				NO
B.1.19.14	O-11	Other Design/FL (%)	>= 95%			93.88%	392				NO
B.1.19.15	O-11	Other Non-Design/FL (%)	>= 95%			95.79%	1,354				YES
B.1.19.16	O-11	INP Standalone/FL (%)	>= 95%			95.00%	40				YES
B.1.19.17	O-11	LNP Standalone/FL (%)	>= 95%			94.22%	882				NO

Unbundled Network Elements - Provisioning

Order Completion Interval

B.2.1.1.1.1	P-4	Switch Ports/<10 circuits/Dispatch/FL (days)	R&B (POTS)	4.54	59,577			5.447			
B.2.1.1.1.2	P-4	Switch Ports/<10 circuits/Non-Dispatch/FL (days)	R&B (POTS)	0.86	717,719			1.285			
B.2.1.1.2.1	P-4	Switch Ports/>=10 circuits/Dispatch/FL (days)	R&B (POTS)	11.42	318			18.832			
B.2.1.1.2.2	P-4	Switch Ports/>=10 circuits/Non-Dispatch/FL (days)	R&B (POTS)	1.00	22			1.392			
B.2.1.2.1.1	P-4	Local Interoffice Transport/<10 circuits/Dispatch/FL (days)	DS1/DS3	15.33	2,385	21.00	4	12.923	6.46669	-0.8764	YES
B.2.1.2.1.2	P-4	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL (days)	DS1/DS3								
B.2.1.2.2.1	P-4	Local Interoffice Transport/>=10 circuits/Dispatch/FL (days)	DS1/DS3								
B.2.1.2.2.2	P-4	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL (days)	DS1/DS3								
B.2.1.3.1.1	P-4	Loop + Port Combinations/<10 circuits/Dispatch/FL (days)	R&B	4.50	61,633	3.97	1,635	5.586	0.13998	3.7951	YES
B.2.1.3.1.2	P-4	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL (days)	R&B	0.87	720,347	0.79	47,428	1.398	0.00663	12.7488	YES
B.2.1.3.1.3	P-4	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL (days)	R&B	0.33	400,933	0.33	23,517	0.000	0.00000		YES
B.2.1.3.1.4	P-4	Loop + Port Combinations/<10 circuits/Dispatch In/FL (days)	R&B	1.55	319,414	1.23	23,911	1.894	0.01270	24.7006	YES
B.2.1.3.2.1	P-4	Loop + Port Combinations/>=10 circuits/Dispatch/FL (days)	R&B	11.32	369	3.37	9	17.986	6.06787	1.3098	YES
B.2.1.3.2.2	P-4	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL (days)	R&B	2.67	285	6.50	2	4.627	3.28337	-1.1668	YES
B.2.1.3.2.3	P-4	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL (days)	R&B	0.33	116			0.000			
B.2.1.3.2.4	P-4	Loop + Port Combinations/>=10 circuits/Dispatch In/FL (days)	R&B	4.27	169	6.50	2	5.463	3.88557	-0.5734	YES
B.2.1.4.1.1	P-4	Combo Other/<10 circuits/Dispatch/FL (days)	R&B&D - Disp	5.38	64,325	11.44	68	8.668	1.05176	-5.7646	NO
B.2.1.4.1.4	P-4	Combo Other/<10 circuits/Dispatch In/FL (days)	R&B&D - Disp	5.38	64,325			8.668			
B.2.1.4.2.1	P-4	Combo Other/>=10 circuits/Dispatch/FL (days)	R&B&D - Disp	16.54	636			18.275			
B.2.1.4.2.4	P-4	Combo Other/>=10 circuits/Dispatch In/FL (days)	R&B&D - Disp	16.54	636			18.275			
B.2.1.6.3.1	P-4	UNE ISDN/<6 circuits/Dispatch/FL (days)	ISDN - BRI	13.64	246	10.69	205	9.525	0.90076	3.2770	YES
B.2.1.6.3.2	P-4	UNE ISDN/<6 circuits/Non-Dispatch/FL (days)	ISDN - BRI	4.66	338			6.323			
B.2.1.6.4.1	P-4	UNE ISDN/6-13 circuits/Dispatch/FL (days)	ISDN - BRI	12.00	1			0.000			
B.2.1.6.4.2	P-4	UNE ISDN/6-13 circuits/Non-Dispatch/FL (days)	ISDN - BRI	2.00	2			1.414			
B.2.1.6.5.1	P-4	UNE ISDN/>=14 circuits/Dispatch/FL (days)	ISDN - BRI								
B.2.1.6.5.2	P-4	UNE ISDN/>=14 circuits/Non-Dispatch/FL (days)	ISDN - BRI								
B.2.1.7.3.1	P-4	Line Sharing/<6 circuits/Dispatch/FL (days)	ADSL to Retail	2.58	9,474	3.45	22	2.017	0.43048	-2.0360	NO
B.2.1.7.3.2	P-4	Line Sharing/<6 circuits/Non-Dispatch/FL (days)	ADSL to Retail	2.20	6,263	2.16	135	0.778	0.06769	0.6347	YES
B.2.1.7.4.1	P-4	Line Sharing/6-13 circuits/Dispatch/FL (days)	ADSL to Retail	2.07	15			0.704			
B.2.1.7.4.2	P-4	Line Sharing/6-13 circuits/Non-Dispatch/FL (days)	ADSL to Retail								
B.2.1.7.5.1	P-4	Line Sharing/>=14 circuits/Dispatch/FL (days)	ADSL to Retail	3.00	1			0.000			
B.2.1.7.5.2	P-4	Line Sharing/>=14 circuits/Non-Dispatch/FL (days)	ADSL to Retail								
B.2.1.8.1.1	P-4	2W Analog Loop Design/<10 circuits/Dispatch/FL (days)	R&B - Disp	4.50	61,633	4.58	202	5.586	0.39370	-0.2069	YES
B.2.1.8.1.2	P-4	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL (days)	R&B - Disp	4.50	61,633			5.586			
B.2.1.8.2.1	P-4	2W Analog Loop Design/>=10 circuits/Dispatch/FL (days)	R&B - Disp	11.32	369	6.00	2	17.986	12.75215	0.4170	YES
B.2.1.8.2.2	P-4	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL (days)	R&B - Disp	11.32	369			17.986			
B.2.1.9.1.1	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL (days)	R&B (POTS) excl SB Or	4.54	59,577	3.75	534	5.447	0.23677	3.3597	YES
B.2.1.9.1.4	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL (days)	R&B (POTS) excl SB Or	1.52	317,662	3.50	4	1.716	0.85777	-2.3060	NO
B.2.1.9.2.1	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL (days)	R&B (POTS) excl SB Or	11.42	318	7.92	12	18.832	5.53789	0.6321	YES
B.2.1.9.2.4	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL (days)	R&B (POTS) excl SB Or	2.43	7	14.00	1	1.813	1.93781	-5.9714	NO
B.2.1.10.1.1	P-4	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL (days)	R&B - Disp	4.50	61,633			5.586			
B.2.1.10.1.2	P-4	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL (days)	R&B - Disp	4.50	61,633			5.586			
B.2.1.10.2.1	P-4	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL (days)	R&B - Disp	11.32	369			17.986			
B.2.1.10.2.2	P-4	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL (days)	R&B - Disp	11.32	369			17.986			
B.2.1.11.1.1	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL (days)	R&B (POTS) excl SB Or	4.54	59,577	4.00	1	5.447	5.44713	0.0997	YES
B.2.1.11.1.4	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL (days)	R&B (POTS) excl SB Or	1.52	317,662			1.716			
B.2.1.11.2.1	P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL (days)	R&B (POTS) excl SB Or	11.42	318			18.832			
B.2.1.11.2.4	P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL (days)	R&B (POTS) excl SB Or	2.43	7			1.813			
B.2.1.12.1.1	P-4	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL (days)	R&B - Disp	4.50	61,633	5.46	52	5.586	0.77501	-1.2372	YES
B.2.1.12.1.2	P-4	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL (days)	R&B - Disp	4.50	61,633			5.586			
B.2.1.12.2.1	P-4	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL (days)	R&B - Disp	11.32	369	9.00	1	17.986	18.00994	0.1287	YES
B.2.1.12.2.2	P-4	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL (days)	R&B - Disp	11.32	369			17.986			
B.2.1.13.1.1	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL (days)	R&B (POTS) excl SB Or	4.54	59,577	5.32	158	5.447	0.43392	-1.7964	NO
B.2.1.13.1.4	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL (days)	R&B (POTS) excl SB Or	1.52	317,662	5.27	136	1.716	0.14714	-25.4872	NO

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - May 2002

			July								June				
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation
B.1.19.8	O-11	2W Analog Loop Design/FL (%)			89.26%	149				NO			92.12%	165	
B.1.19.9	O-11	2W Analog Loop Non-Design/FL (%)			88.38%	327				NO			93.86%	651	
B.1.19.10	O-11	2W Analog Loop w/INP Design/FL (%)			77.78%	9				NO					
B.1.19.11	O-11	2W Analog Loop w/INP Non-Design/FL (%)			100.00%	9				YES			100.00%	7	
B.1.19.12	O-11	2W Analog Loop w/LNP Design/FL (%)			87.50%	16				NO			81.82%	33	
B.1.19.13	O-11	2W Analog Loop w/LNP Non-Design/FL (%)			90.38%	52				NO			90.46%	42	
B.1.19.14	O-11	Other Design/FL (%)			93.24%	340				NO			94.86%	389	
B.1.19.15	O-11	Other Non-Design/FL (%)			95.50%	1,646				YES			95.59%	1,722	
B.1.19.16	O-11	INP Standalone/FL (%)			96.00%	50				YES			92.06%	63	
B.1.19.17	O-11	LNP Standalone/FL (%)			95.27%	804				YES			94.03%	904	
		Unbundled Network Elements - Provisioning													
		Order Completion Interval													
B.2.1.1.1.1	P-4	Switch Ports/<10 circuits/Dispatch/FL (days)	5.31	52,380			6.264				4.81	47,314			5.932
B.2.1.1.1.2	P-4	Switch Ports/<10 circuits/Non-Dispatch/FL (days)	0.87	681,545			1.274				0.88	643,741			1.367
B.2.1.1.2.1	P-4	Switch Ports/>=10 circuits/Dispatch/FL (days)	10.18	331			11.619				9.89	313			13.253
B.2.1.1.2.2	P-4	Switch Ports/>=10 circuits/Non-Dispatch/FL (days)	0.50	8			0.309				13.72	18			29.920
B.2.1.2.1.1	P-4	Local Interoffice Transport/<10 circuits/Dispatch/FL (days)	14.80	2,079	14.50	6	12.523	5.11991	0.0585	YES	15.61	2,096	16.15	13	14.282
B.2.1.2.1.2	P-4	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL (days)													
B.2.1.2.2.1	P-4	Local Interoffice Transport/>=10 circuits/Dispatch/FL (days)											18.00	1	
B.2.1.2.2.2	P-4	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL (days)													
B.2.1.3.1.1	P-4	Loop + Port Combinations/<10 circuits/Dispatch/FL (days)	5.33	53,054	4.16	1,438	6.401	0.17107	6.8846	YES	4.82	47,879	3.62	1,273	5.949
B.2.1.3.1.2	P-4	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL (days)	0.88	683,871	0.74	43,349	1.340	0.00663	21.5089	YES	0.89	645,770	0.69	34,055	1.384
B.2.1.3.1.3	P-4	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL (days)	0.33	390,763	0.33	23,864	0.000	0.00000		YES	0.33	350,092	0.33	19,725	0.000
B.2.1.3.1.4	P-4	Loop + Port Combinations/<10 circuits/Dispatch In/FL (days)	1.61	293,108	1.23	19,485	1.805	0.01335	28.2193	YES	1.54	295,678	1.18	14,330	1.843
B.2.1.3.2.1	P-4	Loop + Port Combinations/>=10 circuits/Dispatch/FL (days)	10.61	390	5.36	11	11.378	3.47859	1.5073	YES	11.41	377	4.20	10	15.675
B.2.1.3.2.2	P-4	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL (days)	3.10	258	4.11	3	6.789	3.94223	-0.2576	YES	7.04	166	7.00	1	14.758
B.2.1.3.2.3	P-4	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL (days)	0.33	53	0.33	1	0.000	0.00000		YES	0.33	34			0.000
B.2.1.3.2.4	P-4	Loop + Port Combinations/>=10 circuits/Dispatch In/FL (days)	3.81	205	6.00	2	7.454	5.29663	-0.4135	YES	8.77	132	7.00	1	16.113
B.2.1.4.1.1	P-4	Combo Other/<10 circuits/Dispatch/FL (days)	6.31	56,075	12.09	65	9.347	1.16002	-4.9817	NO	5.79	50,613	11.57	83	8.822
B.2.1.4.1.4	P-4	Combo Other/<10 circuits/Dispatch In/FL (days)	6.31	56,075			9.347				5.79	50,613			8.822
B.2.1.4.2.1	P-4	Combo Other/>=10 circuits/Dispatch/FL (days)	10.73	395			11.404				14.61	467			18.252
B.2.1.4.2.4	P-4	Combo Other/>=10 circuits/Dispatch In/FL (days)	10.73	395			11.404				14.61	467			18.252
B.2.1.6.3.1	P-4	UNE ISDN/<6 circuits/Dispatch/FL (days)	14.88	318	9.92	226	16.813	1.46276	3.3864	YES	13.80	240	10.02	235	12.343
B.2.1.6.3.2	P-4	UNE ISDN/<6 circuits/Non-Dispatch/FL (days)	3.01	227			6.920				2.14	239			4.510
B.2.1.6.4.1	P-4	UNE ISDN/6-13 circuits/Dispatch/FL (days)			12.00	1									
B.2.1.6.4.2	P-4	UNE ISDN/6-13 circuits/Non-Dispatch/FL (days)	1.00	1			0.000				4.00	1			0.000
B.2.1.6.5.1	P-4	UNE ISDN/>=14 circuits/Dispatch/FL (days)													
B.2.1.6.5.2	P-4	UNE ISDN/>=14 circuits/Non-Dispatch/FL (days)													
B.2.1.7.3.1	*P-4*	*Line Sharing/<6 circuits/Dispatch/FL (days)*	2.75	8,554	6.69	29	1.991	0.37042	-10.6489	NO	2.80	7,820	4.87	31	3.056
B.2.1.7.3.2	P-4	Line Sharing/<6 circuits/Non-Dispatch/FL (days)	2.33	5,851	3.65	108	0.664	0.06448	-20.4845	NO	2.32	5,649	3.77	125	1.038
B.2.1.7.4.1	P-4	Line Sharing/6-13 circuits/Dispatch/FL (days)	3.00	7			1.291				2.31	13			1.316
B.2.1.7.4.2	P-4	Line Sharing/6-13 circuits/Non-Dispatch/FL (days)													
B.2.1.7.5.1	P-4	Line Sharing/>=14 circuits/Dispatch/FL (days)													
B.2.1.7.5.2	P-4	Line Sharing/>=14 circuits/Non-Dispatch/FL (days)													
B.2.1.8.1.1	P-4	2W Analog Loop Design/<10 circuits/Dispatch/FL (days)	5.33	53,054	4.63	255	6.401	0.40181	1.7534	YES	4.82	47,879	4.52	259	5.949
B.2.1.8.1.2	P-4	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL (days)	5.33	53,054			6.401				4.82	47,879			5.949
B.2.1.8.2.1	P-4	2W Analog Loop Design/>=10 circuits/Dispatch/FL (days)	10.61	390	7.00	1	11.378	11.39240	0.3166	YES	11.41	377	6.50	2	15.675
B.2.1.8.2.2	P-4	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL (days)	10.61	390			11.378				11.41	377			15.675
B.2.1.9.1.1	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL (days)	5.31	52,380	4.29	567	6.264	0.26449	3.8314	YES	4.81	47,314	3.73	629	5.932
B.2.1.9.1.4	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL (days)	1.59	291,522	3.64	14	1.698	0.45381	-4.5151	NO	1.53	294,261	3.11	28	1.819
B.2.1.9.2.1	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL (days)	10.18	331	8.08	12	11.619	3.41438	0.6139	YES	9.89	313	6.14	14	13.253
B.2.1.9.2.4	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL (days)	1.00	2	2.00	1	0.000	0.00000		NO	16.40	15			32.264
B.2.1.10.1.1	P-4	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL (days)	5.33	53,054	3.00	1	6.401	6.40107	0.3645	YES	4.82	47,879			5.949
B.2.1.10.1.2	P-4	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL (days)	5.33	53,054			6.401				4.82	47,879			5.949
B.2.1.10.2.1	P-4	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL (days)	10.61	390			11.378				11.41	377			15.675
B.2.1.10.2.2	P-4	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL (days)	10.61	390			11.378				11.41	377			15.675
B.2.1.11.1.1	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL (days)	5.31	52,380			6.264				4.81	47,314	3.00	1	5.932
B.2.1.11.1.4	*P-4*	*2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL (days)*	1.59	291,522	4.00	1	1.698	1.69797	-1.4171	YES	1.53	294,261			1.819
B.2.1.11.2.1	P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL (days)	10.18	331			11.619				9.89	313			13.253
B.2.1.11.2.4	P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL (days)	1.00	2			0.000				16.40	15			32.264
B.2.1.12.1.1	P-4	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL (days)	5.33	53,054	5.42	67	6.401	0.78250	-0.1081	YES	4.82	47,879	5.59	91	5.949
B.2.1.12.1.2	P-4	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL (days)	5.33	53,054			6.401				4.82	47,879			5.949
B.2.1.12.2.1	P-4	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL (days)	10.61	390	6.33	3	11.378	6.59421	0.6481	YES	11.41	377	6.50	4	15.675
B.2.1.12.2.2	P-4	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL (days)	10.61	390			11.378				11.41	377			15.675
B.2.1.13.1.1	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL (days)	5.31	52,380	5.16	243	6.264	0.40278	0.3714	YES	4.81	47,314	4.94	306	5.932
B.2.1.13.1.4	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL (days)	1.59	291,522	5.30	115	1.698	0.15837	-23.4297	NO	1.53	294,261	5.16	172	1.819

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - May 2002

			Standard Error	Zscore	Equity	May BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.1.19.8	O-11	2W Analog Loop Design/FL (%)			NO			91.20%	250				NO
B.1.19.9	O-11	2W Analog Loop Non-Design/FL (%)			NO			92.78%	831				NO
B.1.19.10	O-11	2W Analog Loop w/INP Design/FL (%)						100.00%	4				YES
B.1.19.11	O-11	2W Analog Loop w/INP Non-Design/FL (%)			YES			100.00%	4				YES
B.1.19.12	O-11	2W Analog Loop w/LNP Design/FL (%)			NO			87.13%	101				NO
B.1.19.13	O-11	2W Analog Loop w/LNP Non-Design/FL (%)			NO			90.91%	77				NO
B.1.19.14	O-11	Other Design/FL (%)			NO			90.54%	560				NO
B.1.19.15	O-11	Other Non-Design/FL (%)			YES			95.38%	1,820				YES
B.1.19.16	O-11	INP Standalone/FL (%)			NO			89.47%	76				NO
B.1.19.17	O-11	LNP Standalone/FL (%)			NO			92.96%	810				NO
		Unbundled Network Elements - Provisioning											
		Order Completion Interval											
B.2.1.1.1.1	P-4	Switch Ports/<10 circuits/Dispatch/FL (days)				4.01	68,137			5.992			
B.2.1.1.1.2	P-4	Switch Ports/<10 circuits/Non-Dispatch/FL (days)				0.89	674,661			1.551			
B.2.1.1.2.1	P-4	Switch Ports/>=10 circuits/Dispatch/FL (days)				9.03	344			14.906			
B.2.1.1.2.2	P-4	Switch Ports/>=10 circuits/Non-Dispatch/FL (days)				4.07	10			4.863			
B.2.1.2.1.1	P-4	Local Interoffice Transport/<10 circuits/Dispatch/FL (days)	3.97328	-0.1374	YES	16.35	2,107	15.73	26	14.725	2.90555	0.2117	YES
B.2.1.2.1.2	P-4	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL (days)											
B.2.1.2.2.1	P-4	Local Interoffice Transport/>=10 circuits/Dispatch/FL (days)				13.00	1			0.000			
B.2.1.2.2.2	P-4	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL (days)											
B.2.1.3.1.1	P-4	Loop + Port Combinations/<10 circuits/Dispatch/FL (days)	0.16894	7.1478	YES	4.03	68,740	3.35	1,092	6.013	0.18341	3.7211	YES
B.2.1.3.1.2	P-4	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL (days)	0.00770	25.2536	YES	0.89	677,664	0.65	33,452	1.590	0.00691	27.7361	YES
B.2.1.3.1.3	P-4	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL (days)	0.00000		YES	0.33	380,507	0.33	21,371	0.000	0.00000		YES
B.2.1.3.1.4	P-4	Loop + Port Combinations/<10 circuits/Dispatch In/FL (days)	0.01577	22.5595	YES	1.61	297,157	1.20	12,081	2.202	0.02044	20.0727	YES
B.2.1.3.2.1	P-4	Loop + Port Combinations/>=10 circuits/Dispatch/FL (days)	5.02231	1.4365	YES	10.00	393	5.06	6	17.233	7.08890	0.6974	YES
B.2.1.3.2.2	P-4	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL (days)	14.80239	0.0030	YES	3.38	259	0.44	6	6.599	2.72511	1.0790	YES
B.2.1.3.2.3	P-4	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL (days)				0.33	89	0.33	5	0.000	0.00000		YES
B.2.1.3.2.4	P-4	Loop + Port Combinations/>=10 circuits/Dispatch In/FL (days)	16.17387	0.1096	YES	4.98	170	1.00	1	7.682	7.70446	0.5169	YES
B.2.1.4.1.1	P-4	Combo Other/<10 circuits/Dispatch/FL (days)	0.96918	-5.9612	NO	4.72	71,511	11.97	122	8.396	0.76075	-9.5224	NO
B.2.1.4.1.4	P-4	Combo Other/<10 circuits/Dispatch In/FL (days)				4.72	71,511			8.396			
B.2.1.4.2.1	P-4	Combo Other/>=10 circuits/Dispatch/FL (days)				10.72	413			17.268			
B.2.1.4.2.4	P-4	Combo Other/>=10 circuits/Dispatch In/FL (days)				10.72	413			17.268			
B.2.1.6.3.1	P-4	UNE ISDN/<6 circuits/Dispatch/FL (days)	1.13271	3.3384	YES	11.88	246	10.70	419	8.550	0.68676	1.7222	YES
B.2.1.6.3.2	P-4	UNE ISDN/<6 circuits/Non-Dispatch/FL (days)				3.34	338			6.166			
B.2.1.6.4.1	P-4	UNE ISDN/6-13 circuits/Dispatch/FL (days)											
B.2.1.6.4.2	P-4	UNE ISDN/6-13 circuits/Non-Dispatch/FL (days)				0.67	2			0.471			
B.2.1.6.5.1	P-4	UNE ISDN/>=14 circuits/Dispatch/FL (days)											
B.2.1.6.5.2	P-4	UNE ISDN/>=14 circuits/Non-Dispatch/FL (days)											
B.2.1.7.3.1	P-4	Line Sharing/<6 circuits/Dispatch/FL (days)	0.55002	-3.7580	NO	3.77	6,742	6.30	46	2.628	0.38881	-6.5111	NO
B.2.1.7.3.2	P-4	Line Sharing/<6 circuits/Non-Dispatch/FL (days)	0.09391	-15.4542	NO	3.49	4,577	3.81	129	1.006	0.08984	-3.4671	NO
B.2.1.7.4.1	P-4	Line Sharing/6-13 circuits/Dispatch/FL (days)				8.43	14			10.847			
B.2.1.7.4.2	P-4	Line Sharing/6-13 circuits/Non-Dispatch/FL (days)				4.00	1			0.000			
B.2.1.7.5.1	P-4	Line Sharing/>=14 circuits/Dispatch/FL (days)											
B.2.1.7.5.2	P-4	Line Sharing/>=14 circuits/Non-Dispatch/FL (days)				8.00	1			0.000			
B.2.1.8.1.1	P-4	2W Analog Loop Design/<10 circuits/Dispatch/FL (days)	0.37067	0.8063	YES	4.03	68,740	4.83	232	6.013	0.39546	-2.0400	NO
B.2.1.8.1.2	P-4	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL (days)				4.03	68,740			6.013			
B.2.1.8.2.1	P-4	2W Analog Loop Design/>=10 circuits/Dispatch/FL (days)	11.11354	0.4422	YES	10.00	393	8.00	1	17.233	17.25505	0.1159	YES
B.2.1.8.2.2	P-4	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL (days)				10.00	393			17.233			
B.2.1.9.1.1	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL (days)	0.23808	4.5617	YES	4.01	68,137	3.64	751	5.992	0.21986	1.6654	YES
B.2.1.9.1.4	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL (days)	0.34372	-4.5820	NO	1.61	295,055	3.33	18	2.143	0.50503	-3.4187	NO
B.2.1.9.2.1	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL (days)	3.62028	1.0357	YES	9.03	344	4.55	11	14.906	4.56555	0.9825	YES
B.2.1.9.2.4	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL (days)				5.00	8			5.043			
B.2.1.10.1.1	P-4	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL (days)				4.03	68,740	5.00	1	6.013	6.01336	-0.1616	YES
B.2.1.10.1.2	P-4	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL (days)				4.03	68,740			6.013			
B.2.1.10.2.1	P-4	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL (days)				10.00	393			17.233			
B.2.1.10.2.2	P-4	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL (days)				10.00	393			17.233			
B.2.1.11.1.1	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL (days)	5.93167	0.3056	YES	4.01	68,137			5.992			
B.2.1.11.1.4	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL (days)				1.61	295,055			2.143			
B.2.1.11.2.1	P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL (days)				9.03	344			14.906			
B.2.1.11.2.4	P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL (days)				5.00	8			5.043			
B.2.1.12.1.1	P-4	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL (days)	0.62424	-1.2347	YES	4.03	68,740	5.57	188	6.013	0.43917	-3.5213	NO
B.2.1.12.1.2	P-4	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL (days)				4.03	68,740			6.013			
B.2.1.12.2.1	P-4	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL (days)	7.87917	0.6238	YES	10.00	393	8.38	8	17.233	6.15454	0.2639	YES
B.2.1.12.2.2	P-4	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL (days)				10.00	393			17.233			
B.2.1.13.1.1	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL (days)	0.34018	-0.3780	YES	4.01	68,137	4.98	583	5.992	0.24923	-3.8784	NO
B.2.1.13.1.4	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL (days)	0.13872	-26.1726	NO	1.61	295,055	4.96	260	2.143	0.13294	-25.2068	NO

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			Benchmark/ Analog	August BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.1.13.2.1	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL (days)	R&B (POTS) excl SB Or	11.42	318	8.31	13	18.832	5.32868	0.5835	YES
B.2.1.13.2.4	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL (days)	R&B (POTS) excl SB Or	2.43	7	7.25	8	1.813	0.93814	-5.1394	NO
B.2.1.14.1.1	P-4	Other Design/<10 circuits/Dispatch/FL (days)	Design	25.42	2,692			25.726			
B.2.1.14.1.2	P-4	Other Design/<10 circuits/Non-Dispatch/FL (days)	Design	7.72	890			9.150			
B.2.1.14.2.1	P-4	Other Design/>=10 circuits/Dispatch/FL (days)	Design	23.76	267			16.111			
B.2.1.14.2.2	P-4	Other Design/>=10 circuits/Non-Dispatch/FL (days)	Design	45.87	45			36.060			
B.2.1.15.1.1	P-4	Other Non-Design/<10 circuits/Dispatch/FL (days)	R&B	4.50	61,633	6.00	1	5.586	5.58637	-0.2680	YES
B.2.1.15.1.2	P-4	Other Non-Design/<10 circuits/Non-Dispatch/FL (days)	R&B	0.87	720,347			1.398			
B.2.1.15.2.1	P-4	Other Non-Design/>=10 circuits/Dispatch/FL (days)	R&B	11.32	369			17.986			
B.2.1.15.2.2	P-4	Other Non-Design/>=10 circuits/Non-Dispatch/FL (days)	R&B	2.67	285			4.627			
B.2.1.16.1.1	P-4	INP (Standalone)/<10 circuits/Dispatch/FL (days)	R&B (POTS)	4.54	59,577			5.447			
B.2.1.16.1.2	P-4	INP (Standalone)/<10 circuits/Non-Dispatch/FL (days)	R&B (POTS)	0.86	717,719			1.285			
B.2.1.16.2.1	P-4	INP (Standalone)/>=10 circuits/Dispatch/FL (days)	R&B (POTS)	11.42	318			18.832			
B.2.1.16.2.2	P-4	INP (Standalone)/>=10 circuits/Non-Dispatch/FL (days)	R&B (POTS)	1.00	22			1.392			
B.2.1.17.1.1	P-4	LNP (Standalone)/<10 circuits/Dispatch/FL (days)	R&B (POTS)	4.54	59,577	0.33	3	5.447	3.14496	1.3386	YES
B.2.1.17.1.2	P-4	LNP (Standalone)/<10 circuits/Non-Dispatch/FL (days)	R&B (POTS)	0.86	717,719	0.71	3,247	1.285	0.02260	6.3981	YES
B.2.1.17.2.1	P-4	LNP (Standalone)/>=10 circuits/Dispatch/FL (days)	R&B (POTS)	11.42	318			18.832			
B.2.1.17.2.2	P-4	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL (days)	R&B (POTS)	1.00	22	0.89	6	1.392	0.64092	0.1734	YES
B.2.1.18.1.1	P-4	Digital Loop < DS1/<10 circuits/Dispatch/FL (days)	Digital Loop < DS1	3.27	10,295	8.24	343	4.510	0.24752	-20.0807	NO
B.2.1.18.1.2	P-4	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL (days)	Digital Loop < DS1	2.60	7,340			2.725			
B.2.1.18.2.1	P-4	Digital Loop < DS1/>=10 circuits/Dispatch/FL (days)	Digital Loop < DS1	1.96	16			0.806			
B.2.1.18.2.2	P-4	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL (days)	Digital Loop < DS1	2.00	1			0.000			
B.2.1.19.1.1	P-4	Digital Loop >= DS1/<10 circuits/Dispatch/FL (days)	Digital Loop >= DS1	7.42	1,560	6.42	204	13.589	1.01170	0.9821	YES
B.2.1.19.1.2	P-4	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL (days)	Digital Loop >= DS1	4.06	862			7.913			
B.2.1.19.2.1	P-4	Digital Loop >= DS1/>=10 circuits/Dispatch/FL (days)	Digital Loop >= DS1	27.02	146			17.192			
B.2.1.19.2.2	P-4	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL (days)	Digital Loop >= DS1	23.77	87			30.833			
		Order Completion Interval within X days									
B.2.2.1	P-4	xDSL (ADSL, HDSL and UCL) Loop with Conditioning/<6 circuits/Dispatch/FL (days)	14 days								
B.2.2.2	P-4	xDSL (ADSL, HDSL and UCL) Loop w/o Conditioning/<6 circuits/Dispatch/FL (days)	7 days			4.61	144				YES
		Held Orders									
B.2.3.1.1.1	P-1	Switch Ports/<10 circuits/Facility/FL (days)	R&B (POTS)	8.17	456			8.322			
B.2.3.1.1.2	P-1	Switch Ports/<10 circuits/Equipment/FL (days)	R&B (POTS)	0.00	0						
B.2.3.1.1.3	P-1	Switch Ports/<10 circuits/Other/FL (days)	R&B (POTS)	7.49	97			9.945			
B.2.3.1.2.1	P-1	Switch Ports/>=10 circuits/Facility/FL (days)	R&B (POTS)	0.00	0						
B.2.3.1.2.2	P-1	Switch Ports/>=10 circuits/Equipment/FL (days)	R&B (POTS)	0.00	0						
B.2.3.1.2.3	P-1	Switch Ports/>=10 circuits/Other/FL (days)	R&B (POTS)	0.00	0						
B.2.3.2.1.1	P-1	Local Interoffice Transport/<10 circuits/Facility/FL (days)	DS1/ DS3 - Interoffice	0.00	0	0.00	0				YES
B.2.3.2.1.2	P-1	Local Interoffice Transport/<10 circuits/Equipment/FL (days)	DS1/ DS3 - Interoffice	0.00	0	0.00	0				YES
B.2.3.2.1.3	P-1	Local Interoffice Transport/<10 circuits/Other/FL (days)	DS1/ DS3 - Interoffice	18.75	12	0.00	0	18.631			YES
B.2.3.2.2.1	P-1	Local Interoffice Transport/>=10 circuits/Facility/FL (days)	DS1/ DS3 - Interoffice								
B.2.3.2.2.2	P-1	Local Interoffice Transport/>=10 circuits/Equipment/FL (days)	DS1/ DS3 - Interoffice								
B.2.3.2.2.3	P-1	Local Interoffice Transport/>=10 circuits/Other/FL (days)	DS1/ DS3 - Interoffice								
B.2.3.3.1.1	P-1	Loop + Port Combinations/<10 circuits/Facility/FL (days)	R&B	8.23	466	4.21	14	8.261	2.24067	1.7930	YES
B.2.3.3.1.2	P-1	Loop + Port Combinations/<10 circuits/Equipment/FL (days)	R&B	0.00	0	0.00	0				YES
B.2.3.3.1.3	P-1	Loop + Port Combinations/<10 circuits/Other/FL (days)	R&B	7.44	98	6.50	2	9.909	7.07781	0.1326	YES
B.2.3.3.2.1	P-1	Loop + Port Combinations/>=10 circuits/Facility/FL (days)	R&B	0.00	0	0.00	0				YES
B.2.3.3.2.2	P-1	Loop + Port Combinations/>=10 circuits/Equipment/FL (days)	R&B	0.00	0	0.00	0				YES
B.2.3.3.2.3	P-1	Loop + Port Combinations/>=10 circuits/Other/FL (days)	R&B	0.00	0	0.00	0				YES
B.2.3.4.1.1	P-1	Combo Other/<10 circuits/Facility/FL (days)	R&B&D - Disp	8.31	475	0.00	0	8.355			YES
B.2.3.4.1.2	P-1	Combo Other/<10 circuits/Equipment/FL (days)	R&B&D - Disp	0.00	0	0.00	0				YES
B.2.3.4.1.3	P-1	Combo Other/<10 circuits/Other/FL (days)	R&B&D - Disp	7.44	102	0.00	0	9.497			YES
B.2.3.4.2.1	P-1	Combo Other/>=10 circuits/Facility/FL (days)	R&B&D - Disp	0.00	0						
B.2.3.4.2.2	P-1	Combo Other/>=10 circuits/Equipment/FL (days)	R&B&D - Disp	0.00	0						
B.2.3.4.2.3	P-1	Combo Other/>=10 circuits/Other/FL (days)	R&B&D - Disp	4.00	1			0.000			
B.2.3.5.1.1	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Facility/FL (days)	ADSL to Retail	10.20	54	0.00	0	9.079			YES
B.2.3.5.1.2	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Equipment/FL (days)	ADSL to Retail	4.00	1	0.00	0	0.000			YES
B.2.3.5.1.3	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Other/FL (days)	ADSL to Retail	6.17	6	2.00	1	4.446	4.80220	0.8677	YES
B.2.3.5.2.1	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Facility/FL (days)	ADSL to Retail	0.00	0						
B.2.3.5.2.2	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Equipment/FL (days)	ADSL to Retail	0.00	0						
B.2.3.5.2.3	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Other/FL (days)	ADSL to Retail	0.00	0						
B.2.3.6.1.1	P-1	UNE ISDN/<10 circuits/Facility/FL (days)	ISDN - BRI	4.00	1	8.33	3	0.000	0.00000		NO
B.2.3.6.1.2	P-1	UNE ISDN/<10 circuits/Equipment/FL (days)	ISDN - BRI	0.00	0	0.00	0				YES
B.2.3.6.1.3	P-1	UNE ISDN/<10 circuits/Other/FL (days)	ISDN - BRI	0.00	0	0.00	0				YES
B.2.3.6.2.1	P-1	UNE ISDN/>=10 circuits/Facility/FL (days)	ISDN - BRI								
B.2.3.6.2.2	P-1	UNE ISDN/>=10 circuits/Equipment/FL (days)	ISDN - BRI								
B.2.3.6.2.3	P-1	UNE ISDN/>=10 circuits/Other/FL (days)	ISDN - BRI								
B.2.3.7.1.1	P-1	Line Sharing/<10 circuits/Facility/FL (days)	ADSL to Retail	10.20	54	0.00	0	9.079			YES

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			July								June				
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation
B.2.1.13.2.1	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL (days)	10.18	331	7.67	12	11.619	3.41438	0.7359	YES	9.89	313	7.35	17	13.253
B.2.1.13.2.4	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL (days)	1.00	2	7.36	11	0.000	0.00000		NO	16.40	15	7.00	3	32.264
B.2.1.14.1.1	P-4	Other Design/<10 circuits/Dispatch/FL (days)	23.53	3,021	17.00	1	24.272	24.27609	0.2688	YES	22.71	2,734			22.775
B.2.1.14.1.2	P-4	Other Design/<10 circuits/Non-Dispatch/FL (days)	10.68	646			13.096				9.33	553			11.743
B.2.1.14.2.1	P-4	Other Design/>=10 circuits/Dispatch/FL (days)	20.60	5			9.813				28.00	90			21.948
B.2.1.14.2.2	P-4	Other Design/>=10 circuits/Non-Dispatch/FL (days)	10.00	3			10.817				30.25	4			38.413
B.2.1.15.1.1	P-4	Other Non-Design/<10 circuits/Dispatch/FL (days)	5.33	53,054	10.57	7	6.401	2.41951	-2.1650	NO	4.82	47,879	5.80	20	5.949
B.2.1.15.1.2	P-4	Other Non-Design/<10 circuits/Non-Dispatch/FL (days)	0.88	683,871			1.340				0.89	645,770			1.384
B.2.1.15.2.1	P-4	Other Non-Design/>=10 circuits/Dispatch/FL (days)	10.61	390			11.378				11.41	377			15.675
B.2.1.15.2.2	P-4	Other Non-Design/>=10 circuits/Non-Dispatch/FL (days)	3.10	258			6.789				7.04	166			14.758
B.2.1.16.1.1	P-4	INP (Standalone)/<10 circuits/Dispatch/FL (days)	5.31	52,380			6.264				4.81	47,314			5.932
B.2.1.16.1.2	P-4	INP (Standalone)/<10 circuits/Non-Dispatch/FL (days)	0.87	681,545			1.274				0.88	643,741			1.367
B.2.1.16.2.1	P-4	INP (Standalone)/>=10 circuits/Dispatch/FL (days)	10.18	331			11.619				9.89	313			13.253
B.2.1.16.2.2	P-4	INP (Standalone)/>=10 circuits/Non-Dispatch/FL (days)	0.50	8			0.309				13.72	18			29.920
B.2.1.17.1.1	P-4	LNP (Standalone)/<10 circuits/Dispatch/FL (days)	5.31	52,380	0.33	5	6.264	2.80159	1.7754	YES	4.81	47,314	0.33	12	5.932
B.2.1.17.1.2	P-4	LNP (Standalone)/<10 circuits/Non-Dispatch/FL (days)	0.87	681,545	0.72	3,039	1.274	0.02315	6.7767	YES	0.88	643,741	0.77	2,743	1.367
B.2.1.17.2.1	P-4	LNP (Standalone)/>=10 circuits/Dispatch/FL (days)	10.18	331			11.619				9.89	313			13.253
B.2.1.17.2.2	P-4	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL (days)	0.50	8	0.33	13	0.309	0.13868	1.2019	YES	13.72	18	0.57	7	29.920
B.2.1.18.1.1	P-4	Digital Loop < DS1/<10 circuits/Dispatch/FL (days)	3.58	9,199	7.77	380	5.211	0.27278	-15.3698	NO	3.69	8,431	7.64	422	6.546
B.2.1.18.1.2	P-4	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL (days)	2.68	6,557			2.526				2.72	6,431			2.930
B.2.1.18.2.1	P-4	Digital Loop < DS1/>=10 circuits/Dispatch/FL (days)	2.83	6	12.00	1	1.329	1.43566	-6.3850	NO	2.31	13	4.00	1	1.316
B.2.1.18.2.2	P-4	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL (days)									2.00	1			0.000
B.2.1.19.1.1	P-4	Digital Loop >= DS1/<10 circuits/Dispatch/FL (days)	21.80	660	6.70	226	25.598	1.97285	7.6506	YES	20.86	362	6.00	201	30.789
B.2.1.19.1.2	P-4	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL (days)	4.76	899			10.707				2.90	524			5.008
B.2.1.19.2.1	P-4	Digital Loop >= DS1/>=10 circuits/Dispatch/FL (days)	23.75	4			7.890				13.33	9	9.00	1	9.206
B.2.1.19.2.2	P-4	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL (days)	4.64	92			6.113				9.55	67			9.699
		Order Completion Interval within X days													
B.2.2.1	P-4	xDSL (ADSL, HDSL and UCL) Loop with Conditioning/<6 circuits/Dispatch/FL (days)													
B.2.2.2	P-4	xDSL (ADSL, HDSL and UCL) Loop w/o Conditioning/<6 circuits/Dispatch/FL (days)			4.66	166				YES			4.69	208	
		Held Orders													
B.2.3.1.1.1	P-1	Switch Ports/<10 circuits/Facility/FL (days)	8.84	382			8.466				9.15	412			7.989
B.2.3.1.1.2	P-1	Switch Ports/<10 circuits/Equipment/FL (days)	0.00	0							0.00	0			
B.2.3.1.1.3	P-1	Switch Ports/<10 circuits/Other/FL (days)	8.82	33			11.276				9.40	55			14.695
B.2.3.1.2.1	P-1	Switch Ports/>=10 circuits/Facility/FL (days)	0.00	0							0.00	0			
B.2.3.1.2.2	P-1	Switch Ports/>=10 circuits/Equipment/FL (days)	0.00	0							0.00	0			
B.2.3.1.2.3	P-1	Switch Ports/>=10 circuits/Other/FL (days)	0.00	0							0.00	0			
B.2.3.2.1.1	P-1	Local Interoffice Transport/<10 circuits/Facility/FL (days)	34.00	1	0.00	0	0.000			YES	3.00	1	0.00	0	0.000
B.2.3.2.1.2	P-1	Local Interoffice Transport/<10 circuits/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.2.1.3	P-1	Local Interoffice Transport/<10 circuits/Other/FL (days)	6.80	10	0.00	0	4.467			YES	6.50	2	0.00	0	4.950
B.2.3.2.2.1	P-1	Local Interoffice Transport/>=10 circuits/Facility/FL (days)											0.00	0	
B.2.3.2.2.2	P-1	Local Interoffice Transport/>=10 circuits/Equipment/FL (days)											0.00	0	
B.2.3.2.2.3	P-1	Local Interoffice Transport/>=10 circuits/Other/FL (days)											0.00	0	
B.2.3.3.1.1	P-1	Loop + Port Combinations/<10 circuits/Facility/FL (days)	8.84	382	5.80	10	8.466	2.71192	1.1201	YES	9.17	416	6.20	10	7.998
B.2.3.3.1.2	P-1	Loop + Port Combinations/<10 circuits/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.3.1.3	P-1	Loop + Port Combinations/<10 circuits/Other/FL (days)	8.82	33	1.50	2	11.276	8.21157	0.8912	YES	9.40	55	3.00	2	14.695
B.2.3.3.2.1	P-1	Loop + Port Combinations/>=10 circuits/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.3.2.2	P-1	Loop + Port Combinations/>=10 circuits/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.3.2.3	P-1	Loop + Port Combinations/>=10 circuits/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.4.1.1	P-1	Combo Other/<10 circuits/Facility/FL (days)	8.81	394	0.00	0	8.381			YES	9.19	421	0.00	0	8.001
B.2.3.4.1.2	P-1	Combo Other/<10 circuits/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.4.1.3	P-1	Combo Other/<10 circuits/Other/FL (days)	8.79	38	0.00	0	10.680			YES	11.58	74	0.00	0	14.361
B.2.3.4.2.1	P-1	Combo Other/>=10 circuits/Facility/FL (days)	0.00	0							0.00	0			
B.2.3.4.2.2	P-1	Combo Other/>=10 circuits/Equipment/FL (days)	0.00	0							0.00	0			
B.2.3.4.2.3	P-1	Combo Other/>=10 circuits/Other/FL (days)	0.00	0							0.00	0			
B.2.3.5.1.1	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Facility/FL (days)	7.48	40	3.00	1	6.008	6.08313	0.7356	YES	10.47	32	0.00	0	9.384
B.2.3.5.1.2	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.5.1.3	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Other/FL (days)	2.39	18	0.00	0	2.500			YES	15.50	2	0.00	0	16.263
B.2.3.5.2.1	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Facility/FL (days)	0.00	0							0.00	0	0.00	0	
B.2.3.5.2.2	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Equipment/FL (days)	0.00	0							0.00	0	0.00	0	
B.2.3.5.2.3	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Other/FL (days)	0.00	0							0.00	0	0.00	0	
B.2.3.6.1.1	P-1	UNE ISDN/<10 circuits/Facility/FL (days)	13.67	3	10.80	5	7.024	5.12944	0.5589	YES	3.00	1	8.00	2	0.000
B.2.3.6.1.2	P-1	UNE ISDN/<10 circuits/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.6.1.3	P-1	UNE ISDN/<10 circuits/Other/FL (days)	1.00	2	0.00	0	0.000			YES	7.00	1	0.00	0	0.000
B.2.3.6.2.1	P-1	UNE ISDN/>=10 circuits/Facility/FL (days)			0.00	0									
B.2.3.6.2.2	P-1	UNE ISDN/>=10 circuits/Equipment/FL (days)			0.00	0									
B.2.3.6.2.3	P-1	UNE ISDN/>=10 circuits/Other/FL (days)			0.00	0									
B.2.3.7.1.1	P-1	Line Sharing/<10 circuits/Facility/FL (days)	7.48	40	0.00	0	6.008			YES	10.47	32	7.00	1	9.384

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - May 2002

						May							
			Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.1.13.2.1	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL (days)	3.30039	0.7695	YES	9.03	344	7.74	23	14.906	3.21029	0.4024	YES
B.2.1.13.2.4	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL (days)	20.40560	0.4607	YES	5.00	8	7.00	6	5.043	2.72336	-0.7344	YES
B.2.1.14.1.1	P-4	Other Design/<10 circuits/Dispatch/FL (days)				21.96	2,771			24.759			
B.2.1.14.1.2	P-4	Other Design/<10 circuits/Non-Dispatch/FL (days)				11.37	781			15.820			
B.2.1.14.2.1	P-4	Other Design/>=10 circuits/Dispatch/FL (days)				24.95	20			10.724			
B.2.1.14.2.2	P-4	Other Design/>=10 circuits/Non-Dispatch/FL (days)				32.33	3			48.211			
B.2.1.15.1.1	P-4	Other Non-Design/<10 circuits/Dispatch/FL (days)	1.33056	-0.7345	YES	4.03	68,740	7.25	53	6.013	0.82631	-3.8935	NO
B.2.1.15.1.2	P-4	Other Non-Design/<10 circuits/Non-Dispatch/FL (days)				0.89	677,664			1.590			
B.2.1.15.2.1	P-4	Other Non-Design/>=10 circuits/Dispatch/FL (days)				10.00	393			17.233			
B.2.1.15.2.2	P-4	Other Non-Design/>=10 circuits/Non-Dispatch/FL (days)				3.38	259			6.599			
B.2.1.16.1.1	P-4	INP (Standalone)/<10 circuits/Dispatch/FL (days)				4.01	68,137			5.992			
B.2.1.16.1.2	P-4	INP (Standalone)/<10 circuits/Non-Dispatch/FL (days)				0.89	674,661			1.551			
B.2.1.16.2.1	P-4	INP (Standalone)/>=10 circuits/Dispatch/FL (days)				9.03	344			14.906			
B.2.1.16.2.2	P-4	INP (Standalone)/>=10 circuits/Non-Dispatch/FL (days)				4.07	10			4.863			
B.2.1.17.1.1	P-4	LNP (Standalone)/<10 circuits/Dispatch/FL (days)	1.71252	2.6156	YES	4.01	68,137	5.00	1	5.992	5.99226	-0.1650	YES
B.2.1.17.1.2	P-4	LNP (Standalone)/<10 circuits/Non-Dispatch/FL (days)	0.02616	4.1934	YES	0.89	674,661	0.77	3,347	1.551	0.02688	4.6283	YES
B.2.1.17.2.1	P-4	LNP (Standalone)/>=10 circuits/Dispatch/FL (days)				9.03	344			14.906			
B.2.1.17.2.2	P-4	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL (days)	13.32762	0.9867	YES	4.07	10	0.33	7	4.863	2.39654	1.5578	YES
B.2.1.18.1.1	P-4	Digital Loop < DS1/<10 circuits/Dispatch/FL (days)	0.32652	-12.0953	NO	4.77	7,466	8.89	593	5.638	0.24055	-17.1047	NO
B.2.1.18.1.2	P-4	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL (days)				4.26	5,689			5.861			
B.2.1.18.2.1	P-4	Digital Loop < DS1/>=10 circuits/Dispatch/FL (days)	1.36525	-1.2396	YES	7.50	12			10.992			
B.2.1.18.2.2	P-4	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL (days)				8.00	1			0.000			
B.2.1.19.1.1	P-4	Digital Loop >= DS1/<10 circuits/Dispatch/FL (days)	2.70828	5.4898	YES	28.05	315	6.29	260	41.790	3.50160	6.2132	YES
B.2.1.19.1.2	P-4	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL (days)				1.99	1,178			5.898			
B.2.1.19.2.1	P-4	Digital Loop >= DS1/>=10 circuits/Dispatch/FL (days)	9.70395	0.4466	YES	11.50	2			2.121			
B.2.1.19.2.2	P-4	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL (days)				3.20	57			4.635			
		Order Completion Interval within X days											
B.2.2.1	P-4	xDSL (ADSL, HDSL and UCL) Loop with Conditioning/<6 circuits/Dispatch/FL (days)						5.00	1				YES
B.2.2.2	P-4	xDSL (ADSL, HDSL and UCL) Loop w/o Conditioning/<6 circuits/Dispatch/FL (days)			YES			4.56	188				YES
		Held Orders											
B.2.3.1.1.1	P-1	Switch Ports/<10 circuits/Facility/FL (days)				6.54	404			6.936			
B.2.3.1.1.2	P-1	Switch Ports/<10 circuits/Equipment/FL (days)				0.00	0						
B.2.3.1.1.3	P-1	Switch Ports/<10 circuits/Other/FL (days)				8.92	53			10.486			
B.2.3.1.2.1	P-1	Switch Ports/>=10 circuits/Facility/FL (days)				0.00	0						
B.2.3.1.2.2	P-1	Switch Ports/>=10 circuits/Equipment/FL (days)				0.00	0						
B.2.3.1.2.3	P-1	Switch Ports/>=10 circuits/Other/FL (days)				0.00	0						
B.2.3.2.1.1	P-1	Local Interoffice Transport/<10 circuits/Facility/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.2.1.2	P-1	Local Interoffice Transport/<10 circuits/Equipment/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.2.1.3	P-1	Local Interoffice Transport/<10 circuits/Other/FL (days)			YES	10.71	7	0.00	0	9.759			YES
B.2.3.2.2.1	P-1	Local Interoffice Transport/>=10 circuits/Facility/FL (days)				0.00	0						
B.2.3.2.2.2	P-1	Local Interoffice Transport/>=10 circuits/Equipment/FL (days)				0.00	0						
B.2.3.2.2.3	P-1	Local Interoffice Transport/>=10 circuits/Other/FL (days)				0.00	0						
B.2.3.3.1.1	P-1	Loop + Port Combinations/<10 circuits/Facility/FL (days)	2.55951	1.1606	YES	6.57	407	2.25	4	6.984	3.50916	1.2318	YES
B.2.3.3.1.2	P-1	Loop + Port Combinations/<10 circuits/Equipment/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.3.1.3	P-1	Loop + Port Combinations/<10 circuits/Other/FL (days)	10.57830	0.6050	YES	8.63	56	3.00	3	10.304	6.10610	0.9212	YES
B.2.3.3.2.1	P-1	Loop + Port Combinations/>=10 circuits/Facility/FL (days)			YES	1.43	7	0.00	0	1.134			YES
B.2.3.3.2.2	P-1	Loop + Port Combinations/>=10 circuits/Equipment/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.3.2.3	P-1	Loop + Port Combinations/>=10 circuits/Other/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.4.1.1	P-1	Combo Other/<10 circuits/Facility/FL (days)			YES	6.59	408	0.00	0	6.981			YES
B.2.3.4.1.2	P-1	Combo Other/<10 circuits/Equipment/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.4.1.3	P-1	Combo Other/<10 circuits/Other/FL (days)			YES	13.28	64	0.00	0	20.237			YES
B.2.3.4.2.1	P-1	Combo Other/>=10 circuits/Facility/FL (days)				1.43	7			1.134			
B.2.3.4.2.2	P-1	Combo Other/>=10 circuits/Equipment/FL (days)				0.00	0						
B.2.3.4.2.3	P-1	Combo Other/>=10 circuits/Other/FL (days)				0.00	0						
B.2.3.5.1.1	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Facility/FL (days)			YES	18.07	83	19.00	1	18.253	18.36312	-0.0505	YES
B.2.3.5.1.2	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Equipment/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.5.1.3	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Other/FL (days)			YES	1.00	9	0.00	0	0.000			YES
B.2.3.5.2.1	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Facility/FL (days)			YES	0.00	0						
B.2.3.5.2.2	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Equipment/FL (days)			YES	0.00	0						
B.2.3.5.2.3	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Other/FL (days)			YES	0.00	0						
B.2.3.6.1.1	P-1	UNE ISDN/<10 circuits/Facility/FL (days)	0.00000		NO	12.00	1	0.00	0	0.000			YES
B.2.3.6.1.2	P-1	UNE ISDN/<10 circuits/Equipment/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.6.1.3	P-1	UNE ISDN/<10 circuits/Other/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.6.2.1	P-1	UNE ISDN/>=10 circuits/Facility/FL (days)											
B.2.3.6.2.2	P-1	UNE ISDN/>=10 circuits/Equipment/FL (days)											
B.2.3.6.2.3	P-1	UNE ISDN/>=10 circuits/Other/FL (days)											
B.2.3.7.1.1	P-1	Line Sharing/<10 circuits/Facility/FL (days)	9.52972	0.3640	YES	18.07	83	0.00	0	18.253			YES

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - May 2002

			Benchmark/ Analog	August BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.3.7.1.2	P-1	Line Sharing/<10 circuits/Equipment/FL (days)	ADSL to Retail	4.00	1	0.00	0	0.000			YES
B.2.3.7.1.3	P-1	Line Sharing/<10 circuits/Other/FL (days)	ADSL to Retail	6.17	6	0.00	0	4.446			YES
B.2.3.7.2.1	P-1	Line Sharing/>=10 circuits/Facility/FL (days)	ADSL to Retail	0.00	0						
B.2.3.7.2.2	P-1	Line Sharing/>=10 circuits/Equipment/FL (days)	ADSL to Retail	0.00	0						
B.2.3.7.2.3	P-1	Line Sharing/>=10 circuits/Other/FL (days)	ADSL to Retail	0.00	0						
B.2.3.8.1.1	P-1	2W Analog Loop Design/<10 circuits/Facility/FL (days)	R&B - Disp	8.23	466	9.00	2	8.261	5.85370	-0.1312	YES
B.2.3.8.1.2	P-1	2W Analog Loop Design/<10 circuits/Equipment/FL (days)	R&B - Disp	0.00	0	0.00	0				YES
B.2.3.8.1.3	P-1	2W Analog Loop Design/<10 circuits/Other/FL (days)	R&B - Disp	7.44	98	0.00	0	9.909			YES
B.2.3.8.2.1	P-1	2W Analog Loop Design/>=10 circuits/Facility/FL (days)	R&B - Disp	0.00	0	0.00	0				YES
B.2.3.8.2.2	P-1	2W Analog Loop Design/>=10 circuits/Equipment/FL (days)	R&B - Disp	0.00	0	0.00	0				YES
B.2.3.8.2.3	P-1	2W Analog Loop Design/>=10 circuits/Other/FL (days)	R&B - Disp	0.00	0	0.00	0				YES
B.2.3.9.1.1	P-1	2W Analog Loop Non-Design/<10 circuits/Facility/FL (days)	R&B (POTS) excl SB Or	8.17	456	2.75	4	8.322	4.17908	1.2961	YES
B.2.3.9.1.2	P-1	2W Analog Loop Non-Design/<10 circuits/Equipment/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES
B.2.3.9.1.3	P-1	2W Analog Loop Non-Design/<10 circuits/Other/FL (days)	R&B (POTS) excl SB Or	7.49	97	0.00	0	9.945			YES
B.2.3.9.2.1	P-1	2W Analog Loop Non-Design/>=10 circuits/Facility/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES
B.2.3.9.2.2	P-1	2W Analog Loop Non-Design/>=10 circuits/Equipment/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES
B.2.3.9.2.3	P-1	2W Analog Loop Non-Design/>=10 circuits/Other/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES
B.2.3.10.1.1	P-1	2W Analog Loop w/INP Design/<10 circuits/Facility/FL (days)	R&B - Disp	8.23	466	0.00	0	8.261			YES
B.2.3.10.1.2	P-1	2W Analog Loop w/INP Design/<10 circuits/Equipment/FL (days)	R&B - Disp	0.00	0	0.00	0				YES
B.2.3.10.1.3	P-1	2W Analog Loop w/INP Design/<10 circuits/Other/FL (days)	R&B - Disp	7.44	98	0.00	0	9.909			YES
B.2.3.10.2.1	P-1	2W Analog Loop w/INP Design/>=10 circuits/Facility/FL (days)	R&B - Disp	0.00	0						
B.2.3.10.2.2	P-1	2W Analog Loop w/INP Design/>=10 circuits/Equipment/FL (days)	R&B - Disp	0.00	0						
B.2.3.10.2.3	P-1	2W Analog Loop w/INP Design/>=10 circuits/Other/FL (days)	R&B - Disp	0.00	0						
B.2.3.11.1.1	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Facility/FL (days)	R&B (POTS) excl SB Or	8.17	456	0.00	0	8.322			YES
B.2.3.11.1.2	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Equipment/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES
B.2.3.11.1.3	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Other/FL (days)	R&B (POTS) excl SB Or	7.49	97	0.00	0	9.945			YES
B.2.3.11.2.1	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Facility/FL (days)	R&B (POTS) excl SB Or	0.00	0						
B.2.3.11.2.2	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Equipment/FL (days)	R&B (POTS) excl SB Or	0.00	0						
B.2.3.11.2.3	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Other/FL (days)	R&B (POTS) excl SB Or	0.00	0						
B.2.3.12.1.1	P-1	2W Analog Loop w/LNP Design/<10 circuits/Facility/FL (days)	R&B - Disp	8.23	466	0.00	0	8.261			YES
B.2.3.12.1.2	P-1	2W Analog Loop w/LNP Design/<10 circuits/Equipment/FL (days)	R&B - Disp	0.00	0	0.00	0				YES
B.2.3.12.1.3	P-1	2W Analog Loop w/LNP Design/<10 circuits/Other/FL (days)	R&B - Disp	7.44	98	0.00	0	9.909			YES
B.2.3.12.2.1	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Facility/FL (days)	R&B - Disp	0.00	0	0.00	0				YES
B.2.3.12.2.2	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Equipment/FL (days)	R&B - Disp	0.00	0	0.00	0				YES
B.2.3.12.2.3	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Other/FL (days)	R&B - Disp	0.00	0	0.00	0				YES
B.2.3.13.1.1	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Facility/FL (days)	R&B (POTS) excl SB Or	8.17	456	9.00	1	8.322	8.33086	-0.1000	YES
B.2.3.13.1.2	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Equipment/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES
B.2.3.13.1.3	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Other/FL (days)	R&B (POTS) excl SB Or	7.49	97	0.00	0	9.945			YES
B.2.3.13.2.1	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Facility/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES
B.2.3.13.2.2	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Equipment/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES
B.2.3.13.2.3	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Other/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES
B.2.3.14.1.1	P-1	Other Design/<10 circuits/Facility/FL (days)	Design	12.56	9			12.197			
B.2.3.14.1.2	P-1	Other Design/<10 circuits/Equipment/FL (days)	Design	0.00	0						
B.2.3.14.1.3	P-1	Other Design/<10 circuits/Other/FL (days)	Design	15.33	9			13.058			
B.2.3.14.2.1	P-1	Other Design/>=10 circuits/Facility/FL (days)	Design	0.00	0						
B.2.3.14.2.2	P-1	Other Design/>=10 circuits/Equipment/FL (days)	Design	0.00	0						
B.2.3.14.2.3	P-1	Other Design/>=10 circuits/Other/FL (days)	Design	4.00	1			0.000			
B.2.3.15.1.1	P-1	Other Non-Design/<10 circuits/Facility/FL (days)	R&B	8.23	466	0.00	0	8.261			YES
B.2.3.15.1.2	P-1	Other Non-Design/<10 circuits/Equipment/FL (days)	R&B	0.00	0	0.00	0				YES
B.2.3.15.1.3	P-1	Other Non-Design/<10 circuits/Other/FL (days)	R&B	7.44	98	0.00	0	9.909			YES
B.2.3.15.2.1	P-1	Other Non-Design/>=10 circuits/Facility/FL (days)	R&B	0.00	0						
B.2.3.15.2.2	P-1	Other Non-Design/>=10 circuits/Equipment/FL (days)	R&B	0.00	0						
B.2.3.15.2.3	P-1	Other Non-Design/>=10 circuits/Other/FL (days)	R&B	0.00	0						
B.2.3.16.1.1	P-1	INP (Standalone)/<10 circuits/Facility/FL (days)	R&B (POTS)	8.17	456			8.322			
B.2.3.16.1.2	P-1	INP (Standalone)/<10 circuits/Equipment/FL (days)	R&B (POTS)	0.00	0						
B.2.3.16.1.3	P-1	INP (Standalone)/<10 circuits/Other/FL (days)	R&B (POTS)	7.49	97			9.945			
B.2.3.16.2.1	P-1	INP (Standalone)/>=10 circuits/Facility/FL (days)	R&B (POTS)	0.00	0						
B.2.3.16.2.2	P-1	INP (Standalone)/>=10 circuits/Equipment/FL (days)	R&B (POTS)	0.00	0						
B.2.3.16.2.3	P-1	INP (Standalone)/>=10 circuits/Other/FL (days)	R&B (POTS)	0.00	0						
B.2.3.17.1.1	P-1	LNP (Standalone)/<10 circuits/Facility/FL (days)	R&B (POTS)	8.17	456	0.00	0	8.322			YES
B.2.3.17.1.2	P-1	LNP (Standalone)/<10 circuits/Equipment/FL (days)	R&B (POTS)	0.00	0	0.00	0				YES
B.2.3.17.1.3	P-1	LNP (Standalone)/<10 circuits/Other/FL (days)	R&B (POTS)	7.49	97	3.00	4	9.945	5.07387	0.8859	YES
B.2.3.17.2.1	P-1	LNP (Standalone)/>=10 circuits/Facility/FL (days)	R&B (POTS)	0.00	0	0.00	0				YES
B.2.3.17.2.2	P-1	LNP (Standalone)/>=10 circuits/Equipment/FL (days)	R&B (POTS)	0.00	0	0.00	0				YES
B.2.3.17.2.3	P-1	LNP (Standalone)/>=10 circuits/Other/FL (days)	R&B (POTS)	0.00	0	0.00	0				YES
B.2.3.18.1.1	P-1	Digital Loop < DS1/<10 circuits/Facility/FL (days)	Digital Loop < DS1	10.44	62	8.33	3	9.546	5.64343	0.3725	YES
B.2.3.18.1.2	P-1	Digital Loop < DS1/<10 circuits/Equipment/FL (days)	Digital Loop < DS1	4.00	1	0.00	0	0.000			YES
B.2.3.18.1.3	P-1	Digital Loop < DS1/<10 circuits/Other/FL (days)	Digital Loop < DS1	6.17	6	2.00	1	4.446	4.80220	0.8677	YES

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - M ay 2002

			July								June				
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation
B.2.3.7.1.2	P-1	Line Sharing/<10 circuits/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.7.1.3	P-1	Line Sharing/<10 circuits/Other/FL (days)	2.39	18	0.00	0	2.500			YES	15.50	2	0.00	0	16.263
B.2.3.7.2.1	P-1	Line Sharing/>=10 circuits/Facility/FL (days)	0.00	0							0.00	0			
B.2.3.7.2.2	P-1	Line Sharing/>=10 circuits/Equipment/FL (days)	0.00	0							0.00	0			
B.2.3.7.2.3	P-1	Line Sharing/>=10 circuits/Other/FL (days)	0.00	0							0.00	0			
B.2.3.8.1.1	P-1	2W Analog Loop Design/<10 circuits/Facility/FL (days)	8.84	382	0.00	0	8.466			YES	9.17	416	6.00	1	7.998
B.2.3.8.1.2	P-1	2W Analog Loop Design/<10 circuits/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.8.1.3	P-1	2W Analog Loop Design/<10 circuits/Other/FL (days)	8.82	33	0.00	0	11.276			YES	9.40	55	0.00	0	14.695
B.2.3.8.2.1	P-1	2W Analog Loop Design/>=10 circuits/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.8.2.2	P-1	2W Analog Loop Design/>=10 circuits/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.8.2.3	P-1	2W Analog Loop Design/>=10 circuits/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.9.1.1	P-1	2W Analog Loop Non-Design/<10 circuits/Facility/FL (days)	8.84	382	9.00	1	8.466	8.47684	-0.0191	YES	9.15	412	7.00	2	7.989
B.2.3.9.1.2	P-1	2W Analog Loop Non-Design/<10 circuits/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.9.1.3	P-1	2W Analog Loop Non-Design/<10 circuits/Other/FL (days)	8.82	33	0.00	0	11.276			YES	9.40	55	0.00	0	14.695
B.2.3.9.2.1	P-1	2W Analog Loop Non-Design/>=10 circuits/Facility/FL (days)	0.00	0	10.00	1				NO	0.00	0	0.00	0	
B.2.3.9.2.2	P-1	2W Analog Loop Non-Design/>=10 circuits/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.9.2.3	P-1	2W Analog Loop Non-Design/>=10 circuits/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.10.1.1	P-1	2W Analog Loop w/INP Design/<10 circuits/Facility/FL (days)	8.84	382	0.00	0	8.466			YES	9.17	416			7.998
B.2.3.10.1.2	P-1	2W Analog Loop w/INP Design/<10 circuits/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0			
B.2.3.10.1.3	P-1	2W Analog Loop w/INP Design/<10 circuits/Other/FL (days)	8.82	33	0.00	0	11.276			YES	9.40	55			14.695
B.2.3.10.2.1	P-1	2W Analog Loop w/INP Design/>=10 circuits/Facility/FL (days)	0.00	0							0.00	0			
B.2.3.10.2.2	P-1	2W Analog Loop w/INP Design/>=10 circuits/Equipment/FL (days)	0.00	0							0.00	0			
B.2.3.10.2.3	P-1	2W Analog Loop w/INP Design/>=10 circuits/Other/FL (days)	0.00	0							0.00	0			
B.2.3.11.1.1	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Facility/FL (days)	8.84	382	0.00	0	8.466			YES	9.15	412	0.00	0	7.989
B.2.3.11.1.2	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.11.1.3	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Other/FL (days)	8.82	33	0.00	0	11.276			YES	9.40	55	0.00	0	14.695
B.2.3.11.2.1	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Facility/FL (days)	0.00	0							0.00	0			
B.2.3.11.2.2	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Equipment/FL (days)	0.00	0							0.00	0			
B.2.3.11.2.3	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Other/FL (days)	0.00	0							0.00	0			
B.2.3.12.1.1	P-1	2W Analog Loop w/LNP Design/<10 circuits/Facility/FL (days)	8.84	382	0.00	0	8.466			YES	9.17	416	0.00	0	7.998
B.2.3.12.1.2	P-1	2W Analog Loop w/LNP Design/<10 circuits/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.12.1.3	P-1	2W Analog Loop w/LNP Design/<10 circuits/Other/FL (days)	8.82	33	0.00	0	11.276			YES	9.40	55	0.00	0	14.695
B.2.3.12.2.1	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.12.2.2	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.12.2.3	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.13.1.1	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Facility/FL (days)	8.84	382	20.00	1	8.466	8.47684	-1.3168	YES	9.15	412	0.00	0	7.989
B.2.3.13.1.2	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.13.1.3	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Other/FL (days)	8.82	33	0.00	0	11.276			YES	9.40	55	0.00	0	14.695
B.2.3.13.2.1	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.13.2.2	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.13.2.3	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.14.1.1	P-1	Other Design/<10 circuits/Facility/FL (days)	8.08	12	0.00	0	5.143			YES	11.20	5			8.955
B.2.3.14.1.2	P-1	Other Design/<10 circuits/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0			
B.2.3.14.1.3	P-1	Other Design/<10 circuits/Other/FL (days)	6.86	7	0.00	0	5.843			YES	17.25	20			11.548
B.2.3.14.2.1	P-1	Other Design/>=10 circuits/Facility/FL (days)	0.00	0							0.00	0			
B.2.3.14.2.2	P-1	Other Design/>=10 circuits/Equipment/FL (days)	0.00	0							0.00	0			
B.2.3.14.2.3	P-1	Other Design/>=10 circuits/Other/FL (days)	0.00	0							0.00	0			
B.2.3.15.1.1	P-1	Other Non-Design/<10 circuits/Facility/FL (days)	8.84	382	0.00	0	8.466			YES	9.17	416	5.00	1	7.998
B.2.3.15.1.2	P-1	Other Non-Design/<10 circuits/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.15.1.3	P-1	Other Non-Design/<10 circuits/Other/FL (days)	8.82	33	0.00	0	11.276			YES	9.40	55	0.00	0	14.695
B.2.3.15.2.1	P-1	Other Non-Design/>=10 circuits/Facility/FL (days)	0.00	0							0.00	0			
B.2.3.15.2.2	P-1	Other Non-Design/>=10 circuits/Equipment/FL (days)	0.00	0							0.00	0			
B.2.3.15.2.3	P-1	Other Non-Design/>=10 circuits/Other/FL (days)	0.00	0							0.00	0			
B.2.3.16.1.1	P-1	INP (Standalone)/<10 circuits/Facility/FL (days)	8.84	382			8.466				9.15	412			7.989
B.2.3.16.1.2	P-1	INP (Standalone)/<10 circuits/Equipment/FL (days)	0.00	0							0.00	0			
B.2.3.16.1.3	P-1	INP (Standalone)/<10 circuits/Other/FL (days)	8.82	33			11.276				9.40	55			14.695
B.2.3.16.2.1	P-1	INP (Standalone)/>=10 circuits/Facility/FL (days)	0.00	0							0.00	0			
B.2.3.16.2.2	P-1	INP (Standalone)/>=10 circuits/Equipment/FL (days)	0.00	0							0.00	0			
B.2.3.16.2.3	P-1	INP (Standalone)/>=10 circuits/Other/FL (days)	0.00	0							0.00	0			
B.2.3.17.1.1	P-1	LNP (Standalone)/<10 circuits/Facility/FL (days)	8.84	382	0.00	0	8.466			YES	9.15	412	0.00	0	7.989
B.2.3.17.1.2	P-1	LNP (Standalone)/<10 circuits/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.17.1.3	P-1	LNP (Standalone)/<10 circuits/Other/FL (days)	8.82	33	0.00	0	11.276			YES	9.40	55	0.00	0	14.695
B.2.3.17.2.1	P-1	LNP (Standalone)/>=10 circuits/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.17.2.2	P-1	LNP (Standalone)/>=10 circuits/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.17.2.3	P-1	LNP (Standalone)/>=10 circuits/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.18.1.1	P-1	Digital Loop < DS1/<10 circuits/Facility/FL (days)	7.87	46	10.80	5	6.043	2.84553	-1.0298	YES	10.72	36	8.00	2	9.291
B.2.3.18.1.2	P-1	Digital Loop < DS1/<10 circuits/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.18.1.3	P-1	Digital Loop < DS1/<10 circuits/Other/FL (days)	2.25	20	0.00	0	2.403			YES	10.25	4	0.00	0	11.295

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - May 2002

						May							
			Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.3.7.1.2	P-1	Line Sharing/<10 circuits/Equipment/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.7.1.3	P-1	Line Sharing/<10 circuits/Other/FL (days)			YES	1.00	9	0.00	0	0.000			YES
B.2.3.7.2.1	P-1	Line Sharing/>=10 circuits/Facility/FL (days)				0.00	0						
B.2.3.7.2.2	P-1	Line Sharing/>=10 circuits/Equipment/FL (days)				0.00	0						
B.2.3.7.2.3	P-1	Line Sharing/>=10 circuits/Other/FL (days)				0.00	0						
B.2.3.8.1.1	P-1	2W Analog Loop Design/<10 circuits/Facility/FL (days)	8.00793	0.3959	YES	6.57	407	0.00	0	6.984			YES
B.2.3.8.1.2	P-1	2W Analog Loop Design/<10 circuits/Equipment/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.8.1.3	P-1	2W Analog Loop Design/<10 circuits/Other/FL (days)			YES	8.63	56	0.00	0	10.304			YES
B.2.3.8.2.1	P-1	2W Analog Loop Design/>=10 circuits/Facility/FL (days)			YES	1.43	7	0.00	0	1.134			YES
B.2.3.8.2.2	P-1	2W Analog Loop Design/>=10 circuits/Equipment/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.8.2.3	P-1	2W Analog Loop Design/>=10 circuits/Other/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.9.1.1	P-1	2W Analog Loop Non-Design/<10 circuits/Facility/FL (days)	5.66295	0.3802	YES	6.54	404	1.50	2	6.936	4.91670	1.0260	YES
B.2.3.9.1.2	P-1	2W Analog Loop Non-Design/<10 circuits/Equipment/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.9.1.3	P-1	2W Analog Loop Non-Design/<10 circuits/Other/FL (days)			YES	8.92	53	0.00	0	10.486			YES
B.2.3.9.2.1	P-1	2W Analog Loop Non-Design/>=10 circuits/Facility/FL (days)			YES	0.00	0	8.00	1				NO
B.2.3.9.2.2	P-1	2W Analog Loop Non-Design/>=10 circuits/Equipment/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.9.2.3	P-1	2W Analog Loop Non-Design/>=10 circuits/Other/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.10.1.1	P-1	2W Analog Loop w/INP Design/<10 circuits/Facility/FL (days)				6.57	407	0.00	0	6.984			YES
B.2.3.10.1.2	P-1	2W Analog Loop w/INP Design/<10 circuits/Equipment/FL (days)				0.00	0	0.00	0				YES
B.2.3.10.1.3	P-1	2W Analog Loop w/INP Design/<10 circuits/Other/FL (days)				8.63	56	0.00	0	10.304			YES
B.2.3.10.2.1	P-1	2W Analog Loop w/INP Design/>=10 circuits/Facility/FL (days)				1.43	7			1.134			
B.2.3.10.2.2	P-1	2W Analog Loop w/INP Design/>=10 circuits/Equipment/FL (days)				0.00	0						
B.2.3.10.2.3	P-1	2W Analog Loop w/INP Design/>=10 circuits/Other/FL (days)				0.00	0						
B.2.3.11.1.1	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Facility/FL (days)			YES	6.54	404	0.00	0	6.936			YES
B.2.3.11.1.2	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Equipment/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.11.1.3	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Other/FL (days)			YES	8.92	53	0.00	0	10.486			YES
B.2.3.11.2.1	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Facility/FL (days)				0.00	0						
B.2.3.11.2.2	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Equipment/FL (days)				0.00	0						
B.2.3.11.2.3	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Other/FL (days)				0.00	0						
B.2.3.12.1.1	P-1	2W Analog Loop w/LNP Design/<10 circuits/Facility/FL (days)			YES	6.57	407	25.00	1	6.984	6.99265	-2.6353	NO
B.2.3.12.1.2	P-1	2W Analog Loop w/LNP Design/<10 circuits/Equipment/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.12.1.3	P-1	2W Analog Loop w/LNP Design/<10 circuits/Other/FL (days)			YES	8.63	56	0.00	0	10.304			YES
B.2.3.12.2.1	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Facility/FL (days)			YES	1.43	7	0.00	0	1.134			YES
B.2.3.12.2.2	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Equipment/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.12.2.3	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Other/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.13.1.1	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Facility/FL (days)			YES	6.54	404	0.00	0	6.936			YES
B.2.3.13.1.2	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Equipment/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.13.1.3	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Other/FL (days)			YES	8.92	53	0.00	0	10.486			YES
B.2.3.13.2.1	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Facility/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.13.2.2	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Equipment/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.13.2.3	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Other/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.14.1.1	P-1	Other Design/<10 circuits/Facility/FL (days)				12.00	1			0.000			
B.2.3.14.1.2	P-1	Other Design/<10 circuits/Equipment/FL (days)				0.00	0						
B.2.3.14.1.3	P-1	Other Design/<10 circuits/Other/FL (days)				45.88	8			38.264			
B.2.3.14.2.1	P-1	Other Design/>=10 circuits/Facility/FL (days)				0.00	0						
B.2.3.14.2.2	P-1	Other Design/>=10 circuits/Equipment/FL (days)				0.00	0						
B.2.3.14.2.3	P-1	Other Design/>=10 circuits/Other/FL (days)				0.00	0						
B.2.3.15.1.1	P-1	Other Non-Design/<10 circuits/Facility/FL (days)	8.00793	0.5208	YES	6.57	407	0.00	0	6.984			YES
B.2.3.15.1.2	P-1	Other Non-Design/<10 circuits/Equipment/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.15.1.3	P-1	Other Non-Design/<10 circuits/Other/FL (days)			YES	8.63	56	0.00	0	10.304			YES
B.2.3.15.2.1	P-1	Other Non-Design/>=10 circuits/Facility/FL (days)				1.43	7			1.134			
B.2.3.15.2.2	P-1	Other Non-Design/>=10 circuits/Equipment/FL (days)				0.00	0						
B.2.3.15.2.3	P-1	Other Non-Design/>=10 circuits/Other/FL (days)				0.00	0						
B.2.3.16.1.1	P-1	INP (Standalone)/<10 circuits/Facility/FL (days)				6.54	404			6.936			
B.2.3.16.1.2	P-1	INP (Standalone)/<10 circuits/Equipment/FL (days)				0.00	0						
B.2.3.16.1.3	P-1	INP (Standalone)/<10 circuits/Other/FL (days)				8.92	53			10.486			
B.2.3.16.2.1	P-1	INP (Standalone)/>=10 circuits/Facility/FL (days)				0.00	0						
B.2.3.16.2.2	P-1	INP (Standalone)/>=10 circuits/Equipment/FL (days)				0.00	0						
B.2.3.16.2.3	P-1	INP (Standalone)/>=10 circuits/Other/FL (days)				0.00	0						
B.2.3.17.1.1	P-1	LNP (Standalone)/<10 circuits/Facility/FL (days)			YES	6.54	404	0.00	0	6.936			YES
B.2.3.17.1.2	P-1	LNP (Standalone)/<10 circuits/Equipment/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.17.1.3	P-1	LNP (Standalone)/<10 circuits/Other/FL (days)			YES	8.92	53	0.00	0	10.486			YES
B.2.3.17.2.1	P-1	LNP (Standalone)/>=10 circuits/Facility/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.17.2.2	P-1	LNP (Standalone)/>=10 circuits/Equipment/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.17.2.3	P-1	LNP (Standalone)/>=10 circuits/Other/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.18.1.1	P-1	Digital Loop < DS1/<10 circuits/Facility/FL (days)	6.74968	0.4033	YES	18.00	84	19.00	1	18.155	18.26303	-0.0548	YES
B.2.3.18.1.2	P-1	Digital Loop < DS1/<10 circuits/Equipment/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.18.1.3	P-1	Digital Loop < DS1/<10 circuits/Other/FL (days)			YES	1.80	10	0.00	0	2.530			YES

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - May 2002

			Benchmark/ Analog	August BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.3.18.2.1	P-1	Digital Loop < DS1/>=10 circuits/Facility/FL (days)	Digital Loop < DS1	0.00	0						
B.2.3.18.2.2	P-1	Digital Loop < DS1/>=10 circuits/Equipment/FL (days)	Digital Loop < DS1	0.00	0						
B.2.3.18.2.3	P-1	Digital Loop < DS1/>=10 circuits/Other/FL (days)	Digital Loop < DS1	0.00	0						
B.2.3.19.1.1	P-1	Digital Loop >= DS1/<10 circuits/Facility/FL (days)	Digital Loop >= DS1	0.00	0	3.00	1				NO
B.2.3.19.1.2	P-1	Digital Loop >= DS1/<10 circuits/Equipment/FL (days)	Digital Loop >= DS1	0.00	0	0.00	0				YES
B.2.3.19.1.3	P-1	Digital Loop >= DS1/<10 circuits/Other/FL (days)	Digital Loop >= DS1	2.00	1	0.00	0	0.000			YES
B.2.3.19.2.1	P-1	Digital Loop >= DS1/>=10 circuits/Facility/FL (days)	Digital Loop >= DS1	0.00	0						
B.2.3.19.2.2	P-1	Digital Loop >= DS1/>=10 circuits/Equipment/FL (days)	Digital Loop >= DS1	0.00	0						
B.2.3.19.2.3	P-1	Digital Loop >= DS1/>=10 circuits/Other/FL (days)	Digital Loop >= DS1	0.00	0						
		% Jeopardies - Mechanized									
B.2.5.1	P-2	Switch Ports/FL (%)	R&B (POTS)	0.72%	829,333						
B.2.5.2	P-2	Local Interoffice Transport/FL (%)	DS1/ DS3 - Interoffice	41.42%	2,453						
B.2.5.3	P-2	Loop + Port Combinations/FL (%)	R&B	0.72%	834,470	0.18%	67,345		0.00034	15.9434	YES
B.2.5.4	P-2	Combo Other/FL (%)	R&B&D - Disp	8.87%	76,180	64.86%	37		0.04676	-11.9740	NO
B.2.5.5	P-2	xDSL (ADSL, HDSL and UCL)/FL (%)	ADSL to Retail	6.92%	16,642	4.32%	162		0.02003	1.2956	YES
B.2.5.6	P-2	UNE ISDN/FL (%)	ISDN - BRI	7.68%	963	43.44%	122		0.02560	-13.9706	NO
B.2.5.7	P-2	Line Sharing/FL (%)	ADSL to Retail	6.92%	16,642	0.00%	1		0.25374	0.2726	YES
B.2.5.8	P-2	2W Analog Loop Design/FL (%)	R&B - Disp	0.72%	834,470	18.67%	241		0.00546	-32.8564	NO
B.2.5.9	P-2	2W Analog Loop Non-Design/FL (%)	R&B (POTS) excl SB Or	1.38%	429,258	7.79%	873		0.00396	-16.1925	NO
B.2.5.10	P-2	2W Analog Loop w/INP Design/FL (%)	R&B - Disp	0.72%	834,470						
B.2.5.11	P-2	2W Analog Loop w/INP Non-Design/FL (%)	R&B (POTS) excl SB Or	1.38%	429,258						
B.2.5.12	P-2	2W Analog Loop w/LNP Design/FL (%)	R&B - Disp	0.72%	834,470	10.85%	129		0.00747	-13.5664	NO
B.2.5.13	P-2	2W Analog Loop w/LNP Non-Design/FL (%)	R&B (POTS) excl SB Or	1.38%	429,258	4.64%	582		0.00484	-6.7225	NO
B.2.5.14	P-2	Other Design/FL (%)	Design	18.39%	4,052						
B.2.5.15	P-2	Other Non-Design/FL (%)	R&B	0.72%	834,470						
B.2.5.16	P-2	INP (Standalone)/FL (%)	R&B (POTS)	0.72%	829,333						
B.2.5.17	P-2	LNP (Standalone)/FL (%)	R&B (POTS)	0.72%	829,333	0.00%	2,858		0.00158	4.5313	YES
B.2.5.18	P-2	Digital Loop < DS1/FL (%)	Digital Loop < DS1	7.37%	18,598	21.51%	279		0.01576	-8.9738	NO
B.2.5.19	P-2	Digital Loop >= DS1/FL (%)	Digital Loop >= DS1	10.18%	2,850	66.54%	266		0.01938	-29.0808	NO
		% Jeopardies - Non-Mechanized									
B.2.6.1	P-2	Switch Ports/FL (%)	Diagnostic								Diagnostic
B.2.6.2	P-2	Local Interoffice Transport/FL (%)	Diagnostic			0.00%	4				Diagnostic
B.2.6.3	P-2	Loop + Port Combinations/FL (%)	Diagnostic			1.24%	2,254				Diagnostic
B.2.6.4	P-2	Combo Other/FL (%)	Diagnostic			41.82%	55				Diagnostic
B.2.6.5	P-2	xDSL (ADSL, HDSL and UCL)/FL (%)	Diagnostic			11.36%	44				Diagnostic
B.2.6.6	P-2	UNE ISDN/FL (%)	Diagnostic			29.51%	122				Diagnostic
B.2.6.7	P-2	Line Sharing/FL (%)	Diagnostic			0.72%	139				Diagnostic
B.2.6.8	P-2	2W Analog Loop Design/FL (%)	Diagnostic			10.53%	57				Diagnostic
B.2.6.9	P-2	2W Analog Loop Non-Design/FL (%)	Diagnostic			3.64%	55				Diagnostic
B.2.6.10	P-2	2W Analog Loop w/INP Design/FL (%)	Diagnostic			0.00%	1				Diagnostic
B.2.6.11	P-2	2W Analog Loop w/INP Non-Design/FL (%)	Diagnostic			0.00%	2				Diagnostic
B.2.6.12	P-2	2W Analog Loop w/LNP Design/FL (%)	Diagnostic			15.38%	13				Diagnostic
B.2.6.13	P-2	2W Analog Loop w/LNP Non-Design/FL (%)	Diagnostic			5.88%	17				Diagnostic
B.2.6.14	P-2	Other Design/FL (%)	Diagnostic			0.00%	1				Diagnostic
B.2.6.15	P-2	Other Non-Design/FL (%)	Diagnostic			0.00%	3				Diagnostic
B.2.6.16	P-2	INP (Standalone)/FL (%)	Diagnostic								Diagnostic
B.2.6.17	P-2	LNP (Standalone)/FL (%)	Diagnostic			0.00%	422				Diagnostic
B.2.6.18	P-2	Digital Loop < DS1/FL (%)	Diagnostic			23.60%	161				Diagnostic
B.2.6.19	P-2	Digital Loop >= DS1/FL (%)	Diagnostic			51.72%	87				Diagnostic
		Average Jeopardy Notice Interval - Mechanized									
B.2.8.1	P-2	Switch Ports/FL (hours)	>= 48 hrs								
B.2.8.2	P-2	Local Interoffice Transport/FL (hours)	>= 48 hrs								
B.2.8.3	P-2	Loop + Port Combinations/FL (hours)	>= 48 hrs			133.34	76				YES
B.2.8.4	P-2	Combo Other/FL (hours)	>= 48 hrs			339.80	24				YES
B.2.8.5	P-2	xDSL (ADSL, HDSL and UCL)/FL (hours)	>= 48 hrs			118.06	5				YES
B.2.8.6	P-2	UNE ISDN/FL (hours)	>= 48 hrs			318.40	53				YES
B.2.8.7	P-2	Line Sharing/FL (hours)	>= 48 hrs								
B.2.8.8	P-2	2W Analog Loop Design/FL (hours)	>= 48 hrs			173.69	44				YES
B.2.8.9	P-2	2W Analog Loop Non-Design/FL (hours)	>= 48 hrs			111.21	58				YES
B.2.8.10	P-2	2W Analog Loop w/INP Design/FL (hours)	>= 48 hrs								
B.2.8.11	P-2	2W Analog Loop w/INP Non-Design/FL (hours)	>= 48 hrs								
B.2.8.12	P-2	2W Analog Loop w/LNP Design/FL (hours)	>= 48 hrs			141.32	14				YES
B.2.8.13	P-2	2W Analog Loop w/LNP Non-Design/FL (hours)	>= 48 hrs			120.06	27				YES
B.2.8.14	P-2	Other Design/FL (hours)	>= 48 hrs								
B.2.8.15	P-2	Other Non-Design/FL (hours)	>= 48 hrs								
B.2.8.16	P-2	INP (Standalone)/FL (hours)	>= 48 hrs								

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - May 2002

			July								June				
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation
B.2.3.18.2.1	P-1	Digital Loop < DS1/>=10 circuits/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.18.2.2	P-1	Digital Loop < DS1/>=10 circuits/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.18.2.3	P-1	Digital Loop < DS1/>=10 circuits/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.19.1.1	P-1	Digital Loop >= DS1/<10 circuits/Facility/FL (days)	5.50	4	6.00	1	3.317	3.70810	-0.1348	YES	0.00	0	10.00	1	
B.2.3.19.1.2	P-1	Digital Loop >= DS1/<10 circuits/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.19.1.3	P-1	Digital Loop >= DS1/<10 circuits/Other/FL (days)	7.00	1	0.00	0	0.000			YES	0.00	0	0.00	0	
B.2.3.19.2.1	P-1	Digital Loop >= DS1/>=10 circuits/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.19.2.2	P-1	Digital Loop >= DS1/>=10 circuits/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
B.2.3.19.2.3	P-1	Digital Loop >= DS1/>=10 circuits/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	
		% Jeopardies - Mechanized													
B.2.5.1	P-2	Switch Ports/FL (%)	0.62%	781,014							0.64%	738,209			
B.2.5.2	P-2	Local Interoffice Transport/FL (%)	31.90%	2,301							42.61%	2,173			
B.2.5.3	P-2	Loop + Port Combinations/FL (%)	0.63%	784,350	0.13%	64,975		0.00032	15.4819	YES	0.65%	741,237	0.17%	49,281	
B.2.5.4	P-2	Combo Other/FL (%)	8.37%	65,862	77.78%	18		0.06529	-10.6302	NO	8.99%	61,123	60.00%	5	
B.2.5.5	P-2	xDSL (ADSL, HDSL and UCL)/FL (%)	7.84%	15,452	4.40%	91		0.02826	1.2180	YES	7.00%	14,650	5.33%	150	
B.2.5.6	P-2	UNE ISDN/FL (%)	11.94%	695	36.43%	140		0.03004	-8.1509	NO	13.86%	577	39.34%	122	
B.2.5.7	P-2	Line Sharing/FL (%)	7.84%	15,452	0.00%	3		0.15518	0.5050	YES	7.00%	14,650	0.00%	1	
B.2.5.8	P-2	2W Analog Loop Design/FL (%)	0.63%	784,350	19.08%	304		0.00454	-40.6836	NO	0.65%	741,237	13.98%	322	
B.2.5.9	P-2	2W Analog Loop Non-Design/FL (%)	1.23%	390,887	6.25%	960		0.00357	-14.0616	NO	1.21%	388,805	7.15%	811	
B.2.5.10	P-2	2W Analog Loop w/INP Design/FL (%)	0.63%	784,350							0.65%	741,237			
B.2.5.11	P-2	2W Analog Loop w/INP Non-Design/FL (%)	1.23%	390,887							1.21%	388,805			
B.2.5.12	P-2	2W Analog Loop w/LNP Design/FL (%)	0.63%	784,350	8.52%	176		0.00596	-13.2453	NO	0.65%	741,237	7.00%	243	
B.2.5.13	P-2	2W Analog Loop w/LNP Non-Design/FL (%)	1.23%	390,887	4.74%	696		0.00419	-8.3749	NO	1.21%	388,805	3.89%	746	
B.2.5.14	P-2	Other Design/FL (%)	15.68%	3,801							16.67%	4,194			
B.2.5.15	P-2	Other Non-Design/FL (%)	0.63%	784,350	100.00%	1		0.07906	-12.5698	NO	0.65%	741,237			
B.2.5.16	P-2	INP (Standalone)/FL (%)	0.62%	781,014							0.64%	738,209			
B.2.5.17	P-2	LNP (Standalone)/FL (%)	0.62%	781,014	0.00%	2,711		0.00151	4.0976	YES	0.64%	738,209	0.00%	2,410	
B.2.5.18	P-2	Digital Loop < DS1/FL (%)	8.14%	16,852	24.55%	224		0.01839	-8.9292	NO	7.90%	16,047	21.13%	265	
B.2.5.19	P-2	Digital Loop >= DS1/FL (%)	13.08%	1,713	53.46%	318		0.02059	-19.6163	NO	10.03%	1,087	63.60%	261	
		% Jeopardies - Non-Mechanized													
B.2.6.1	P-2	Switch Ports/FL (%)								Diagnostic					
B.2.6.2	P-2	Local Interoffice Transport/FL (%)			0.00%	7				Diagnostic			0.00%	15	
B.2.6.3	P-2	Loop + Port Combinations/FL (%)			1.12%	1,873				Diagnostic			1.84%	1,688	
B.2.6.4	P-2	Combo Other/FL (%)			40.00%	75				Diagnostic			54.46%	112	
B.2.6.5	P-2	xDSL (ADSL, HDSL and UCL)/FL (%)			9.14%	175				Diagnostic			10.32%	126	
B.2.6.6	P-2	UNE ISDN/FL (%)			20.59%	204				Diagnostic			20.29%	138	
B.2.6.7	P-2	Line Sharing/FL (%)			0.00%	143				Diagnostic			0.00%	166	
B.2.6.8	P-2	2W Analog Loop Design/FL (%)			6.67%	60				Diagnostic			7.02%	57	
B.2.6.9	P-2	2W Analog Loop Non-Design/FL (%)			6.67%	90				Diagnostic			2.73%	110	
B.2.6.10	P-2	2W Analog Loop w/INP Design/FL (%)			0.00%	1				Diagnostic					
B.2.6.11	P-2	2W Analog Loop w/INP Non-Design/FL (%)			0.00%	2				Diagnostic			33.33%	3	
B.2.6.12	P-2	2W Analog Loop w/LNP Design/FL (%)			0.00%	14				Diagnostic			11.11%	9	
B.2.6.13	P-2	2W Analog Loop w/LNP Non-Design/FL (%)			7.41%	27				Diagnostic			0.00%	12	
B.2.6.14	P-2	Other Design/FL (%)			0.00%	2				Diagnostic					
B.2.6.15	P-2	Other Non-Design/FL (%)			0.00%	7				Diagnostic			8.00%	25	
B.2.6.16	P-2	INP (Standalone)/FL (%)								Diagnostic					
B.2.6.17	P-2	LNP (Standalone)/FL (%)			0.00%	375				Diagnostic			0.00%	371	
B.2.6.18	P-2	Digital Loop < DS1/FL (%)			14.67%	368				Diagnostic			15.16%	244	
B.2.6.19	P-2	Digital Loop >= DS1/FL (%)			49.43%	87				Diagnostic			56.99%	93	
		Average Jeopardy Notice Interval - Mechanized													
B.2.8.1	P-2	Switch Ports/FL (hours)													
B.2.8.2	P-2	Local Interoffice Transport/FL (hours)			138.34	56				YES			117.58	62	
B.2.8.3	P-2	Loop + Port Combinations/FL (hours)			362.44	14				YES			293.61	3	
B.2.8.4	P-2	Combo Other/FL (hours)			76.45	3				YES			95.37	6	
B.2.8.5	P-2	xDSL (ADSL, HDSL and UCL)/FL (hours)			327.15	50				YES			324.35	47	
B.2.8.6	P-2	UNE ISDN/FL (hours)													
B.2.8.7	P-2	Line Sharing/FL (hours)			173.25	55				YES			147.19	44	
B.2.8.8	P-2	2W Analog Loop Design/FL (hours)			153.12	55				YES			99.83	42	
B.2.8.9	P-2	2W Analog Loop Non-Design/FL (hours)													
B.2.8.10	P-2	2W Analog Loop w/INP Design/FL (hours)													
B.2.8.11	P-2	2W Analog Loop w/INP Non-Design/FL (hours)			153.69	15				YES			178.08	16	
B.2.8.12	P-2	2W Analog Loop w/LNP Design/FL (hours)			150.39	32				YES			112.94	28	
B.2.8.13	P-2	2W Analog Loop w/LNP Non-Design/FL (hours)													
B.2.8.14	P-2	Other Design/FL (hours)			436.60	1				YES					
B.2.8.15	P-2	Other Non-Design/FL (hours)													
B.2.8.16	P-2	INP (Standalone)/FL (hours)													

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			Standard Error	Zscore	Equity	May BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.3.18.2.1	P-1	Digital Loop < DS1/>=10 circuits/Facility/FL (days)			YES	0.00	0						
B.2.3.18.2.2	P-1	Digital Loop < DS1/>=10 circuits/Equipment/FL (days)			YES	0.00	0						
B.2.3.18.2.3	P-1	Digital Loop < DS1/>=10 circuits/Other/FL (days)			YES	0.00	0						
B.2.3.19.1.1	P-1	Digital Loop >= DS1/<10 circuits/Facility/FL (days)			NO	0.00	0	2.00	2				NO
B.2.3.19.1.2	P-1	Digital Loop >= DS1/<10 circuits/Equipment/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.19.1.3	P-1	Digital Loop >= DS1/<10 circuits/Other/FL (days)			YES	0.00	0	0.00	0				YES
B.2.3.19.2.1	P-1	Digital Loop >= DS1/>=10 circuits/Facility/FL (days)			YES	0.00	0						
B.2.3.19.2.2	P-1	Digital Loop >= DS1/>=10 circuits/Equipment/FL (days)			YES	0.00	0						
B.2.3.19.2.3	P-1	Digital Loop >= DS1/>=10 circuits/Other/FL (days)			YES	0.00	0						
		% Jeopardies - Mechanized											
B.2.5.1	P-2	Switch Ports/FL (%)				0.64%	833,269						
B.2.5.2	P-2	Local Interoffice Transport/FL (%)				46.09%	2,263						
B.2.5.3	P-2	Loop + Port Combinations/FL (%)	0.00037	12.6847	YES	0.65%	837,334	0.15%	40,051		0.00041	12.0940	YES
B.2.5.4	P-2	Combo Other/FL (%)	0.12795	-3.9865	NO	7.20%	84,333	100.00%	4		0.12921	-7.1826	NO
B.2.5.5	P-2	xDSL (ADSL, HDSL and UCL)/FL (%)	0.02093	0.7945	YES	7.96%	14,327						
B.2.5.6	P-2	UNE ISDN/FL (%)	0.03444	-7.3990	NO	8.71%	769	38.89%	162		0.02438	-12.3775	NO
B.2.5.7	P-2	Line Sharing/FL (%)	0.25510	0.2743	YES	7.96%	14,327	0.00%	3		0.15633	0.5094	YES
B.2.5.8	P-2	2W Analog Loop Design/FL (%)	0.00447	-29.7993	NO	0.65%	837,334	16.84%	285		0.00474	-34.1509	NO
B.2.5.9	P-2	2W Analog Loop Non-Design/FL (%)	0.00385	-15.4455	NO	1.17%	453,645	7.21%	1,373		0.00291	-20.7662	NO
B.2.5.10	P-2	2W Analog Loop w/INP Design/FL (%)				0.65%	837,334						
B.2.5.11	P-2	2W Analog Loop w/INP Non-Design/FL (%)				1.17%	453,645						
B.2.5.12	P-2	2W Analog Loop w/LNP Design/FL (%)	0.00515	-12.3310	NO	0.65%	837,334	12.97%	370		0.00416	-29.6151	NO
B.2.5.13	P-2	2W Analog Loop w/LNP Non-Design/FL (%)	0.00401	-6.6735	NO	1.17%	453,645	4.25%	1,272		0.00302	-10.1749	NO
B.2.5.14	P-2	Other Design/FL (%)				16.39%	4,228						
B.2.5.15	P-2	Other Non-Design/FL (%)				0.65%	837,334						
B.2.5.16	P-2	INP (Standalone)/FL (%)				0.64%	833,269						
B.2.5.17	P-2	LNP (Standalone)/FL (%)	0.00162	3.9279	YES	0.64%	833,269	0.00%	3,008		0.00145	4.3860	YES
B.2.5.18	P-2	Digital Loop < DS1/FL (%)	0.01671	-7.9186	NO	8.45%	16,216	38.89%	162		0.02196	-13.8621	NO
B.2.5.19	P-2	Digital Loop >= DS1/FL (%)	0.02070	-25.8756	NO	8.27%	1,742	72.26%	274		0.01790	-35.7592	NO
		% Jeopardies - Non-Mechanized											
B.2.6.1	P-2	Switch Ports/FL (%)			Diagnostic								Diagnostic
B.2.6.2	P-2	Local Interoffice Transport/FL (%)			Diagnostic			0.00%	30				Diagnostic
B.2.6.3	P-2	Loop + Port Combinations/FL (%)			Diagnostic			0.63%	2,396				Diagnostic
B.2.6.4	P-2	Combo Other/FL (%)			Diagnostic			55.43%	175				Diagnostic
B.2.6.5	P-2	xDSL (ADSL, HDSL and UCL)/FL (%)			Diagnostic			6.56%	305				Diagnostic
B.2.6.6	P-2	UNE ISDN/FL (%)			Diagnostic			23.40%	329				Diagnostic
B.2.6.7	P-2	Line Sharing/FL (%)			Diagnostic			0.45%	223				Diagnostic
B.2.6.8	P-2	2W Analog Loop Design/FL (%)			Diagnostic			4.85%	103				Diagnostic
B.2.6.9	P-2	2W Analog Loop Non-Design/FL (%)			Diagnostic			3.21%	218				Diagnostic
B.2.6.10	P-2	2W Analog Loop w/INP Design/FL (%)			Diagnostic			0.00%	1				Diagnostic
B.2.6.11	P-2	2W Analog Loop w/INP Non-Design/FL (%)			Diagnostic			0.00%	2				Diagnostic
B.2.6.12	P-2	2W Analog Loop w/LNP Design/FL (%)			Diagnostic			8.00%	25				Diagnostic
B.2.6.13	P-2	2W Analog Loop w/LNP Non-Design/FL (%)			Diagnostic			5.56%	36				Diagnostic
B.2.6.14	P-2	Other Design/FL (%)			Diagnostic								Diagnostic
B.2.6.15	P-2	Other Non-Design/FL (%)			Diagnostic			3.13%	64				Diagnostic
B.2.6.16	P-2	INP (Standalone)/FL (%)			Diagnostic								Diagnostic
B.2.6.17	P-2	LNP (Standalone)/FL (%)			Diagnostic			0.00%	365				Diagnostic
B.2.6.18	P-2	Digital Loop < DS1/FL (%)			Diagnostic			15.33%	613				Diagnostic
B.2.6.19	P-2	Digital Loop >= DS1/FL (%)			Diagnostic			45.97%	211				Diagnostic
		Average Jeopardy Notice Interval - Mechanized											
B.2.8.1	P-2	Switch Ports/FL (hours)											
B.2.8.2	P-2	Local Interoffice Transport/FL (hours)											
B.2.8.3	P-2	Loop + Port Combinations/FL (hours)			YES			121.73	40				YES
B.2.8.4	P-2	Combo Other/FL (hours)			YES			280.03	4				YES
B.2.8.5	P-2	xDSL (ADSL, HDSL and UCL)/FL (hours)			YES								
B.2.8.6	P-2	UNE ISDN/FL (hours)			YES			314.60	59				YES
B.2.8.7	P-2	Line Sharing/FL (hours)											
B.2.8.8	P-2	2W Analog Loop Design/FL (hours)			YES			200.55	47				YES
B.2.8.9	P-2	2W Analog Loop Non-Design/FL (hours)			YES			113.61	79				YES
B.2.8.10	P-2	2W Analog Loop w/INP Design/FL (hours)											
B.2.8.11	P-2	2W Analog Loop w/INP Non-Design/FL (hours)											
B.2.8.12	P-2	2W Analog Loop w/LNP Design/FL (hours)			YES			163.91	47				YES
B.2.8.13	P-2	2W Analog Loop w/LNP Non-Design/FL (hours)			YES			149.60	54				YES
B.2.8.14	P-2	Other Design/FL (hours)											
B.2.8.15	P-2	Other Non-Design/FL (hours)											
B.2.8.16	P-2	INP (Standalone)/FL (hours)											

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - May 2002

			Benchmark/ Analog	August							
				BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.8.17	P-2	LNP (Standalone)/FL (hours)	>= 48 hrs								
B.2.8.18	P-2	Digital Loop < DS1/FL (hours)	>= 48 hrs			301.12	58				YES
B.2.8.19	P-2	Digital Loop >= DS1/FL (hours)	>= 48 hrs			185.40	177				YES
		Average Jeopardy Notice Interval - Non-Mechanized									
B.2.9.1	P-2	Switch Ports/FL (hours)	Diagnostic								Diagnostic
B.2.9.2	P-2	Local Interoffice Transport/FL (hours)	Diagnostic								Diagnostic
B.2.9.3	P-2	Loop + Port Combinations/FL (hours)	Diagnostic			126.05	25				Diagnostic
B.2.9.4	P-2	Combo Other/FL (hours)	Diagnostic			382.68	23				Diagnostic
B.2.9.5	P-2	xDSL (ADSL, HDSL and UCL)/FL (hours)	Diagnostic			121.84	5				Diagnostic
B.2.9.6	P-2	UNE ISDN/FL (hours)	Diagnostic			254.13	33				Diagnostic
B.2.9.7	P-2	Line Sharing/FL (hours)	Diagnostic								Diagnostic
B.2.9.8	P-2	2W Analog Loop Design/FL (hours)	Diagnostic			106.08	6				Diagnostic
B.2.9.9	P-2	2W Analog Loop Non-Design/FL (hours)	Diagnostic			300.11	2				Diagnostic
B.2.9.10	P-2	2W Analog Loop w/INP Design/FL (hours)	Diagnostic								Diagnostic
B.2.9.11	P-2	2W Analog Loop w/INP Non-Design/FL (hours)	Diagnostic								Diagnostic
B.2.9.12	P-2	2W Analog Loop w/LNP Design/FL (hours)	Diagnostic			138.84	2				Diagnostic
B.2.9.13	P-2	2W Analog Loop w/LNP Non-Design/FL (hours)	Diagnostic			133.07	1				Diagnostic
B.2.9.14	P-2	Other Design/FL (hours)	Diagnostic								Diagnostic
B.2.9.15	P-2	Other Non-Design/FL (hours)	Diagnostic								Diagnostic
B.2.9.16	P-2	INP (Standalone)/FL (hours)	Diagnostic								Diagnostic
B.2.9.17	P-2	LNP (Standalone)/FL (hours)	Diagnostic								Diagnostic
B.2.9.18	P-2	Digital Loop < DS1/FL (hours)	Diagnostic			243.50	35				Diagnostic
B.2.9.19	P-2	Digital Loop >= DS1/FL (hours)	Diagnostic			246.22	46				Diagnostic
		% Jeopardy Notice >= 48 hours - Mechanized									
B.2.10.1	P-2	Switch Ports/FL (%)	95% >= 48 hrs								
B.2.10.2	P-2	Local Interoffice Transport/FL (%)	95% >= 48 hrs								
B.2.10.3	P-2	Loop + Port Combinations/FL (%)	95% >= 48 hrs			98.68%	76				YES
B.2.10.4	P-2	Combo Other/FL (%)	95% >= 48 hrs			100.00%	24				YES
B.2.10.5	P-2	xDSL (ADSL, HDSL and UCL)/FL (%)	95% >= 48 hrs			100.00%	5				YES
B.2.10.6	P-2	UNE ISDN/FL (%)	95% >= 48 hrs			100.00%	53				YES
B.2.10.7	P-2	Line Sharing/FL (%)	95% >= 48 hrs								
B.2.10.8	P-2	2W Analog Loop Design/FL (%)	95% >= 48 hrs			100.00%	44				YES
B.2.10.9	P-2	2W Analog Loop Non-Design/FL (%)	95% >= 48 hrs			98.28%	58				YES
B.2.10.10	P-2	2W Analog Loop w/INP Design/FL (%)	95% >= 48 hrs								
B.2.10.11	P-2	2W Analog Loop w/INP Non-Design/FL (%)	95% >= 48 hrs								
B.2.10.12	P-2	2W Analog Loop w/LNP Design/FL (%)	95% >= 48 hrs			100.00%	14				YES
B.2.10.13	P-2	2W Analog Loop w/LNP Non-Design/FL (%)	95% >= 48 hrs			100.00%	27				YES
B.2.10.14	P-2	Other Design/FL (%)	95% >= 48 hrs								
B.2.10.15	P-2	Other Non-Design/FL (%)	95% >= 48 hrs								
B.2.10.16	P-2	INP (Standalone)/FL (%)	95% >= 48 hrs								
B.2.10.17	P-2	LNP (Standalone)/FL (%)	95% >= 48 hrs								
B.2.10.18	P-2	Digital Loop < DS1/FL (%)	95% >= 48 hrs			100.00%	58				YES
B.2.10.19	P-2	Digital Loop >= DS1/FL (%)	95% >= 48 hrs			99.44%	177				YES
		% Jeopardy Notice >= 48 hours - Non-Mechanized									
B.2.11.1	P-2	Switch Ports/FL (%)	Diagnostic								Diagnostic
B.2.11.2	P-2	Local Interoffice Transport/FL (%)	Diagnostic								Diagnostic
B.2.11.3	P-2	Loop + Port Combinations/FL (%)	Diagnostic			92.00%	25				Diagnostic
B.2.11.4	P-2	Combo Other/FL (%)	Diagnostic			100.00%	23				Diagnostic
B.2.11.5	P-2	xDSL (ADSL, HDSL and UCL)/FL (%)	Diagnostic			80.00%	5				Diagnostic
B.2.11.6	P-2	UNE ISDN/FL (%)	Diagnostic			96.97%	33				Diagnostic
B.2.11.7	P-2	Line Sharing/FL (%)	Diagnostic								Diagnostic
B.2.11.8	P-2	2W Analog Loop Design/FL (%)	Diagnostic			100.00%	6				Diagnostic
B.2.11.9	P-2	2W Analog Loop Non-Design/FL (%)	Diagnostic			100.00%	2				Diagnostic
B.2.11.10	P-2	2W Analog Loop w/INP Design/FL (%)	Diagnostic								Diagnostic
B.2.11.11	P-2	2W Analog Loop w/INP Non-Design/FL (%)	Diagnostic								Diagnostic
B.2.11.12	P-2	2W Analog Loop w/LNP Design/FL (%)	Diagnostic			100.00%	2				Diagnostic
B.2.11.13	P-2	2W Analog Loop w/LNP Non-Design/FL (%)	Diagnostic			100.00%	1				Diagnostic
B.2.11.14	P-2	Other Design/FL (%)	Diagnostic								Diagnostic
B.2.11.15	P-2	Other Non-Design/FL (%)	Diagnostic								Diagnostic
B.2.11.16	P-2	INP (Standalone)/FL (%)	Diagnostic								Diagnostic
B.2.11.17	P-2	LNP (Standalone)/FL (%)	Diagnostic								Diagnostic
B.2.11.18	P-2	Digital Loop < DS1/FL (%)	Diagnostic			94.29%	35				Diagnostic
B.2.11.19	P-2	Digital Loop >= DS1/FL (%)	Diagnostic			100.00%	46				Diagnostic
		Coordinated Customers Conversions									
B.2.12.1	P-7	Loops with INP/FL (%)	>= 95% w in 15 min								
B.2.12.2	P-7	Loops with LNP/FL (%)	>= 95% w in 15 min			100.00%	2,240				YES

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - M ay 2002

			July								June				
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation
B.2.8.17	P-2	LNP (Standalone)/FL (hours)													
B.2.8.18	P-2	Digital Loop < DS1/FL (hours)			312.96	53				YES			298.43	53	
B.2.8.19	P-2	Digital Loop >= DS1/FL (hours)			198.27	170				YES			188.20	164	
		Average Jeopardy Notice Interval - Non-Mechanized													
B.2.9.1	P-2	Switch Ports/FL (hours)								Diagnostic					
B.2.9.2	P-2	Local Interoffice Transport/FL (hours)								Diagnostic					
B.2.9.3	P-2	Loop + Port Combinations/FL (hours)			137.32	13				Diagnostic			149.99	23	
B.2.9.4	P-2	Combo Other/FL (hours)			366.99	30				Diagnostic			339.52	60	
B.2.9.5	P-2	xDSL (ADSL, HDSL and UCL)/FL (hours)			195.53	13				Diagnostic			174.12	12	
B.2.9.6	P-2	UNE ISDN/FL (hours)			297.07	41				Diagnostic			272.64	24	
B.2.9.7	P-2	Line Sharing/FL (hours)								Diagnostic					
B.2.9.8	P-2	2W Analog Loop Design/FL (hours)			226.85	4				Diagnostic			90.25	3	
B.2.9.9	P-2	2W Analog Loop Non-Design/FL (hours)			245.21	5				Diagnostic			121.13	2	
B.2.9.10	P-2	2W Analog Loop w/INP Design/FL (hours)								Diagnostic					
B.2.9.11	P-2	2W Analog Loop w/INP Non-Design/FL (hours)								Diagnostic			60.68	1	
B.2.9.12	P-2	2W Analog Loop w/LNP Design/FL (hours)								Diagnostic			206.80	1	
B.2.9.13	P-2	2W Analog Loop w/LNP Non-Design/FL (hours)			152.76	2				Diagnostic					
B.2.9.14	P-2	Other Design/FL (hours)								Diagnostic					
B.2.9.15	P-2	Other Non-Design/FL (hours)								Diagnostic			118.96	2	
B.2.9.16	P-2	INP (Standalone)/FL (hours)								Diagnostic					
B.2.9.17	P-2	LNP (Standalone)/FL (hours)								Diagnostic					
B.2.9.18	P-2	Digital Loop < DS1/FL (hours)			277.03	51				Diagnostic			248.58	32	
B.2.9.19	P-2	Digital Loop >= DS1/FL (hours)			219.12	42				Diagnostic			257.18	51	
		% Jeopardy Notice >= 48 hours - Mechanized													
B.2.10.1	P-2	Switch Ports/FL (%)													
B.2.10.2	P-2	Local Interoffice Transport/FL (%)													
B.2.10.3	P-2	Loop + Port Combinations/FL (%)			98.21%	56				YES			88.71%	62	
B.2.10.4	P-2	Combo Other/FL (%)			100.00%	14				YES			100.00%	3	
B.2.10.5	P-2	xDSL (ADSL, HDSL and UCL)/FL (%)			100.00%	3				YES			83.33%	6	
B.2.10.6	P-2	UNE ISDN/FL (%)			100.00%	50				YES			97.87%	47	
B.2.10.7	P-2	Line Sharing/FL (%)													
B.2.10.8	P-2	2W Analog Loop Design/FL (%)			96.36%	55				YES			100.00%	44	
B.2.10.9	P-2	2W Analog Loop Non-Design/FL (%)			98.18%	55				YES			97.62%	42	
B.2.10.10	P-2	2W Analog Loop w/INP Design/FL (%)													
B.2.10.11	P-2	2W Analog Loop w/INP Non-Design/FL (%)													
B.2.10.12	P-2	2W Analog Loop w/LNP Design/FL (%)			100.00%	15				YES			100.00%	16	
B.2.10.13	P-2	2W Analog Loop w/LNP Non-Design/FL (%)			90.63%	32				NO			100.00%	28	
B.2.10.14	P-2	Other Design/FL (%)													
B.2.10.15	P-2	Other Non-Design/FL (%)			100.00%	1				YES					
B.2.10.16	P-2	INP (Standalone)/FL (%)													
B.2.10.17	P-2	LNP (Standalone)/FL (%)													
B.2.10.18	P-2	Digital Loop < DS1/FL (%)			100.00%	53				YES			96.23%	53	
B.2.10.19	P-2	Digital Loop >= DS1/FL (%)			99.41%	170				YES			99.39%	164	
		% Jeopardy Notice >= 48 hours - Non-Mechanized													
B.2.11.1	P-2	Switch Ports/FL (%)								Diagnostic					
B.2.11.2	P-2	Local Interoffice Transport/FL (%)								Diagnostic					
B.2.11.3	P-2	Loop + Port Combinations/FL (%)			92.31%	13				Diagnostic			86.96%	23	
B.2.11.4	P-2	Combo Other/FL (%)			100.00%	30				Diagnostic			100.00%	60	
B.2.11.5	P-2	xDSL (ADSL, HDSL and UCL)/FL (%)			92.31%	13				Diagnostic			83.33%	12	
B.2.11.6	P-2	UNE ISDN/FL (%)			100.00%	41				Diagnostic			95.83%	24	
B.2.11.7	P-2	Line Sharing/FL (%)								Diagnostic					
B.2.11.8	P-2	2W Analog Loop Design/FL (%)			100.00%	4				Diagnostic			100.00%	3	
B.2.11.9	P-2	2W Analog Loop Non-Design/FL (%)			80.00%	5				Diagnostic			50.00%	2	
B.2.11.10	P-2	2W Analog Loop w/INP Design/FL (%)								Diagnostic					
B.2.11.11	P-2	2W Analog Loop w/INP Non-Design/FL (%)								Diagnostic			100.00%	1	
B.2.11.12	P-2	2W Analog Loop w/LNP Design/FL (%)								Diagnostic			100.00%	1	
B.2.11.13	P-2	2W Analog Loop w/LNP Non-Design/FL (%)			100.00%	2				Diagnostic					
B.2.11.14	P-2	Other Design/FL (%)								Diagnostic					
B.2.11.15	P-2	Other Non-Design/FL (%)								Diagnostic			100.00%	2	
B.2.11.16	P-2	INP (Standalone)/FL (%)								Diagnostic					
B.2.11.17	P-2	LNP (Standalone)/FL (%)								Diagnostic					
B.2.11.18	P-2	Digital Loop < DS1/FL (%)			98.04%	51				Diagnostic			93.75%	32	
B.2.11.19	P-2	Digital Loop >= DS1/FL (%)			100.00%	42				Diagnostic			100.00%	51	
		Coordinated Customers Conversions													
B.2.12.1	P-7	Loops with INP/FL (%)													
B.2.12.2	P-7	Loops with LNP/FL (%)			100.00%	2,841				YES			100.00%	3,254	

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - May 2002

			Standard Error	Zscore	Equity	May: BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.8.17	P-2	LNP (Standalone)/FL (hours)											
B.2.8.18	P-2	Digital Loop < DS1/FL (hours)			YES			314.60	59				YES
B.2.8.19	P-2	Digital Loop >= DS1/FL (hours)			YES			182.61	197				YES
		Average Jeoperdy Notice Interval - Non-Mechanized											
B.2.9.1	P-2	Switch Ports/FL (hours)			Diagnostic								Diagnostic
B.2.9.2	P-2	Local Interoffice Transport/FL (hours)			Diagnostic								Diagnostic
B.2.9.3	P-2	Loop + Port Combinations/FL (hours)			Diagnostic			196.35	11				Diagnostic
B.2.9.4	P-2	Combo Other/FL (hours)			Diagnostic			317.51	97				Diagnostic
B.2.9.5	P-2	xDSL (ADSL, HDSL and UCL)/FL (hours)			Diagnostic			126.33	17				Diagnostic
B.2.9.6	P-2	UNE ISDN/FL (hours)			Diagnostic			272.17	69				Diagnostic
B.2.9.7	P-2	Line Sharing/FL (hours)			Diagnostic								Diagnostic
B.2.9.8	P-2	2W Analog Loop Design/FL (hours)			Diagnostic			101.24	5				Diagnostic
B.2.9.9	P-2	2W Analog Loop Non-Design/FL (hours)			Diagnostic			142.18	6				Diagnostic
B.2.9.10	P-2	2W Analog Loop w/INP Design/FL (hours)			Diagnostic								Diagnostic
B.2.9.11	P-2	2W Analog Loop w/INP Non-Design/FL (hours)			Diagnostic								Diagnostic
B.2.9.12	P-2	2W Analog Loop w/LNP Design/FL (hours)			Diagnostic			127.73	2				Diagnostic
B.2.9.13	P-2	2W Analog Loop w/LNP Non-Design/FL (hours)			Diagnostic			156.56	2				Diagnostic
B.2.9.14	P-2	Other Design/FL (hours)			Diagnostic								Diagnostic
B.2.9.15	P-2	Other Non-Design/FL (hours)			Diagnostic			154.48	2				Diagnostic
B.2.9.16	P-2	INP (Standalone)/FL (hours)			Diagnostic								Diagnostic
B.2.9.17	P-2	LNP (Standalone)/FL (hours)			Diagnostic								Diagnostic
B.2.9.18	P-2	Digital Loop < DS1/FL (hours)			Diagnostic			246.40	84				Diagnostic
B.2.9.19	P-2	Digital Loop >= DS1/FL (hours)			Diagnostic			221.03	94				Diagnostic
		% Jeopardy Notice >= 48 hours - Mechanized											
B.2.10.1	P-2	Switch Ports/FL (%)											
B.2.10.2	P-2	Local Interoffice Transport/FL (%)											
B.2.10.3	P-2	Loop + Port Combinations/FL (%)			NO			70.00%	40				NO
B.2.10.4	P-2	Combo Other/FL (%)			YES			100.00%	4				YES
B.2.10.5	P-2	xDSL (ADSL, HDSL and UCL)/FL (%)			NO								
B.2.10.6	P-2	UNE ISDN/FL (%)			YES			96.61%	59				YES
B.2.10.7	P-2	Line Sharing/FL (%)											
B.2.10.8	P-2	2W Analog Loop Design/FL (%)			YES			93.62%	47				NO
B.2.10.9	P-2	2W Analog Loop Non-Design/FL (%)			YES			96.20%	79				YES
B.2.10.10	P-2	2W Analog Loop w/INP Design/FL (%)											
B.2.10.11	P-2	2W Analog Loop w/INP Non-Design/FL (%)											
B.2.10.12	P-2	2W Analog Loop w/LNP Design/FL (%)			YES			100.00%	47				YES
B.2.10.13	P-2	2W Analog Loop w/LNP Non-Design/FL (%)			YES			96.30%	54				YES
B.2.10.14	P-2	Other Design/FL (%)											
B.2.10.15	P-2	Other Non-Design/FL (%)											
B.2.10.16	P-2	INP (Standalone)/FL (%)											
B.2.10.17	P-2	LNP (Standalone)/FL (%)											
B.2.10.18	P-2	Digital Loop < DS1/FL (%)			YES			96.61%	59				YES
B.2.10.19	P-2	Digital Loop >= DS1/FL (%)			YES			100.00%	197				YES
		% Jeopardy Notice >= 48 hours - Non-Mechanized											
B.2.11.1	P-2	Switch Ports/FL (%)			Diagnostic								Diagnostic
B.2.11.2	P-2	Local Interoffice Transport/FL (%)			Diagnostic								Diagnostic
B.2.11.3	P-2	Loop + Port Combinations/FL (%)			Diagnostic			100.00%	11				Diagnostic
B.2.11.4	P-2	Combo Other/FL (%)			Diagnostic			100.00%	97				Diagnostic
B.2.11.5	P-2	xDSL (ADSL, HDSL and UCL)/FL (%)			Diagnostic			82.35%	17				Diagnostic
B.2.11.6	P-2	UNE ISDN/FL (%)			Diagnostic			98.55%	69				Diagnostic
B.2.11.7	P-2	Line Sharing/FL (%)			Diagnostic								Diagnostic
B.2.11.8	P-2	2W Analog Loop Design/FL (%)			Diagnostic			100.00%	5				Diagnostic
B.2.11.9	P-2	2W Analog Loop Non-Design/FL (%)			Diagnostic			83.33%	6				Diagnostic
B.2.11.10	P-2	2W Analog Loop w/INP Design/FL (%)			Diagnostic								Diagnostic
B.2.11.11	P-2	2W Analog Loop w/INP Non-Design/FL (%)			Diagnostic								Diagnostic
B.2.11.12	P-2	2W Analog Loop w/LNP Design/FL (%)			Diagnostic			100.00%	2				Diagnostic
B.2.11.13	P-2	2W Analog Loop w/LNP Non-Design/FL (%)			Diagnostic			100.00%	2				Diagnostic
B.2.11.14	P-2	Other Design/FL (%)			Diagnostic								Diagnostic
B.2.11.15	P-2	Other Non-Design/FL (%)			Diagnostic			100.00%	2				Diagnostic
B.2.11.16	P-2	INP (Standalone)/FL (%)			Diagnostic								Diagnostic
B.2.11.17	P-2	LNP (Standalone)/FL (%)			Diagnostic								Diagnostic
B.2.11.18	P-2	Digital Loop < DS1/FL (%)			Diagnostic			96.43%	84				Diagnostic
B.2.11.19	P-2	Digital Loop >= DS1/FL (%)			Diagnostic			100.00%	94				Diagnostic
		Coordinated Customers Conversions											
B.2.12.1	P-7	Loops with INP/FL (%)											
B.2.12.2	P-7	Loops with LNP/FL (%)			YES			99.84%	6,396				YES

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - May 2002

			Benchmark/ Analog	August BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
		% Hot Cuts > 15 minutes Early									
B.2.13.1	P-7A	Time-Specific SL1/FL (%)	<= 5%			0.00%	412				YES
B.2.13.2	P-7A	Time-Specific SL2/FL (%)	<= 5%			6.25%	16				NO
B.2.13.3	P-7A	Non-Time Specific SL1/FL (%)	<= 5%			0.00%	31				YES
B.2.13.4	P-7A	Non-Time Specific SL2/FL (%)	<= 5%			0.00%	110				YES
		Hot Cut Timeliness									
B.2.14.1	P-7A	Time-Specific SL1/FL (%)	>= 95% w in 15 min			100.00%	412				YES
B.2.14.2	P-7A	Time-Specific SL2/FL (%)	>= 95% w in 15 min			93.75%	16				NO
B.2.14.3	P-7A	Non-Time Specific SL1/FL (%)	>= 95% w in 15 min			100.00%	31				YES
B.2.14.4	P-7A	Non-Time Specific SL2/FL (%)	>= 95% w in 15 min			100.00%	110				YES
		% Hot Cuts > 15 minutes Late									
B.2.15.1	P-7A	Time-Specific SL1/FL (%)	<= 5%			0.00%	412				YES
B.2.15.2	P-7A	Time-Specific SL2/FL (%)	<= 5%			0.00%	16				YES
B.2.15.3	P-7A	Non-Time Specific SL1/FL (%)	<= 5%			0.00%	31				YES
B.2.15.4	P-7A	Non-Time Specific SL2/FL (%)	<= 5%			0.00%	110				YES
		Average Recovery Time - CCC									
B.2.16.1	P-7B	Loops with INP/FL (minutes)	Diagnostic								Diagnostic
B.2.16.2	P-7B	Loops with LNP/FL (minutes)	Diagnostic			284.27	11				Diagnostic
		% Provisioning Troubles within 7 Days - Hot Cuts									
B.2.17.1.1	P-7C	UNE Loop Design/Dispatch/FL (%)	<= 5%			4.09%	660				YES
B.2.17.1.2	P-7C	UNE Loop Design/Non-Dispatch/FL (%)	<= 5%								
B.2.17.2.1	P-7C	UNE Loop Non-Design/Dispatch/FL (%)	<= 5%			1.10%	1,361				YES
B.2.17.2.2	P-7C	UNE Loop Non-Design/Non-Dispatch/FL (%)	<= 5%			0.40%	753				YES
		% Missed Installation Appointments									
B.2.18.1.1.1	P-3	Switch Ports/<10 circuits/Dispatch/FL (%)	R&B (POTS)	5.53%	69,594						
B.2.18.1.1.2	P-3	Switch Ports/<10 circuits/Non-Dispatch/FL (%)	R&B (POTS)	0.02%	759,312						
B.2.18.1.2.1	P-3	Switch Ports/>=10 circuits/Dispatch/FL (%)	R&B (POTS)	3.96%	379						
B.2.18.1.2.2	P-3	Switch Ports/>=10 circuits/Non-Dispatch/FL (%)	R&B (POTS)	0.00%	23						
B.2.18.2.1.1	P-3	Local Interoffice Transport/<10 circuits/Dispatch/FL (%)	DS1/DS3	1.51%	2,385	0.00%	5		0.05459	0.2765	YES
B.2.18.2.1.2	P-3	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL (%)	DS1/DS3								
B.2.18.2.2.1	P-3	Local Interoffice Transport/>=10 circuits/Dispatch/FL (%)	DS1/DS3								
B.2.18.2.2.2	P-3	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL (%)	DS1/DS3								
B.2.18.3.1.1	P-3	Loop + Port Combinations/<10 circuits/Dispatch/FL (%)	R&B	5.40%	71,710	5.06%	2,076		0.00503	0.6818	YES
B.2.18.3.1.2	P-3	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL (%)	R&B	0.02%	762,023	0.19%	67,451		0.00006	-28.4557	NO
B.2.18.3.1.3	P-3	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL (%)	R&B	0.00%	401,246	0.00%	23,629		0.00000		YES
B.2.18.3.1.4	P-3	Loop + Port Combinations/<10 circuits/Dispatch In/FL (%)	R&B	0.04%	360,777	0.29%	43,822		0.00011	-22.8122	NO
B.2.18.3.2.1	P-3	Loop + Port Combinations/>=10 circuits/Dispatch/FL (%)	R&B	3.35%	448	7.14%	14		0.04882	-0.7772	YES
B.2.18.3.2.2	P-3	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL (%)	R&B	0.00%	292	0.00%	5		0.00000		YES
B.2.18.3.2.3	P-3	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL (%)	R&B	0.00%	120						
B.2.18.3.2.4	P-3	Loop + Port Combinations/>=10 circuits/Dispatch In/FL (%)	R&B	0.00%	172	0.00%	5		0.00000		YES
B.2.18.4.1.1	P-3	Combo Other/<10 circuits/Dispatch/FL (%)	R&B&D - Disp	5.30%	74,548	2.20%	91		0.02350	1.3201	YES
B.2.18.4.1.4	P-3	Combo Other/<10 circuits/Dispatch In/FL (%)	R&B&D - Disp	5.30%	74,548						
B.2.18.4.2.1	P-3	Combo Other/>=10 circuits/Dispatch/FL (%)	R&B&D - Disp	2.11%	757						
B.2.18.4.2.4	P-3	Combo Other/>=10 circuits/Dispatch In/FL (%)	R&B&D - Disp	2.11%	757						
B.2.18.5.1.1	P-3	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL (%)	ADSL to Retail	3.10%	10,345	2.58%	194		0.01257	0.4183	YES
B.2.18.5.1.2	P-3	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL (%)	ADSL to Retail	0.05%	6,331						
B.2.18.5.2.1	P-3	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL (%)	ADSL to Retail	0.00%	19						
B.2.18.5.2.2	P-3	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL (%)	ADSL to Retail								
B.2.18.6.1.1	P-3	UNE ISDN/<10 circuits/Dispatch/FL (%)	ISDN - BRI	3.47%	490	6.50%	246		0.01430	-2.1222	NO
B.2.18.6.1.2	P-3	UNE ISDN/<10 circuits/Non-Dispatch/FL (%)	ISDN - BRI	0.85%	469						
B.2.18.6.2.1	P-3	UNE ISDN/>=10 circuits/Dispatch/FL (%)	ISDN - BRI								
B.2.18.6.2.2	P-3	UNE ISDN/>=10 circuits/Non-Dispatch/FL (%)	ISDN - BRI								
B.2.18.7.1.1	P-3	Line Sharing/<10 circuits/Dispatch/FL (%)	ADSL to Retail	3.10%	10,345	0.00%	25		0.03472	0.8937	YES
B.2.18.7.1.2	P-3	Line Sharing/<10 circuits/Non-Dispatch/FL (%)	ADSL to Retail	0.05%	6,331	0.00%	138		0.00187	0.2530	YES
B.2.18.7.2.1	P-3	Line Sharing/>=10 circuits/Dispatch/FL (%)	ADSL to Retail	0.00%	19						
B.2.18.7.2.2	P-3	Line Sharing/>=10 circuits/Non-Dispatch/FL (%)	ADSL to Retail								
B.2.18.8.1.1	P-3	2W Analog Loop Design/<10 circuits/Dispatch/FL (%)	R&B - Disp	5.40%	71,710	2.31%	303		0.01301	2.3751	YES
B.2.18.8.1.2	P-3	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL (%)	R&B - Disp	5.40%	71,710						
B.2.18.8.2.1	P-3	2W Analog Loop Design/>=10 circuits/Dispatch/FL (%)	R&B - Disp	3.35%	448	0.00%	4		0.09035	0.3706	YES
B.2.18.8.2.2	P-3	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL (%)	R&B - Disp	3.35%	448						
B.2.18.9.1.1	P-3	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL (%)	R&B (POTS) excl SB Or	5.53%	69,594	1.46%	893		0.00770	5.2898	YES
B.2.18.9.1.4	P-3	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL (%)	R&B (POTS) excl SB Or	0.04%	358,945	0.00%	9		0.00695	0.0626	YES
B.2.18.9.2.1	P-3	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL (%)	R&B (POTS) excl SB Or	3.96%	379	5.00%	20		0.04473	-0.2330	YES
B.2.18.9.2.4	P-3	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL (%)	R&B (POTS) excl SB Or	0.00%	8	0.00%	1		0.00000		YES
B.2.18.10.1.1	P-3	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL (%)	R&B - Disp	5.40%	71,710	0.00%	2		0.15963	0.3379	YES

BellSouth Monthly Performance Summary

Florida Interim, August 2002 - M ay 2002

			July								June				
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation
		% Hot Cuts > 15 minutes Early													
B.2.13.1	P-7A	*Time-Specific SL1/FL (%)*			0.20%	491				YES			0.49%	615	
B.2.13.2	P-7A	Time-Specific SL2/FL (%)			0.00%	21				YES			0.00%	21	
B.2.13.3	P-7A	Non-Time Specific SL1/FL (%)			0.00%	32				YES			0.00%	38	
B.2.13.4	P-7A	Non-Time Specific SL2/FL (%)			0.00%	135				YES			0.00%	206	
		Hot Cut Timeliness													
B.2.14.1	P-7A	Time-Specific SL1/FL (%)			99.80%	491				YES			97.07%	615	
B.2.14.2	P-7A	Time-Specific SL2/FL (%)			100.00%	21				YES			95.24%	21	
B.2.14.3	P-7A	Non-Time Specific SL1/FL (%)			100.00%	32				YES			100.00%	38	
B.2.14.4	P-7A	Non-Time Specific SL2/FL (%)			100.00%	135				YES			100.00%	206	
		% Hot Cuts > 15 minutes Late													
B.2.15.1	P-7A	Time-Specific SL1/FL (%)			0.00%	491				YES			2.44%	615	
B.2.15.2	P-7A	Time-Specific SL2/FL (%)			0.00%	21				YES			4.76%	21	
B.2.15.3	P-7A	Non-Time Specific SL1/FL (%)			0.00%	32				YES			0.00%	38	
B.2.15.4	P-7A	Non-Time Specific SL2/FL (%)			0.00%	135				YES			0.00%	206	
		Average Recovery Time - CCC													
B.2.16.1	P-7B	Loops with INP/FL (minutes)								Diagnostic					
B.2.16.2	P-7B	Loops with LNP/FL (minutes)			661.27	26				Diagnostic			553.91	18	
		% Provisioning Troubles within 7 Days - Hot Cuts													
B.2.17.1.1	P-7C	UNE Loop Design/Dispatch/FL (%)			2.10%	1,000				YES			2.70%	1,518	
B.2.17.1.2	P-7C	UNE Loop Design/Non-Dispatch/FL (%)													
B.2.17.2.1	P-7C	UNE Loop Non-Design/Dispatch/FL (%)			1.18%	1,445				YES			0.76%	3,795	
B.2.17.2.2	P-7C	UNE Loop Non-Design/Non-Dispatch/FL (%)			0.25%	810				YES			0.27%	1,127	
		% Missed Installation Appointments													
B.2.18.1.1.1	P-3	Switch Ports/<10 circuits/Dispatch/FL (%)	6.37%	61,289							5.38%	56,131			
B.2.18.1.1.2	P-3	Switch Ports/<10 circuits/Non-Dispatch/FL (%)	0.02%	720,032							0.03%	682,774			
B.2.18.1.2.1	P-3	Switch Ports/>=10 circuits/Dispatch/FL (%)	6.10%	377							4.63%	367			
B.2.18.1.2.2	P-3	Switch Ports/>=10 circuits/Non-Dispatch/FL (%)	0.00%	9							0.00%	21			
B.2.18.2.1.1	P-3	Local Interoffice Transport/<10 circuits/Dispatch/FL (%)	1.15%	2,080	0.00%	6		0.04366	0.2643	YES	0.95%	2,096	0.00%	14	
B.2.18.2.1.2	P-3	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL (%)													
B.2.18.2.2.1	P-3	Local Interoffice Transport/>=10 circuits/Dispatch/FL (%)											0.00%	1	
B.2.18.2.2.2	P-3	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL (%)													
B.2.18.3.1.1	P-3	Loop + Port Combinations/<10 circuits/Dispatch/FL (%)	6.35%	62,022	5.39%	1,817		0.00580	1.6444	YES	5.40%	56,778	4.08%	1,567	
B.2.18.3.1.2	P-3	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL (%)	0.02%	722,384	0.09%	65,002		0.00006	-10.6193	NO	0.03%	684,871	0.13%	49,428	
B.2.18.3.1.3	P-3	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL (%)	0.00%	391,101	0.00%	23,910		0.00000		YES	0.00%	350,336	0.00%	19,770	
B.2.18.3.1.4	P-3	Loop + Port Combinations/<10 circuits/Dispatch In/FL (%)	0.05%	331,283	0.14%	41,092		0.00012	-7.7290	NO	0.06%	334,535	0.22%	29,658	
B.2.18.3.2.1	P-3	Loop + Port Combinations/>=10 circuits/Dispatch/FL (%)	6.11%	442	14.29%	14		0.06501	-1.2578	YES	8.64%	440	0.00%	14	
B.2.18.3.2.2	P-3	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL (%)	0.00%	267	0.00%	4		0.00000		YES	0.00%	172	0.00%	3	
B.2.18.3.2.3	P-3	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL (%)	0.00%	56	0.00%	1		0.00000		YES	0.00%	35			
B.2.18.3.2.4	P-3	Loop + Port Combinations/>=10 circuits/Dispatch In/FL (%)	0.00%	211	0.00%	3		0.00000		YES	0.00%	137	0.00%	3	
B.2.18.4.1.1	P-3	Combo Other/<10 circuits/Dispatch/FL (%)	6.19%	65,194	1.06%	94		0.02487	2.0608	YES	5.26%	59,646	3.70%	108	
B.2.18.4.1.4	P-3	Combo Other/<10 circuits/Dispatch In/FL (%)	6.19%	65,194							5.26%	59,646			
B.2.18.4.2.1	P-3	Combo Other/>=10 circuits/Dispatch/FL (%)	6.04%	447							7.17%	530			
B.2.18.4.2.4	P-3	Combo Other/>=10 circuits/Dispatch In/FL (%)	6.04%	447							7.17%	530			
B.2.18.5.1.1	P-3	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL (%)	5.51%	9,549	2.66%	263		0.01426	1.9963	YES	4.67%	9,617	2.21%	272	
B.2.18.5.1.2	P-3	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/FL (%)	0.02%	5,904							0.00%	5,793			
B.2.18.5.2.1	P-3	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/FL (%)	0.00%	7							0.00%	14	0.00%	1	
B.2.18.5.2.2	P-3	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/FL (%)													
B.2.18.6.1.1	P-3	UNE ISDN/<10 circuits/Dispatch/FL (%)	5.48%	347	2.32%	345		0.01730	1.8250	YES	7.00%	257	2.11%	284	
B.2.18.6.1.2	P-3	UNE ISDN/<10 circuits/Non-Dispatch/FL (%)	1.15%	348							3.16%	316			
B.2.18.6.2.1	P-3	UNE ISDN/>=10 circuits/Dispatch/FL (%)			0.00%	1									
B.2.18.6.2.2	P-3	UNE ISDN/>=10 circuits/Non-Dispatch/FL (%)													
B.2.18.7.1.1	P-3	Line Sharing/<10 circuits/Dispatch/FL (%)	5.51%	9,549	8.57%	35		0.03863	-0.7928	YES	4.67%	9,617	5.41%	37	
B.2.18.7.1.2	P-3	Line Sharing/<10 circuits/Non-Dispatch/FL (%)	0.02%	5,904	0.00%	112		0.00124	0.1365	YES	0.00%	5,793	0.00%	129	
B.2.18.7.2.1	P-3	Line Sharing/>=10 circuits/Dispatch/FL (%)	0.00%	7							0.00%	14			
B.2.18.7.2.2	P-3	Line Sharing/>=10 circuits/Non-Dispatch/FL (%)													
B.2.18.8.1.1	P-3	2W Analog Loop Design/<10 circuits/Dispatch/FL (%)	6.35%	62,022	2.12%	377		0.01260	3.3550	YES	5.40%	56,778	1.38%	362	
B.2.18.8.1.2	P-3	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL (%)	6.35%	62,022							5.40%	56,778			
B.2.18.8.2.1	P-3	2W Analog Loop Design/>=10 circuits/Dispatch/FL (%)	6.11%	442	0.00%	3		0.13874	0.4403	YES	8.64%	440	0.00%	5	
B.2.18.8.2.2	P-3	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL (%)	6.11%	442							8.64%	440			
B.2.18.9.1.1	P-3	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL (%)	6.37%	61,289	1.90%	1,002		0.00778	5.7503	YES	5.38%	56,131	2.30%	870	
B.2.18.9.1.4	P-3	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL (%)	0.05%	329,680	0.00%	15		0.00576	0.0864	YES	0.06%	333,057	0.00%	31	
B.2.18.9.2.1	P-3	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL (%)	6.10%	377	3.45%	29		0.04612	0.5751	YES	4.63%	367	5.00%	20	
B.2.18.9.2.4	P-3	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL (%)	0.00%	3	0.00%	1		0.00000		YES	0.00%	18			
B.2.18.10.1.1	P-3	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL (%)	6.35%	62,022	0.00%	1		0.24382	0.2603	YES	5.40%	56,778			