BellSouth Monthly Performance Summary
Tennessee, August 2002 - M ay 2002

			Benchmark/ Analog	August BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
	Unbundled Network Elements - Ordering										
	% Rejected Service Requests - Mechanized										
B.1.1.1	O-7	Switch Ports/TN (%)	Diagnostic								Diagnostic
B.1.1.2	O-7	Local Interoffice Transport/TN (%)	Diagnostic								Diagnostic
B.1.1.3	O-7	Loop + Port Combinations/TN (%)	Diagnostic			14.62%	23,570				Diagnostic
B.1.1.4	O-7	Combo Other/TN (%)	Diagnostic								Diagnostic
B.1.1.5	O-7	xDSL (ADSL, HDSL and UCL)/TN (%)	Diagnostic			23.08%	169				Diagnostic
B.1.1.6	O-7	ISDN Loop (UDN, UDC)/TN (%)	Diagnostic			15.09%	53				Diagnostic
B.1.1.7	O-7	Line Sharing/TN (%)	Diagnostic			17.65%	68				Diagnostic
B.1.1.8	O-7	2W Analog Loop Design/TN (%)	Diagnostic			22.15%	456				Diagnostic
B.1.1.9	O-7	2W Analog Loop Non-Design/TN (%)	Diagnostic			13.21%	53				Diagnostic
B.1.1.10	O-7	2W Analog Loop w/INP Design/TN (%)	Diagnostic								Diagnostic
B.1.1.11	O-7	2W Analog Loop w/INP Non-Design/TN (%)	Diagnostic								Diagnostic
B.1.1.12	O-13	2W Analog Loop w/LNP Design/TN (%)	Diagnostic			45.45%	11				Diagnostic
B.1.1.13	O-13	2W Analog Loop w/LNP Non-Design/TN (%)	Diagnostic								Diagnostic
B.1.1.14	O-7	Other Design/TN (%)	Diagnostic			13.64%	198				Diagnostic
B.1.1.15	O-7	Other Non-Design/TN (%)	Diagnostic			13.07%	482				Diagnostic
B.1.1.16	O-7	INP Standalone/TN (%)	Diagnostic								Diagnostic
B.1.1.17	O-13	LNP Standalone/TN (%)	Diagnostic			8.43%	166				Diagnostic
	% Rejected Service Requests - Partially Mechanized										
B.1.2.1	O-7	Switch Ports/TN (%)	Diagnostic								Diagnostic
B.1.2.2	O-7	Local Interoffice Transport/TN (%)	Diagnostic								Diagnostic
B.1.2.3	O-7	Loop + Port Combinations/TN (%)	Diagnostic			32.30%	3,954				Diagnostic
B.1.2.4	O-7	Combo Other/TN (%)	Diagnostic								Diagnostic
B.1.2.5	O-7	xDSL (ADSL, HDSL and UCL)/TN (%)	Diagnostic			13.33%	15				Diagnostic
B.1.2.6	O-7	ISDN Loop (UDN, UDC)/TN (%)	Diagnostic			10.00%	10				Diagnostic
B.1.2.7	O-7	Line Sharing/TN (%)	Diagnostic			45.00%	20				Diagnostic
B.1.2.8	O-7	2W Analog Loop Design/TN (%)	Diagnostic			25.58%	86				Diagnostic
B.1.2.9	O-7	2W Analog Loop Non-Design/TN (%)	Diagnostic			29.63%	27				Diagnostic
B.1.2.10	O-7	2W Analog Loop w/INP Design/TN (%)	Diagnostic								Diagnostic
B.1.2.11	O-7	2W Analog Loop w/INP Non-Design/TN (%)	Diagnostic								Diagnostic
B.1.2.12	O-13	2W Analog Loop w/LNP Design/TN (%)	Diagnostic			13.49%	126				Diagnostic
B.1.2.13	O-13	2W Analog Loop w/LNP Non-Design/TN (%)	Diagnostic								Diagnostic
B.1.2.14	O-7	Other Design/TN (%)	Diagnostic			19.32%	88				Diagnostic
B.1.2.15	O-7	Other Non-Design/TN (%)	Diagnostic			35.16%	128				Diagnostic
B.1.2.16	O-7	INP Standalone/TN (%)	Diagnostic								Diagnostic
B.1.2.17	O-13	LNP Standalone/TN (%)	Diagnostic			21.88%	192				Diagnostic
	% Rejected Service Requests - Non-Mechanized										
B.1.3.1	O-7	Switch Ports/TN (%)	Diagnostic								Diagnostic
B.1.3.2	O-7	Local Interoffice Transport/TN (%)	Diagnostic			100.00%	3				Diagnostic
B.1.3.3	O-7	Loop + Port Combinations/TN (%)	Diagnostic			55.99%	459				Diagnostic
B.1.3.4	O-7	Combo Other/TN (%)	Diagnostic			28.85%	52				Diagnostic
B.1.3.5	O-7	xDSL (ADSL, HDSL and UCL)/TN (%)	Diagnostic			12.50%	8				Diagnostic
B.1.3.6	O-7	ISDN Loop (UDN, UDC)/TN (%)	Diagnostic			0.00%	5				Diagnostic
B.1.3.7	O-7	Line Sharing/TN (%)	Diagnostic			100.00%	2				Diagnostic
B.1.3.8	O-7	2W Analog Loop Design/TN (%)	Diagnostic			45.83%	24				Diagnostic
B.1.3.9	O-7	2W Analog Loop Non-Design/TN (%)	Diagnostic			48.98%	49				Diagnostic
B.1.3.10	O-7	2W Analog Loop w/INP Design/TN (%)	Diagnostic			50.00%	2				Diagnostic
B.1.3.11	O-7	2W Analog Loop w/INP Non-Design/TN (%)	Diagnostic			33.33%	3				Diagnostic
B.1.3.12	O-13	2W Analog Loop w/LNP Design/TN (%)	Diagnostic			40.91%	22				Diagnostic
B.1.3.13	O-13	2W Analog Loop w/LNP Non-Design/TN (%)	Diagnostic			66.67%	6				Diagnostic
B.1.3.14	O-7	Other Design/TN (%)	Diagnostic			36.96%	92				Diagnostic
B.1.3.15	O-7	Other Non-Design/TN (%)	Diagnostic			32.81%	707				Diagnostic
B.1.3.16	O-7	INP Standalone/TN (%)	Diagnostic			16.22%	37				Diagnostic
B.1.3.17	O-13	LNP Standalone/TN (%)	Diagnostic			22.69%	432				Diagnostic
	Reject Interval - Mechanized										
B.1.4.1	O-8	Switch Ports/TN (%)	>= 97% w in 1 hr								
B.1.4.2	O-8	Local Interoffice Transport/TN (%)	>= 97% w in 1 hr			95.90%	3,463				NO
B.1.4.3	O-8	Loop + Port Combinations/TN (%)	>= 97% w in 1 hr								
B.1.4.4	O-8	Combo Other/TN (%)	>= 97% w in 1 hr			97.44%	39				YES
B.1.4.5	O-8	xDSL (ADSL, HDSL and UCL)/TN (%)	>= 97% w in 1 hr			87.50%	8				NO
B.1.4.6	O-8	ISDN Loop (UDN, UDC)/TN (%)	>= 97% w in 1 hr			58.33%	12				NO
B.1.4.7	O-8	Line Sharing/TN (%)	>= 97% w in 1 hr								

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			July								Jul		
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume
	Unbundled Network Elements - Ordering													
	% Rejected Service Requests - Mechanized													
B.1.1.1	O-7	Switch Ports/TN (%)								Diagnostic				
B.1.1.2	O-7	Local Interoffice Transport/TN (%)								Diagnostic				
B.1.1.3	O-7	Loop + Port Combinations/TN (%)			14.48%	12,789				Diagnostic			17.45%	6,149
B.1.1.4	O-7	Combo Other/TN (%)								Diagnostic			3.23%	31
B.1.1.5	O-7	xDSL (ADSL, HDSL and UCL)/TN (%)			21.43%	154				Diagnostic			14.53%	296
B.1.1.6	O-7	ISDN Loop (UDN, UDC)/TN (%)			8.70%	92				Diagnostic			10.17%	59
B.1.1.7	O-7	Line Sharing/TN (%)			27.66%	94				Diagnostic			22.37%	76
B.1.1.8	O-7	2W Analog Loop Design/TN (%)			13.00%	400				Diagnostic			15.64%	569
B.1.1.9	O-7	2W Analog Loop Non-Design/TN (%)			6.12%	49				Diagnostic			10.42%	48
B.1.1.10	O-7	2W Analog Loop w/INP Design/TN (%)								Diagnostic				
B.1.1.11	O-7	2W Analog Loop w/INP Non-Design/TN (%)								Diagnostic				
B.1.1.12	O-13	2W Analog Loop w/LNP Design/TN (%)			36.36%	11				Diagnostic			64.71%	17
B.1.1.13	O-13	2W Analog Loop w/LNP Non-Design/TN (%)								Diagnostic				
B.1.1.14	O-7	Other Design/TN (%)			10.18%	167				Diagnostic			8.49%	212
B.1.1.15	O-7	Other Non-Design/TN (%)			22.26%	337				Diagnostic			22.67%	300
B.1.1.16	O-7	INP Standalone/TN (%)								Diagnostic			100.00%	2
B.1.1.17	O-13	LNP Standalone/TN (%)			14.14%	198				Diagnostic			7.87%	127
	% Rejected Service Requests - Partially Mechanized													
B.1.2.1	O-7	Switch Ports/TN (%)								Diagnostic				
B.1.2.2	O-7	Local Interoffice Transport/TN (%)								Diagnostic				
B.1.2.3	O-7	Loop + Port Combinations/TN (%)			32.73%	3,025				Diagnostic			36.28%	2,230
B.1.2.4	O-7	Combo Other/TN (%)			0.00%	2				Diagnostic			33.33%	3
B.1.2.5	O-7	xDSL (ADSL, HDSL and UCL)/TN (%)			8.00%	25				Diagnostic			28.57%	7
B.1.2.6	O-7	ISDN Loop (UDN, UDC)/TN (%)			0.00%	9				Diagnostic			0.00%	9
B.1.2.7	O-7	Line Sharing/TN (%)			34.48%	29				Diagnostic			36.84%	19
B.1.2.8	O-7	2W Analog Loop Design/TN (%)			17.65%	51				Diagnostic			17.33%	75
B.1.2.9	O-7	2W Analog Loop Non-Design/TN (%)			26.32%	19				Diagnostic			33.33%	12
B.1.2.10	O-7	2W Analog Loop w/INP Design/TN (%)								Diagnostic				
B.1.2.11	O-7	2W Analog Loop w/INP Non-Design/TN (%)								Diagnostic				
B.1.2.12	O-13	2W Analog Loop w/LNP Design/TN (%)			13.43%	134				Diagnostic			20.83%	120
B.1.2.13	O-13	2W Analog Loop w/LNP Non-Design/TN (%)								Diagnostic				
B.1.2.14	O-7	Other Design/TN (%)			22.89%	83				Diagnostic			13.51%	74
B.1.2.15	O-7	Other Non-Design/TN (%)			28.00%	125				Diagnostic			35.14%	111
B.1.2.16	O-7	INP Standalone/TN (%)								Diagnostic				
B.1.2.17	O-13	LNP Standalone/TN (%)			17.33%	150				Diagnostic			11.18%	170
	% Rejected Service Requests - Non-Mechanized													
B.1.3.1	O-7	Switch Ports/TN (%)								Diagnostic			0.00%	1
B.1.3.2	O-7	Local Interoffice Transport/TN (%)								Diagnostic				
B.1.3.3	O-7	Loop + Port Combinations/TN (%)			50.30%	330				Diagnostic			47.57%	267
B.1.3.4	O-7	Combo Other/TN (%)			28.07%	57				Diagnostic			42.50%	40
B.1.3.5	O-7	xDSL (ADSL, HDSL and UCL)/TN (%)			33.33%	24				Diagnostic			14.29%	28
B.1.3.6	O-7	ISDN Loop (UDN, UDC)/TN (%)			29.41%	17				Diagnostic			21.74%	23
B.1.3.7	O-7	Line Sharing/TN (%)			60.00%	5				Diagnostic			28.57%	7
B.1.3.8	O-7	2W Analog Loop Design/TN (%)			43.64%	55				Diagnostic			31.91%	47
B.1.3.9	O-7	2W Analog Loop Non-Design/TN (%)			56.52%	46				Diagnostic			64.10%	39
B.1.3.10	O-7	2W Analog Loop w/INP Design/TN (%)								Diagnostic				
B.1.3.11	O-7	2W Analog Loop w/INP Non-Design/TN (%)								Diagnostic			100.00%	1
B.1.3.12	O-13	2W Analog Loop w/LNP Design/TN (%)			43.75%	16				Diagnostic			36.36%	33
B.1.3.13	O-13	2W Analog Loop w/LNP Non-Design/TN (%)			71.43%	7				Diagnostic				
B.1.3.14	O-7	Other Design/TN (%)			28.57%	70				Diagnostic			38.06%	134
B.1.3.15	O-7	Other Non-Design/TN (%)			34.89%	662				Diagnostic			25.04%	615
B.1.3.16	O-7	INP Standalone/TN (%)			16.67%	18				Diagnostic			33.33%	21
B.1.3.17	O-13	LNP Standalone/TN (%)			22.33%	403				Diagnostic			20.60%	403
	Reject Interval - Mechanized													
B.1.4.1	O-8	Switch Ports/TN (%)												
B.1.4.2	O-8	Local Interoffice Transport/TN (%)			93.54%	1,873				NO			93.78%	1,077
B.1.4.3	O-8	Loop + Port Combinations/TN (%)											0.00%	1
B.1.4.4	O-8	Combo Other/TN (%)			93.94%	33				NO			97.67%	43
B.1.4.5	O-8	xDSL (ADSL, HDSL and UCL)/TN (%)			100.00%	8				YES			85.71%	7
B.1.4.6	O-8	ISDN Loop (UDN, UDC)/TN (%)			84.62%	26				NO			58.82%	17
B.1.4.7	O-8	Line Sharing/TN (%)												

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			ne				May							
			Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
	Unbundled Network Elements - Ordering													
	% Rejected Service Requests - Mechanized													
B.1.1.1	O-7	Switch Ports/TN (%)				Diagnostic								Diagnostic
B.1.1.2	O-7	Local Interoffice Transport/TN (%)				Diagnostic								Diagnostic
B.1.1.3	O-7	Loop + Port Combinations/TN (%)				Diagnostic			29.84%	7,872				Diagnostic
B.1.1.4	O-7	Combo Other/TN (%)				Diagnostic								Diagnostic
B.1.1.5	O-7	xDSL (ADSL, HDSL and UCL)/TN (%)				Diagnostic			26.18%	191				Diagnostic
B.1.1.6	O-7	ISDN Loop (UDN, UDC)/TN (%)				Diagnostic			9.76%	41				Diagnostic
B.1.1.7	O-7	Line Sharing/TN (%)				Diagnostic			17.07%	82				Diagnostic
B.1.1.8	O-7	2W Analog Loop Design/TN (%)				Diagnostic			18.64%	676				Diagnostic
B.1.1.9	O-7	2W Analog Loop Non-Design/TN (%)				Diagnostic			2.27%	44				Diagnostic
B.1.1.10	O-7	2W Analog Loop w/INP Design/TN (%)				Diagnostic								Diagnostic
B.1.1.11	O-7	2W Analog Loop w/INP Non-Design/TN (%)				Diagnostic								Diagnostic
B.1.1.12	O-13	2W Analog Loop w/LNP Design/TN (%)				Diagnostic			35.00%	20				Diagnostic
B.1.1.13	O-13	2W Analog Loop w/LNP Non-Design/TN (%)				Diagnostic								Diagnostic
B.1.1.14	O-7	Other Design/TN (%)				Diagnostic			12.00%	175				Diagnostic
B.1.1.15	O-7	Other Non-Design/TN (%)				Diagnostic			26.91%	327				Diagnostic
B.1.1.16	O-7	INP Standalone/TN (%)				Diagnostic								Diagnostic
B.1.1.17	O-13	LNP Standalone/TN (%)				Diagnostic			12.04%	108				Diagnostic
	% Rejected Service Requests - Partially Mechanized													
B.1.2.1	O-7	Switch Ports/TN (%)				Diagnostic								Diagnostic
B.1.2.2	O-7	Local Interoffice Transport/TN (%)				Diagnostic								Diagnostic
B.1.2.3	O-7	Loop + Port Combinations/TN (%)				Diagnostic			40.87%	4,752				Diagnostic
B.1.2.4	O-7	Combo Other/TN (%)				Diagnostic								Diagnostic
B.1.2.5	O-7	xDSL (ADSL, HDSL and UCL)/TN (%)				Diagnostic			3.57%	28				Diagnostic
B.1.2.6	O-7	ISDN Loop (UDN, UDC)/TN (%)				Diagnostic			4.88%	82				Diagnostic
B.1.2.7	O-7	Line Sharing/TN (%)				Diagnostic			61.54%	13				Diagnostic
B.1.2.8	O-7	2W Analog Loop Design/TN (%)				Diagnostic			25.62%	121				Diagnostic
B.1.2.9	O-7	2W Analog Loop Non-Design/TN (%)				Diagnostic			0.00%	2				Diagnostic
B.1.2.10	O-7	2W Analog Loop w/INP Design/TN (%)				Diagnostic								Diagnostic
B.1.2.11	O-7	2W Analog Loop w/INP Non-Design/TN (%)				Diagnostic								Diagnostic
B.1.2.12	O-13	2W Analog Loop w/LNP Design/TN (%)				Diagnostic			18.06%	144				Diagnostic
B.1.2.13	O-13	2W Analog Loop w/LNP Non-Design/TN (%)				Diagnostic								Diagnostic
B.1.2.14	O-7	Other Design/TN (%)				Diagnostic			28.77%	73				Diagnostic
B.1.2.15	O-7	Other Non-Design/TN (%)				Diagnostic			36.43%	140				Diagnostic
B.1.2.16	O-7	INP Standalone/TN (%)				Diagnostic								Diagnostic
B.1.2.17	O-13	LNP Standalone/TN (%)				Diagnostic			32.52%	163				Diagnostic
	% Rejected Service Requests - Non-Mechanized													
B.1.3.1	O-7	Switch Ports/TN (%)				Diagnostic								Diagnostic
B.1.3.2	O-7	Local Interoffice Transport/TN (%)				Diagnostic			57.69%	26				Diagnostic
B.1.3.3	O-7	Loop + Port Combinations/TN (%)				Diagnostic			47.74%	243				Diagnostic
B.1.3.4	O-7	Combo Other/TN (%)				Diagnostic			57.14%	14				Diagnostic
B.1.3.5	O-7	xDSL (ADSL, HDSL and UCL)/TN (%)				Diagnostic			29.55%	44				Diagnostic
B.1.3.6	O-7	ISDN Loop (UDN, UDC)/TN (%)				Diagnostic			24.00%	25				Diagnostic
B.1.3.7	O-7	Line Sharing/TN (%)				Diagnostic			30.00%	10				Diagnostic
B.1.3.8	O-7	2W Analog Loop Design/TN (%)				Diagnostic			31.37%	51				Diagnostic
B.1.3.9	O-7	2W Analog Loop Non-Design/TN (%)				Diagnostic			47.14%	70				Diagnostic
B.1.3.10	O-7	2W Analog Loop w/INP Design/TN (%)				Diagnostic								Diagnostic
B.1.3.11	O-7	2W Analog Loop w/INP Non-Design/TN (%)				Diagnostic								Diagnostic
B.1.3.12	O-13	2W Analog Loop w/LNP Design/TN (%)				Diagnostic			28.57%	42				Diagnostic
B.1.3.13	O-13	2W Analog Loop w/LNP Non-Design/TN (%)				Diagnostic								Diagnostic
B.1.3.14	O-7	Other Design/TN (%)				Diagnostic			30.11%	176				Diagnostic
B.1.3.15	O-7	Other Non-Design/TN (%)				Diagnostic			31.96%	751				Diagnostic
B.1.3.16	O-7	INP Standalone/TN (%)				Diagnostic			26.67%	15				Diagnostic
B.1.3.17	O-13	LNP Standalone/TN (%)				Diagnostic			26.67%	345				Diagnostic
	Reject Interval - Mechanized													
B.1.4.1	O-8	Switch Ports/TN (%)												
B.1.4.2	O-8	Local Interoffice Transport/TN (%)				NO			95.75%	2,351				NO
B.1.4.3	O-8	Loop + Port Combinations/TN (%)				NO								
B.1.4.4	O-8	Combo Other/TN (%)				YES			100.00%	51				YES
B.1.4.5	O-8	xDSL (ADSL, HDSL and UCL)/TN (%)				NO			0.00%	4				NO
B.1.4.6	O-8	ISDN Loop (UDN, UDC)/TN (%)				NO			71.43%	14				NO
B.1.4.7	O-8	Line Sharing/TN (%)												

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			Benchmark/ Analog	August							
				BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.1.4.8	O-8	2W Analog Loop Design/TN (%)	>= 97% w in 1 hr			73.53%	102				NO
B.1.4.9	O-8	2W Analog Loop Non-Design/TN (%)	>= 97% w in 1 hr			71.43%	7				NO
B.1.4.10	O-8	2W Analog Loop w/INP Design/TN (%)	>= 97% w in 1 hr								
B.1.4.11	O-8	2W Analog Loop w/INP Non-Design/TN (%)	>= 97% w in 1 hr								
B.1.4.12	O-14	2W Analog Loop w/LNP Design/TN (%)	>= 97% w in 1 hr			60.00%	5				NO
B.1.4.13	O-14	2W Analog Loop w/LNP Non-Design/TN (%)	>= 97% w in 1 hr								
B.1.4.14	O-8	Other Design/TN (%)	>= 97% w in 1 hr			77.78%	27				NO
B.1.4.15	O-8	Other Non-Design/TN (%)	>= 97% w in 1 hr			68.18%	66				NO
B.1.4.16	O-8	INP Standalone/TN (%)	>= 97% w in 1 hr								
B.1.4.17	O-14	LNP Standalone/TN (%)	>= 97% w in 1 hr			100.00%	14				YES
		Reject Interval - Partially Mechanized - 10 hours	>= 85% w in 10 hrs								
B.1.7.1	O-8	Switch Ports/TN (%)	>= 85% w in 10 hrs								
B.1.7.2	O-8	Local Interoffice Transport/TN (%)	>= 85% w in 10 hrs			96.30%	1,299				YES
B.1.7.3	O-8	Loop + Port Combinations/TN (%)	>= 85% w in 10 hrs								
B.1.7.4	O-8	Combo Other/TN (%)	>= 85% w in 10 hrs			100.00%	2				YES
B.1.7.5	O-8	xDSL (ADSL, HDSL and UCL)/TN (%)	>= 85% w in 10 hrs			100.00%	1				YES
B.1.7.6	O-8	ISDN Loop (UDN, UDC)/TN (%)	>= 85% w in 10 hrs			91.67%	12				YES
B.1.7.7	O-8	Line Sharing/TN (%)	>= 85% w in 10 hrs			73.91%	23				NO
B.1.7.8	O-8	2W Analog Loop Design/TN (%)	>= 85% w in 10 hrs			87.50%	8				YES
B.1.7.9	O-8	2W Analog Loop Non-Design/TN (%)	>= 85% w in 10 hrs								
B.1.7.10	O-8	2W Analog Loop w/INP Design/TN (%)	>= 85% w in 10 hrs								
B.1.7.11	O-8	2W Analog Loop w/INP Non-Design/TN (%)	>= 85% w in 10 hrs			100.00%	18				YES
B.1.7.12	O-14	2W Analog Loop w/LNP Design/TN (%)	>= 85% w in 10 hrs								
B.1.7.13	O-14	2W Analog Loop w/LNP Non-Design/TN (%)	>= 85% w in 10 hrs			85.00%	20				YES
B.1.7.14	O-8	Other Design/TN (%)	>= 85% w in 10 hrs			97.83%	46				YES
B.1.7.15	O-8	Other Non-Design/TN (%)	>= 85% w in 10 hrs								
B.1.7.16	O-8	INP Standalone/TN (%)	>= 85% w in 10 hrs			90.91%	44				YES
B.1.7.17	O-14	LNP Standalone/TN (%)	>= 85% w in 10 hrs								
		Reject Interval - Non-Mechanized	>= 85% w in 24 hrs								
B.1.8.1	O-8	Switch Ports/TN (%)	>= 85% w in 24 hrs			100.00%	3				YES
B.1.8.2	O-8	Local Interoffice Transport/TN (%)	>= 85% w in 24 hrs			96.93%	261				YES
B.1.8.3	O-8	Loop + Port Combinations/TN (%)	>= 85% w in 24 hrs			100.00%	15				YES
B.1.8.4	O-8	Combo Other/TN (%)	>= 85% w in 24 hrs			100.00%	1				YES
B.1.8.5	O-8	xDSL (ADSL, HDSL and UCL)/TN (%)	>= 85% w in 24 hrs								
B.1.8.6	O-8	ISDN Loop (UDN, UDC)/TN (%)	>= 85% w in 24 hrs			100.00%	2				YES
B.1.8.7	O-8	Line Sharing/TN (%)	>= 85% w in 24 hrs			100.00%	11				YES
B.1.8.8	O-8	2W Analog Loop Design/TN (%)	>= 85% w in 24 hrs			100.00%	26				YES
B.1.8.9	O-8	2W Analog Loop Non-Design/TN (%)	>= 85% w in 24 hrs			100.00%	1				YES
B.1.8.10	O-8	2W Analog Loop w/INP Design/TN (%)	>= 85% w in 24 hrs			100.00%	1				YES
B.1.8.11	O-8	2W Analog Loop w/INP Non-Design/TN (%)	>= 85% w in 24 hrs			100.00%	9				NO
B.1.8.12	O-14	2W Analog Loop w/LNP Design/TN (%)	>= 85% w in 24 hrs			75.00%	4				YES
B.1.8.13	O-14	2W Analog Loop w/LNP Non-Design/TN (%)	>= 85% w in 24 hrs			100.00%	35				YES
B.1.8.14	O-8	Other Design/TN (%)	>= 85% w in 24 hrs			99.15%	234				YES
B.1.8.15	O-8	Other Non-Design/TN (%)	>= 85% w in 24 hrs			100.00%	6				YES
B.1.8.16	O-8	INP Standalone/TN (%)	>= 85% w in 24 hrs			100.00%	99				YES
B.1.8.17	O-14	LNP Standalone/TN (%)	>= 85% w in 24 hrs								
		FOC Timeliness - Mechanized	>= 95% w in 3 hrs								
B.1.9.1	O-9	Switch Ports/TN (%)	>= 95% w in 3 hrs								
B.1.9.2	O-9	Local Interoffice Transport/TN (%)	>= 95% w in 3 hrs			98.20%	20,064				YES
B.1.9.3	O-9	Loop + Port Combinations/TN (%)	>= 95% w in 3 hrs								
B.1.9.4	O-9	Combo Other/TN (%)	>= 95% w in 3 hrs			96.88%	128				YES
B.1.9.5	O-9	xDSL (ADSL, HDSL and UCL)/TN (%)	>= 95% w in 3 hrs			95.00%	40				YES
B.1.9.6	O-9	ISDN Loop (UDN, UDC)/TN (%)	>= 95% w in 3 hrs			100.00%	61				YES
B.1.9.7	O-9	Line Sharing/TN (%)	>= 95% w in 3 hrs			97.55%	367				YES
B.1.9.8	O-9	2W Analog Loop Design/TN (%)	>= 95% w in 3 hrs			100.00%	47				YES
B.1.9.9	O-9	2W Analog Loop Non-Design/TN (%)	>= 95% w in 3 hrs								
B.1.9.10	O-9	2W Analog Loop w/INP Design/TN (%)	>= 95% w in 3 hrs								
B.1.9.11	O-9	2W Analog Loop w/INP Non-Design/TN (%)	>= 95% w in 3 hrs			100.00%	4				YES
B.1.9.12	O-15	2W Analog Loop w/LNP Design/TN (%)	>= 95% w in 3 hrs								
B.1.9.13	O-15	2W Analog Loop w/LNP Non-Design/TN (%)	>= 95% w in 3 hrs			98.21%	168				YES
B.1.9.14	O-9	Other Design/TN (%)	>= 95% w in 3 hrs			87.22%	407				NO
B.1.9.15	O-9	Other Non-Design/TN (%)	>= 95% w in 3 hrs								
B.1.9.16	O-9	INP Standalone/TN (%)	>= 95% w in 3 hrs			100.00%	149				YES
B.1.9.17	O-15	LNP Standalone/TN (%)	>= 95% w in 3 hrs								
		FOC Timeliness - Partially Mechanized - 10 hours	>= 85% w in 10 hrs								
B.1.12.1	O-9	Switch Ports/TN (%)									

BellSouth Monthly Performance Summary
Tennessee, August 2002 - M ay 2002

			July								Jul			
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.1.4.8	O-8	2W Analog Loop Design/TN (%)			75.00%	52				NO			65.93%	91
B.1.4.9	O-8	2W Analog Loop Non-Design/TN (%)			75.00%	4				NO			16.67%	6
B.1.4.10	O-8	2W Analog Loop w/INP Design/TN (%)												
B.1.4.11	O-8	2W Analog Loop w/INP Non-Design/TN (%)												
B.1.4.12	O-14	2W Analog Loop w/LNP Design/TN (%)			25.00%	4				NO			100.00%	11
B.1.4.13	O-14	2W Analog Loop w/LNP Non-Design/TN (%)												
B.1.4.14	O-8	Other Design/TN (%)			66.67%	18				NO			50.00%	18
B.1.4.15	O-8	Other Non-Design/TN (%)			85.33%	75				NO			75.00%	68
B.1.4.16	O-8	INP Standalone/TN (%)											0.00%	2
B.1.4.17	O-14	LNP Standalone/TN (%)			82.76%	29				NO			100.00%	10
		Reject Interval - Partially Mechanized - 10 hours												
B.1.7.1	O-8	Switch Ports/TN (%)												
B.1.7.2	O-8	Local Interoffice Transport/TN (%)			96.70%	1,001				YES			97.57%	824
B.1.7.3	O-8	Loop + Port Combinations/TN (%)											100.00%	1
B.1.7.4	O-8	Combo Other/TN (%)			100.00%	2				YES			50.00%	2
B.1.7.5	O-8	xDSL (ADSL, HDSL and UCL)/TN (%)												
B.1.7.6	O-8	ISDN Loop (UDN, UDC)/TN (%)			90.00%	10				YES			75.00%	8
B.1.7.7	O-8	Line Sharing/TN (%)			77.78%	9				NO			84.62%	13
B.1.7.8	O-8	2W Analog Loop Design/TN (%)			100.00%	5				YES			60.00%	5
B.1.7.9	O-8	2W Analog Loop Non-Design/TN (%)												
B.1.7.10	O-8	2W Analog Loop w/INP Design/TN (%)												
B.1.7.11	O-8	2W Analog Loop w/INP Non-Design/TN (%)			100.00%	18				YES			92.00%	25
B.1.7.12	O-14	2W Analog Loop w/LNP Design/TN (%)												
B.1.7.13	O-14	2W Analog Loop w/LNP Non-Design/TN (%)			95.00%	20				YES			100.00%	13
B.1.7.14	O-8	Other Design/TN (%)			97.14%	35				YES			100.00%	39
B.1.7.15	O-8	Other Non-Design/TN (%)												
B.1.7.16	O-8	INP Standalone/TN (%)			85.71%	28				YES			74.07%	27
B.1.7.17	O-14	LNP Standalone/TN (%)												
		Reject Interval - Non-Mechanized												
B.1.8.1	O-8	Switch Ports/TN (%)												
B.1.8.2	O-8	Local Interoffice Transport/TN (%)			96.45%	169				YES			93.08%	130
B.1.8.3	O-8	Loop + Port Combinations/TN (%)			100.00%	16				YES			94.12%	17
B.1.8.4	O-8	Combo Other/TN (%)			100.00%	8				YES			100.00%	4
B.1.8.5	O-8	xDSL (ADSL, HDSL and UCL)/TN (%)			100.00%	5				YES			100.00%	5
B.1.8.6	O-8	ISDN Loop (UDN, UDC)/TN (%)			100.00%	3				YES			100.00%	2
B.1.8.7	O-8	Line Sharing/TN (%)			100.00%	25				YES			100.00%	15
B.1.8.8	O-8	2W Analog Loop Design/TN (%)			100.00%	27				YES			100.00%	30
B.1.8.9	O-8	2W Analog Loop Non-Design/TN (%)												
B.1.8.10	O-8	2W Analog Loop w/INP Design/TN (%)											100.00%	1
B.1.8.11	O-8	2W Analog Loop w/INP Non-Design/TN (%)			100.00%	7				YES			91.67%	12
B.1.8.12	O-14	2W Analog Loop w/LNP Design/TN (%)			100.00%	5				YES				
B.1.8.13	O-14	2W Analog Loop w/LNP Non-Design/TN (%)			100.00%	20				YES			100.00%	53
B.1.8.14	O-8	Other Design/TN (%)			100.00%	231				YES			98.75%	160
B.1.8.15	O-8	Other Non-Design/TN (%)			100.00%	3				YES			100.00%	7
B.1.8.16	O-8	INP Standalone/TN (%)			100.00%	92				YES			100.00%	85
B.1.8.17	O-14	LNP Standalone/TN (%)												
		FOC Timeliness - Mechanized												
B.1.9.1	O-9	Switch Ports/TN (%)												
B.1.9.2	O-9	Local Interoffice Transport/TN (%)			98.89%	10,939				YES			97.21%	5,084
B.1.9.3	O-9	Loop + Port Combinations/TN (%)											25.00%	4
B.1.9.4	O-9	Combo Other/TN (%)			98.25%	114				YES			94.23%	208
B.1.9.5	O-9	xDSL (ADSL, HDSL and UCL)/TN (%)			98.77%	81				YES			96.15%	52
B.1.9.6	O-9	ISDN Loop (UDN, UDC)/TN (%)			100.00%	70				YES			96.77%	62
B.1.9.7	O-9	Line Sharing/TN (%)			99.44%	356				YES			98.99%	497
B.1.9.8	O-9	2W Analog Loop Design/TN (%)			100.00%	46				YES			100.00%	46
B.1.9.9	O-9	2W Analog Loop Non-Design/TN (%)												
B.1.9.10	O-9	2W Analog Loop w/INP Design/TN (%)												
B.1.9.11	O-9	2W Analog Loop w/INP Non-Design/TN (%)			100.00%	7				YES			100.00%	6
B.1.9.12	O-15	2W Analog Loop w/LNP Design/TN (%)												
B.1.9.13	O-15	2W Analog Loop w/LNP Non-Design/TN (%)			99.34%	151				YES			99.48%	194
B.1.9.14	O-9	Other Design/TN (%)			78.24%	239				NO			71.70%	212
B.1.9.15	O-9	Other Non-Design/TN (%)												
B.1.9.16	O-9	INP Standalone/TN (%)			98.82%	170				YES			96.52%	115
B.1.9.17	O-15	LNP Standalone/TN (%)												
		FOC Timeliness - Partially Mechanized - 10 hours												
B.1.12.1	O-9	Switch Ports/TN (%)												

BellSouth Monthly Performance Summary

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			19				May							
			Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.1.4.8	O-8	2W Analog Loop Design/TN (%)				NO			76.91%	128				NO
B.1.4.9	O-8	2W Analog Loop Non-Design/TN (%)				NO			50.00%	2				NO
B.1.4.10	O-8	2W Analog Loop w/INP Design/TN (%)												
B.1.4.11	O-8	2W Analog Loop w/INP Non-Design/TN (%)												
B.1.4.12	O-14	2W Analog Loop w/LNP Design/TN (%)				YES			71.43%	7				NO
B.1.4.13	O-14	2W Analog Loop w/LNP Non-Design/TN (%)												
B.1.4.14	O-8	Other Design/TN (%)				NO			71.43%	21				NO
B.1.4.15	O-8	Other Non-Design/TN (%)				NO			79.55%	88				
B.1.4.16	O-8	INP Standalone/TN (%)				NO								
B.1.4.17	O-14	LNP Standalone/TN (%)				YES			100.00%	13				YES
		Reject Interval - Partially Mechanized - 10 hours												
B.1.7.1	O-8	Switch Ports/TN (%)												
B.1.7.2	O-8	Local Interoffice Transport/TN (%)				YES			76.27%	1,951				NO
B.1.7.3	O-8	Loop + Port Combinations/TN (%)				YES								
B.1.7.4	O-8	Combo Other/TN (%)				NO			100.00%	1				YES
B.1.7.5	O-8	xDSL (ADSL, HDSL and UCL)/TN (%)							100.00%	4				YES
B.1.7.6	O-8	ISDN Loop (UDN, UDC)/TN (%)				NO			75.00%	8				NO
B.1.7.7	O-8	Line Sharing/TN (%)				NO			100.00%	31				YES
B.1.7.8	O-8	2W Analog Loop Design/TN (%)				NO								
B.1.7.9	O-8	2W Analog Loop Non-Design/TN (%)												
B.1.7.10	O-8	2W Analog Loop w/INP Design/TN (%)												
B.1.7.11	O-8	2W Analog Loop w/INP Non-Design/TN (%)				YES			96.15%	26				YES
B.1.7.12	O-14	2W Analog Loop w/LNP Design/TN (%)												
B.1.7.13	O-14	2W Analog Loop w/LNP Non-Design/TN (%)				YES			76.19%	21				NO
B.1.7.14	O-8	Other Design/TN (%)				YES			94.12%	51				YES
B.1.7.15	O-8	Other Non-Design/TN (%)												
B.1.7.16	O-8	INP Standalone/TN (%)				NO			94.34%	53				YES
B.1.7.17	O-14	LNP Standalone/TN (%)												
		Reject Interval - Non-Mechanized												
B.1.8.1	O-8	Switch Ports/TN (%)							100.00%	15				YES
B.1.8.2	O-8	Local Interoffice Transport/TN (%)				YES			95.69%	116				YES
B.1.8.3	O-8	Loop + Port Combinations/TN (%)				YES			100.00%	8				YES
B.1.8.4	O-8	Combo Other/TN (%)				YES			100.00%	13				YES
B.1.8.5	O-8	xDSL (ADSL, HDSL and UCL)/TN (%)				YES			100.00%	6				YES
B.1.8.6	O-8	ISDN Loop (UDN, UDC)/TN (%)				YES			100.00%	3				YES
B.1.8.7	O-8	Line Sharing/TN (%)				YES			100.00%	16				YES
B.1.8.8	O-8	2W Analog Loop Design/TN (%)				YES			100.00%	33				YES
B.1.8.9	O-8	2W Analog Loop Non-Design/TN (%)												
B.1.8.10	O-8	2W Analog Loop w/INP Design/TN (%)				YES								
B.1.8.11	O-8	2W Analog Loop w/INP Non-Design/TN (%)				YES			100.00%	12				YES
B.1.8.12	O-14	2W Analog Loop w/LNP Design/TN (%)												
B.1.8.13	O-14	2W Analog Loop w/LNP Non-Design/TN (%)				YES			98.11%	53				YES
B.1.8.14	O-8	Other Design/TN (%)				YES			99.60%	252				YES
B.1.8.15	O-8	Other Non-Design/TN (%)				YES			100.00%	4				YES
B.1.8.16	O-8	INP Standalone/TN (%)				YES			98.92%	93				YES
B.1.8.17	O-14	LNP Standalone/TN (%)												
		FOC Timeliness - Mechanized												
B.1.9.1	O-9	Switch Ports/TN (%)												
B.1.9.2	O-9	Local Interoffice Transport/TN (%)				YES			99.24%	5,374				YES
B.1.9.3	O-9	Loop + Port Combinations/TN (%)				NO								
B.1.9.4	O-9	Combo Other/TN (%)				NO			97.83%	138				YES
B.1.9.5	O-9	xDSL (ADSL, HDSL and UCL)/TN (%)				YES			100.00%	32				YES
B.1.9.6	O-9	ISDN Loop (UDN, UDC)/TN (%)				YES			98.57%	70				YES
B.1.9.7	O-9	Line Sharing/TN (%)				YES			98.90%	545				YES
B.1.9.8	O-9	2W Analog Loop Design/TN (%)				YES			100.00%	42				YES
B.1.9.9	O-9	2W Analog Loop Non-Design/TN (%)												
B.1.9.10	O-9	2W Analog Loop w/INP Design/TN (%)												
B.1.9.11	O-9	2W Analog Loop w/INP Non-Design/TN (%)				YES			100.00%	13				YES
B.1.9.12	O-15	2W Analog Loop w/LNP Design/TN (%)												
B.1.9.13	O-15	2W Analog Loop w/LNP Non-Design/TN (%)				YES			98.63%	146				NO
B.1.9.14	O-9	Other Design/TN (%)				NO			75.42%	236				
B.1.9.15	O-9	Other Non-Design/TN (%)												
B.1.9.16	O-9	INP Standalone/TN (%)				YES			95.51%	89				YES
B.1.9.17	O-15	LNP Standalone/TN (%)												
		FOC Timeliness - Partially Mechanized - 10 hours												
B.1.12.1	O-9	Switch Ports/TN (%)												

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			Benchmark/ Analog	August BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.1.12.2	O-9	Local Interoffice Transport/TN (%)	>= 85% w in 10 hrs								
B.1.12.3	O-9	Loop + Port Combinations/TN (%)	>= 85% w in 10 hrs			91.23%	3,183				YES
B.1.12.4	O-9	Combo Other/TN (%)	>= 85% w in 10 hrs								
B.1.12.5	O-9	xDSL (ADSL, HDSL and UCL)/TN (%)	>= 85% w in 10 hrs			92.86%	14				YES
B.1.12.6	O-9	ISDN Loop (UDN, UDC)/TN (%)	>= 85% w in 10 hrs			100.00%	8				YES
B.1.12.7	O-9	Line Sharing/TN (%)	>= 85% w in 10 hrs			91.67%	12				YES
B.1.12.8	O-9	2W Analog Loop Design/TN (%)	>= 85% w in 10 hrs			92.96%	71				YES
B.1.12.9	O-9	2W Analog Loop Non-Design/TN (%)	>= 85% w in 10 hrs			95.65%	23				YES
B.1.12.10	O-9	2W Analog Loop w/INP Design/TN (%)	>= 85% w in 10 hrs								
B.1.12.11	O-9	2W Analog Loop w/INP Non-Design/TN (%)	>= 85% w in 10 hrs								
B.1.12.12	O-15	2W Analog Loop w/LNP Design/TN (%)	>= 85% w in 10 hrs								
B.1.12.13	O-15	2W Analog Loop w/LNP Non-Design/TN (%)	>= 85% w in 10 hrs			96.43%	112				YES
B.1.12.14	O-9	Other Design/TN (%)	>= 85% w in 10 hrs								
B.1.12.15	O-9	Other Non-Design/TN (%)	>= 85% w in 10 hrs								YES
B.1.12.16	O-9	INP Standalone/TN (%)	>= 85% w in 10 hrs			93.33%	75				YES
B.1.12.17	O-15	LNP Standalone/TN (%)	>= 85% w in 10 hrs			86.36%	88				YES
		FOC Timeliness - Non-Mechanized									
B.1.13.1	O-9	Switch Ports/TN (%)	>= 85% w in 36 hrs								
B.1.13.2	O-9	Local Interoffice Transport/TN (%)	>= 85% w in 36 hrs								
B.1.13.3	O-9	Loop + Port Combinations/TN (%)	>= 85% w in 36 hrs			95.85%	193				YES
B.1.13.4	O-9	Combo Other/TN (%)	>= 85% w in 36 hrs			91.89%	37				YES
B.1.13.5	O-9	xDSL (ADSL, HDSL and UCL)/TN (%)	>= 85% w in 36 hrs			100.00%	7				YES
B.1.13.6	O-9	ISDN Loop (UDN, UDC)/TN (%)	>= 85% w in 36 hrs			100.00%	5				YES
B.1.13.7	O-9	Line Sharing/TN (%)	>= 85% w in 36 hrs								
B.1.13.8	O-9	2W Analog Loop Design/TN (%)	>= 85% w in 36 hrs			100.00%	15				YES
B.1.13.9	O-9	2W Analog Loop Non-Design/TN (%)	>= 85% w in 36 hrs			100.00%	22				YES
B.1.13.10	O-9	2W Analog Loop w/INP Design/TN (%)	>= 85% w in 36 hrs			100.00%	1				YES
B.1.13.11	O-9	2W Analog Loop w/INP Non-Design/TN (%)	>= 85% w in 36 hrs			100.00%	1				YES
B.1.13.12	O-15	2W Analog Loop w/LNP Design/TN (%)	>= 85% w in 36 hrs			100.00%	1				YES
B.1.13.13	O-15	2W Analog Loop w/LNP Non-Design/TN (%)	>= 85% w in 36 hrs			91.67%	12				YES
B.1.13.14	O-9	Other Design/TN (%)	>= 85% w in 36 hrs			66.67%	3				NO
B.1.13.15	O-9	Other Non-Design/TN (%)	>= 85% w in 36 hrs			100.00%	59				YES
B.1.13.16	O-9	INP Standalone/TN (%)	>= 85% w in 36 hrs			99.79%	480				YES
B.1.13.17	O-15	LNP Standalone/TN (%)	>= 85% w in 36 hrs			100.00%	24				YES
			>= 85% w in 36 hrs			99.70%	337				YES
		FOC & Reject Response Completeness - Mechanized									
B.1.14.1.1	O-11	Switch Ports/EDI/TN (%)	>= 95%								
B.1.14.1.2	O-11	Switch Ports/TAG/TN (%)	>= 95%								
B.1.14.2.1	O-11	Local Interoffice Transport/EDI/TN (%)	>= 95%								
B.1.14.2.2	O-11	Local Interoffice Transport/TAG/TN (%)	>= 95%								
B.1.14.3.1	O-11	Loop + Port Combinations/EDI/TN (%)	>= 95%			99.50%	18,718				YES
B.1.14.3.2	O-11	Loop + Port Combinations/TAG/TN (%)	>= 95%			99.90%	4,852				YES
B.1.14.4.1	O-11	Combo Other/EDI/TN (%)	>= 95%								
B.1.14.4.2	O-11	Combo Other/TAG/TN (%)	>= 95%								
B.1.14.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/TN (%)	>= 95%			100.00%	135				YES
B.1.14.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/TN (%)	>= 95%			94.12%	34				NO
B.1.14.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/TN (%)	>= 95%			100.00%	23				YES
B.1.14.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/TN (%)	>= 95%			83.33%	30				NO
B.1.14.7.1	O-11	Line Sharing/EDI/TN (%)	>= 95%			100.00%	53				YES
B.1.14.7.2	O-11	Line Sharing/TAG/TN (%)	>= 95%			100.00%	15				YES
B.1.14.8.1	O-11	2W Analog Loop Design/EDI/TN (%)	>= 95%			99.28%	278				YES
B.1.14.8.2	O-11	2W Analog Loop Design/TAG/TN (%)	>= 95%			96.31%	178				YES
B.1.14.9.1	O-11	2W Analog Loop Non-Design/EDI/TN (%)	>= 95%								
B.1.14.9.2	O-11	2W Analog Loop Non-Design/TAG/TN (%)	>= 95%			98.11%	53				YES
B.1.14.10.1	O-11	2W Analog Loop w/INP Design/EDI/TN (%)	>= 95%								
B.1.14.10.2	O-11	2W Analog Loop w/INP Design/TAG/TN (%)	>= 95%								
B.1.14.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/TN (%)	>= 95%								
B.1.14.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/TN (%)	>= 95%								
B.1.14.12.1	O-11	2W Analog Loop w/LNP Design/EDI/TN (%)	>= 95%								
B.1.14.12.2	O-11	2W Analog Loop w/LNP Design/TAG/TN (%)	>= 95%								
B.1.14.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/TN (%)	>= 95%								
B.1.14.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/TN (%)	>= 95%								
B.1.14.14.1	O-11	Other Design/EDI/TN (%)	>= 95%			100.00%	5				YES
B.1.14.14.2	O-11	Other Design/TAG/TN (%)	>= 95%			66.67%	6				NO
B.1.14.15.1	O-11	Other Non-Design/EDI/TN (%)	>= 95%								
B.1.14.15.2	O-11	Other Non-Design/TAG/TN (%)	>= 95%								
B.1.14.16.1	O-11	INP Standalone/EDI/TN (%)	>= 95%								
B.1.14.16.2	O-11	INP Standalone/TAG/TN (%)	>= 95%								
			>= 95%			98.96%	96				YES
			>= 95%			96.08%	102				YES
			>= 95%			98.09%	209				YES
			>= 95%			93.41%	273				NO
			>= 95%								
			>= 95%								
			>= 95%								

BellSouth Monthly Performance Summary

Tennessee, August 2002 - M ay 2002

			July							Jul			
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.1.12.2	O-9	Local Interoffice Transport/TN (%)			93.33%	2,220				YES			89.96%	1,733
B.1.12.3	O-9	Loop + Port Combinations/TN (%)			100.00%	2				YES			100.00%	2
B.1.12.4	O-9	Combo Other/TN (%)			100.00%	19				YES			100.00%	3
B.1.12.5	O-9	xDSL (ADSL, HDSL and UCL)/TN (%)			80.00%	5				NO			81.82%	11
B.1.12.6	O-9	ISDN Loop (UDN, UDC)/TN (%)			94.44%	18				YES			92.31%	13
B.1.12.7	O-9	Line Sharing/TN (%)			100.00%	47				YES			98.51%	67
B.1.12.8	O-9	2W Analog Loop Design/TN (%)			93.33%	15				YES			100.00%	9
B.1.12.9	O-9	2W Analog Loop Non-Design/TN (%)												
B.1.12.10	O-9	2W Analog Loop w/INP Design/TN (%)												
B.1.12.11	O-9	2W Analog Loop w/INP Non-Design/TN (%)			96.49%	114				YES			95.74%	94
B.1.12.12	O-15	2W Analog Loop w/LNP Design/TN (%)												
B.1.12.13	O-15	2W Analog Loop w/LNP Non-Design/TN (%)			95.38%	65				YES			95.38%	65
B.1.12.14	O-9	Other Design/TN (%)			71.43%	91				NO			75.68%	74
B.1.12.15	O-9	Other Non-Design/TN (%)												
B.1.12.16	O-9	INP Standalone/TN (%)			89.92%	129				YES			87.42%	159
B.1.12.17	O-15	LNP Standalone/TN (%)												
		FOC Timeliness - Non-Mechanized												
B.1.13.1	O-9	Switch Ports/TN (%)											100.00%	1
B.1.13.2	O-9	Local Interoffice Transport/TN (%)			97.39%	153				YES			97.08%	137
B.1.13.3	O-9	Loop + Port Combinations/TN (%)			100.00%	40				YES			96.00%	25
B.1.13.4	O-9	Combo Other/TN (%)			100.00%	15				YES			100.00%	25
B.1.13.5	O-9	xDSL (ADSL, HDSL and UCL)/TN (%)			100.00%	12				YES			100.00%	18
B.1.13.6	O-9	ISDN Loop (UDN, UDC)/TN (%)			100.00%	2				YES			100.00%	6
B.1.13.7	O-9	Line Sharing/TN (%)			100.00%	28				YES			93.55%	31
B.1.13.8	O-9	2W Analog Loop Design/TN (%)			100.00%	15				YES			100.00%	12
B.1.13.9	O-9	2W Analog Loop Non-Design/TN (%)												
B.1.13.10	O-9	2W Analog Loop w/INP Design/TN (%)												
B.1.13.11	O-9	2W Analog Loop w/INP Non-Design/TN (%)			100.00%	9				YES			100.00%	23
B.1.13.12	O-15	2W Analog Loop w/LNP Design/TN (%)			100.00%	2				YES				
B.1.13.13	O-15	2W Analog Loop w/LNP Non-Design/TN (%)			100.00%	52				YES			98.82%	85
B.1.13.14	O-9	Other Design/TN (%)			99.77%	431				YES			99.58%	471
B.1.13.15	O-9	Other Non-Design/TN (%)			100.00%	13				YES			100.00%	11
B.1.13.16	O-9	INP Standalone/TN (%)			100.00%	312				YES			99.68%	308
B.1.13.17	O-15	LNP Standalone/TN (%)												
		FOC & Reject Response Completeness - Mechanized												
B.1.14.1.1	O-11	Switch Ports/EDI/TN (%)												
B.1.14.1.2	O-11	Switch Ports/TAG/TN (%)												
B.1.14.2.1	O-11	Local Interoffice Transport/EDI/TN (%)			99.95%	8,246				YES			99.85%	2,662
B.1.14.2.2	O-11	Local Interoffice Transport/TAG/TN (%)			99.93%	4,543				YES			99.77%	3,487
B.1.14.3.1	O-11	Loop + Port Combinations/EDI/TN (%)											16.13%	31
B.1.14.3.2	O-11	Loop + Port Combinations/TAG/TN (%)												
B.1.14.4.1	O-11	Combo Other/EDI/TN (%)			95.90%	122				YES			94.61%	167
B.1.14.4.2	O-11	Combo Other/TAG/TN (%)			93.75%	32				NO			71.32%	129
B.1.14.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/TN (%)			100.00%	42				YES			96.55%	58
B.1.14.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/TN (%)			94.00%	50				NO			100.00%	1
B.1.14.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/TN (%)			100.00%	68				YES			100.00%	59
B.1.14.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/TN (%)			100.00%	26				YES			100.00%	17
B.1.14.7.1	O-11	Line Sharing/EDI/TN (%)			99.08%	218				YES			99.55%	445
B.1.14.7.2	O-11	Line Sharing/TAG/TN (%)			98.35%	182				YES			97.58%	124
B.1.14.8.1	O-11	2W Analog Loop Design/EDI/TN (%)												
B.1.14.8.2	O-11	2W Analog Loop Design/TAG/TN (%)			100.00%	49				YES			100.00%	48
B.1.14.9.1	O-11	2W Analog Loop Non-Design/EDI/TN (%)												
B.1.14.9.2	O-11	2W Analog Loop Non-Design/TAG/TN (%)												
B.1.14.10.1	O-11	2W Analog Loop w/INP Design/EDI/TN (%)												
B.1.14.10.2	O-11	2W Analog Loop w/INP Design/TAG/TN (%)												
B.1.14.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/TN (%)			100.00%	3				YES			100.00%	10
B.1.14.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/TN (%)			100.00%	8				YES			100.00%	7
B.1.14.12.1	O-11	2W Analog Loop w/LNP Design/EDI/TN (%)												
B.1.14.12.2	O-11	2W Analog Loop w/LNP Design/TAG/TN (%)												
B.1.14.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/TN (%)			98.73%	79				YES			96.99%	133
B.1.14.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/TN (%)			100.00%	88				YES			100.00%	79
B.1.14.14.1	O-11	Other Design/EDI/TN (%)			100.00%	86				YES			100.00%	84
B.1.14.14.2	O-11	Other Design/TAG/TN (%)			91.63%	251				NO			88.89%	216
B.1.14.15.1	O-11	Other Non-Design/EDI/TN (%)												
B.1.14.15.2	O-11	Other Non-Design/TAG/TN (%)											100.00%	2
B.1.14.16.1	O-11	INP Standalone/EDI/TN (%)												
B.1.14.16.2	O-11	INP Standalone/TAG/TN (%)												

BellSouth Monthly Performance Summary

Tennessee, August 2002 - May 2002

			ne				May							
			Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.1.12.2	O-9	Local Interoffice Transport/TN (%)												
B.1.12.3	O-9	Loop + Port Combinations/TN (%)				YES			76.14%	3,122				NO
B.1.12.4	O-9	Combo Other/TN (%)				YES								
B.1.12.5	O-9	xDSL (ADSL, HDSL and UCL)/TN (%)				YES			96.00%	25				YES
B.1.12.6	O-9	ISDN Loop (UDN, UDC)/TN (%)				NO			92.59%	81				YES
B.1.12.7	O-9	Line Sharing/TN (%)				YES			87.50%	8				YES
B.1.12.8	O-9	2W Analog Loop Design/TN (%)				YES			95.70%	93				YES
B.1.12.9	O-9	2W Analog Loop Non-Design/TN (%)				YES			100.00%	2				YES
B.1.12.10	O-9	2W Analog Loop w/INP Design/TN (%)												
B.1.12.11	O-9	2W Analog Loop w/INP Non-Design/TN (%)												
B.1.12.12	O-15	2W Analog Loop w/LNP Design/TN (%)				YES			92.62%	122				YES
B.1.12.13	O-15	2W Analog Loop w/LNP Non-Design/TN (%)												
B.1.12.14	O-9	Other Design/TN (%)				YES			96.15%	52				YES
B.1.12.15	O-9	Other Non-Design/TN (%)				NO			65.56%	90				NO
B.1.12.16	O-9	INP Standalone/TN (%)												
B.1.12.17	O-15	LNP Standalone/TN (%)				YES			90.09%	111				YES
	FOC Timeliness - Non-Mechanized													
B.1.13.1	O-9	Switch Ports/TN (%)				YES								
B.1.13.2	O-9	Local Interoffice Transport/TN (%)							100.00%	13				YES
B.1.13.3	O-9	Loop + Port Combinations/TN (%)				YES			99.15%	118				YES
B.1.13.4	O-9	Combo Other/TN (%)				YES			100.00%	8				YES
B.1.13.5	O-9	xDSL (ADSL, HDSL and UCL)/TN (%)				YES			100.00%	33				YES
B.1.13.6	O-9	ISDN Loop (UDN, UDC)/TN (%)				YES			100.00%	20				YES
B.1.13.7	O-9	Line Sharing/TN (%)				YES			100.00%	6				YES
B.1.13.8	O-9	2W Analog Loop Design/TN (%)				YES			100.00%	36				YES
B.1.13.9	O-9	2W Analog Loop Non-Design/TN (%)				YES			100.00%	28				YES
B.1.13.10	O-9	2W Analog Loop w/INP Design/TN (%)												
B.1.13.11	O-9	2W Analog Loop w/INP Non-Design/TN (%)												
B.1.13.12	O-15	2W Analog Loop w/LNP Design/TN (%)				YES			96.43%	28				YES
B.1.13.13	O-15	2W Analog Loop w/LNP Non-Design/TN (%)												
B.1.13.14	O-9	Other Design/TN (%)				YES			100.00%	124				YES
B.1.13.15	O-9	Other Non-Design/TN (%)				YES			99.24%	528				YES
B.1.13.16	O-9	INP Standalone/TN (%)				YES			100.00%	10				YES
B.1.13.17	O-15	LNP Standalone/TN (%)				YES			99.60%	247				YES
	FOC & Reject Response Completeness - Mechanized													
B.1.14.1.1	O-11	Switch Ports/EDI/TN (%)												
B.1.14.1.2	O-11	Switch Ports/TAG/TN (%)												
B.1.14.2.1	O-11	Local Interoffice Transport/EDI/TN (%)												
B.1.14.2.2	O-11	Local Interoffice Transport/TAG/TN (%)												
B.1.14.3.1	O-11	Loop + Port Combinations/EDI/TN (%)				YES			98.02%	4,607				YES
B.1.14.3.2	O-11	Loop + Port Combinations/TAG/TN (%)				YES			97.73%	3,265				YES
B.1.14.4.1	O-11	Combo Other/EDI/TN (%)				NO								
B.1.14.4.2	O-11	Combo Other/TAG/TN (%)												
B.1.14.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/TN (%)				NO			98.05%	154				YES
B.1.14.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/TN (%)				NO			91.89%	37				NO
B.1.14.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/TN (%)				YES			96.88%	32				YES
B.1.14.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/TN (%)				YES			44.44%	9				NO
B.1.14.7.1	O-11	Line Sharing/EDI/TN (%)				YES			98.65%	74				YES
B.1.14.7.2	O-11	Line Sharing/TAG/TN (%)				YES			100.00%	8				YES
B.1.14.8.1	O-11	2W Analog Loop Design/EDI/TN (%)				YES			96.47%	538				YES
B.1.14.8.2	O-11	2W Analog Loop Design/TAG/TN (%)				YES			98.55%	138				YES
B.1.14.9.1	O-11	2W Analog Loop Non-Design/EDI/TN (%)												
B.1.14.9.2	O-11	2W Analog Loop Non-Design/TAG/TN (%)				YES			97.73%	44				YES
B.1.14.10.1	O-11	2W Analog Loop w/INP Design/EDI/TN (%)												
B.1.14.10.2	O-11	2W Analog Loop w/INP Design/TAG/TN (%)												
B.1.14.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/TN (%)												
B.1.14.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/TN (%)												
B.1.14.12.1	O-11	2W Analog Loop w/LNP Design/EDI/TN (%)				YES			100.00%	14				YES
B.1.14.12.2	O-11	2W Analog Loop w/LNP Design/TAG/TN (%)				YES			100.00%	6				YES
B.1.14.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/TN (%)												
B.1.14.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/TN (%)												
B.1.14.14.1	O-11	Other Design/EDI/TN (%)				YES			92.08%	101				NO
B.1.14.14.2	O-11	Other Design/TAG/TN (%)				YES			97.30%	74				YES
B.1.14.15.1	O-11	Other Non-Design/EDI/TN (%)				YES			97.97%	148				YES
B.1.14.15.2	O-11	Other Non-Design/TAG/TN (%)				NO			91.06%	179				NO
B.1.14.16.1	O-11	INP Standalone/EDI/TN (%)												
B.1.14.16.2	O-11	INP Standalone/TAG/TN (%)				YES								

BellSouth Monthly Performance Summary

Tennessee, August 2002 - M ay 2002

			Benchmark/ Analog	August BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.1.14.17.1	O-11	LNP Standalone/EDI/TN (%)	>= 95%			98.94%	94				YES
B.1.14.17.2	O-11	LNP Standalone/TAG/TN (%)	>= 95%			97.22%	72				YES
		FOC & Reject Response Completeness - Partially Mechanized									
B.1.15.1.1	O-11	Switch Ports/EDI/TN (%)	>= 95%								
B.1.15.1.2	O-11	Switch Ports/TAG/TN (%)	>= 95%								
B.1.15.2.1	O-11	Local Interoffice Transport/EDI/TN (%)	>= 95%								
B.1.15.2.2	O-11	Local Interoffice Transport/TAG/TN (%)	>= 95%								
B.1.15.3.1	O-11	Loop + Port Combinations/EDI/TN (%)	>= 95%			99.79%	1,907				YES
B.1.15.3.2	O-11	Loop + Port Combinations/TAG/TN (%)	>= 95%			99.80%	2,047				YES
B.1.15.4.1	O-11	Combo Other/EDI/TN (%)	>= 95%								
B.1.15.4.2	O-11	Combo Other/TAG/TN (%)	>= 95%								
B.1.15.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/TN (%)	>= 95%			100.00%	10				YES
B.1.15.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/TN (%)	>= 95%			100.00%	5				YES
B.1.15.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/TN (%)	>= 95%			100.00%	6				YES
B.1.15.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/TN (%)	>= 95%			50.00%	4				NO
B.1.15.7.1	O-11	Line Sharing/EDI/TN (%)	>= 95%			100.00%	15				YES
B.1.15.7.2	O-11	Line Sharing/TAG/TN (%)	>= 95%			100.00%	5				YES
B.1.15.8.1	O-11	2W Analog Loop Design/EDI/TN (%)	>= 95%			100.00%	56				YES
B.1.15.8.2	O-11	2W Analog Loop Design/TAG/TN (%)	>= 95%			100.00%	30				YES
B.1.15.9.1	O-11	2W Analog Loop Non-Design/EDI/TN (%)	>= 95%								
B.1.15.9.2	O-11	2W Analog Loop Non-Design/TAG/TN (%)	>= 95%			100.00%	27				YES
B.1.15.10.1	O-11	2W Analog Loop w/INP Design/EDI/TN (%)	>= 95%								
B.1.15.10.2	O-11	2W Analog Loop w/INP Design/TAG/TN (%)	>= 95%								
B.1.15.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/TN (%)	>= 95%								
B.1.15.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/TN (%)	>= 95%								
B.1.15.12.1	O-11	2W Analog Loop w/LNP Design/EDI/TN (%)	>= 95%			100.00%	50				YES
B.1.15.12.2	O-11	2W Analog Loop w/LNP Design/TAG/TN (%)	>= 95%			100.00%	76				YES
B.1.15.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/TN (%)	>= 95%								
B.1.15.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/TN (%)	>= 95%								
B.1.15.14.1	O-11	Other Design/EDI/TN (%)	>= 95%			100.00%	45				YES
B.1.15.14.2	O-11	Other Design/TAG/TN (%)	>= 95%			100.00%	43				YES
B.1.15.15.1	O-11	Other Non-Design/EDI/TN (%)	>= 95%			100.00%	31				YES
B.1.15.15.2	O-11	Other Non-Design/TAG/TN (%)	>= 95%			97.94%	97				YES
B.1.15.16.1	O-11	INP Standalone/EDI/TN (%)	>= 95%								
B.1.15.16.2	O-11	INP Standalone/TAG/TN (%)	>= 95%								
B.1.15.17.1	O-11	LNP Standalone/EDI/TN (%)	>= 95%			100.00%	140				YES
B.1.15.17.2	O-11	LNP Standalone/TAG/TN (%)	>= 95%			100.00%	52				YES
		FOC & Reject Response Completeness - Non-Mechanized									
B.1.16.1	O-11	Switch Ports/TN (%)	>= 95%								
B.1.16.2	O-11	Local Interoffice Transport/TN (%)	>= 95%			100.00%	3				YES
B.1.16.3	O-11	Loop + Port Combinations/TN (%)	>= 95%			95.21%	459				YES
B.1.16.4	O-11	Combo Other/TN (%)	>= 95%			94.23%	52				NO
B.1.16.5	O-11	xDSL (ADSL, HDSL and UCL)/TN (%)	>= 95%			100.00%	8				YES
B.1.16.6	O-11	ISDN Loop (UDN, UDC)/TN (%)	>= 95%			100.00%	5				YES
B.1.16.7	O-11	Line Sharing/TN (%)	>= 95%			100.00%	2				YES
B.1.16.8	O-11	2W Analog Loop Design/TN (%)	>= 95%			100.00%	24				YES
B.1.16.9	O-11	2W Analog Loop Non-Design/TN (%)	>= 95%			89.80%	49				NO
B.1.16.10	O-11	2W Analog Loop w/INP Design/TN (%)	>= 95%			100.00%	2				YES
B.1.16.11	O-11	2W Analog Loop w/INP Non-Design/TN (%)	>= 95%			66.67%	3				NO
B.1.16.12	O-11	2W Analog Loop w/LNP Design/TN (%)	>= 95%			95.45%	22				YES
B.1.16.13	O-11	2W Analog Loop w/LNP Non-Design/TN (%)	>= 95%			100.00%	6				YES
B.1.16.14	O-11	Other Design/TN (%)	>= 95%			100.00%	92				YES
B.1.16.15	O-11	Other Non-Design/TN (%)	>= 95%			98.44%	707				YES
B.1.16.16	O-11	INP Standalone/TN (%)	>= 95%			78.38%	37				NO
B.1.16.17	O-11	LNP Standalone/TN (%)	>= 95%			98.61%	432				YES
		FOC & Reject Response Completeness (Multiple Responses) - Mechanized									
B.1.17.1.1	O-11	Switch Ports/EDI/TN (%)	>= 95%								
B.1.17.1.2	O-11	Switch Ports/TAG/TN (%)	>= 95%								
B.1.17.2.1	O-11	Local Interoffice Transport/EDI/TN (%)	>= 95%								
B.1.17.2.2	O-11	Local Interoffice Transport/TAG/TN (%)	>= 95%								
B.1.17.3.1	O-11	Loop + Port Combinations/EDI/TN (%)	>= 95%			98.67%	18,625				YES
B.1.17.3.2	O-11	Loop + Port Combinations/TAG/TN (%)	>= 95%			99.77%	4,847				YES
B.1.17.4.1	O-11	Combo Other/EDI/TN (%)	>= 95%								
B.1.17.4.2	O-11	Combo Other/TAG/TN (%)	>= 95%								
B.1.17.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/TN (%)	>= 95%			99.26%	135				YES
B.1.17.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/TN (%)	>= 95%			81.25%	32				NO

BellSouth Monthly Performance Summary

Tennessee, August 2002 - M ay 2002

			July								Jul			
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.1.14.17.1	O-11	LNP Standalone/EDI/TN (%)			100.00%	125				YES			100.00%	79
B.1.14.17.2	O-11	LNP Standalone/TAG/TN (%)			98.63%	73				YES			95.83%	48
		FOC & Reject Response Completeness - Partially Mechanized												
B.1.15.1.1	O-11	Switch Ports/EDI/TN (%)												
B.1.15.1.2	O-11	Switch Ports/TAG/TN (%)												
B.1.15.2.1	O-11	Local Interoffice Transport/EDI/TN (%)												
B.1.15.2.2	O-11	Local Interoffice Transport/TAG/TN (%)												
B.1.15.3.1	O-11	Loop + Port Combinations/EDI/TN (%)			99.91%	1,138				YES			99.44%	535
B.1.15.3.2	O-11	Loop + Port Combinations/TAG/TN (%)			99.89%	1,887				YES			99.82%	1,695
B.1.15.4.1	O-11	Combo Other/EDI/TN (%)			100.00%	2				YES			100.00%	3
B.1.15.4.2	O-11	Combo Other/TAG/TN (%)												
B.1.15.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/TN (%)			87.50%	16				NO			75.00%	4
B.1.15.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/TN (%)			66.67%	9				NO			66.67%	3
B.1.15.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/TN (%)			33.33%	3				NO			100.00%	5
B.1.15.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/TN (%)			83.33%	6				NO			100.00%	4
B.1.15.7.1	O-11	Line Sharing/EDI/TN (%)			100.00%	24				YES			100.00%	14
B.1.15.7.2	O-11	Line Sharing/TAG/TN (%)			100.00%	5				YES			100.00%	5
B.1.15.8.1	O-11	2W Analog Loop Design/EDI/TN (%)			100.00%	27				YES			100.00%	62
B.1.15.8.2	O-11	2W Analog Loop Design/TAG/TN (%)			100.00%	24				YES			100.00%	13
B.1.15.9.1	O-11	2W Analog Loop Non-Design/EDI/TN (%)												
B.1.15.9.2	O-11	2W Analog Loop Non-Design/TAG/TN (%)			100.00%	19				YES			100.00%	12
B.1.15.10.1	O-11	2W Analog Loop w/INP Design/EDI/TN (%)												
B.1.15.10.2	O-11	2W Analog Loop w/INP Design/TAG/TN (%)												
B.1.15.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/TN (%)												
B.1.15.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/TN (%)												
B.1.15.12.1	O-11	2W Analog Loop w/LNP Design/EDI/TN (%)			100.00%	39				YES			100.00%	69
B.1.15.12.2	O-11	2W Analog Loop w/LNP Design/TAG/TN (%)			98.95%	95				YES			98.04%	51
B.1.15.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/TN (%)												
B.1.15.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/TN (%)												
B.1.15.14.1	O-11	Other Design/EDI/TN (%)			97.56%	41				YES			100.00%	51
B.1.15.14.2	O-11	Other Design/TAG/TN (%)			100.00%	42				YES			100.00%	23
B.1.15.15.1	O-11	Other Non-Design/EDI/TN (%)			100.00%	38				YES			100.00%	34
B.1.15.15.2	O-11	Other Non-Design/TAG/TN (%)			93.10%	87				NO			96.10%	77
B.1.15.16.1	O-11	INP Standalone/EDI/TN (%)												
B.1.15.16.2	O-11	INP Standalone/TAG/TN (%)												
B.1.15.17.1	O-11	LNP Standalone/EDI/TN (%)			100.00%	102				YES			100.00%	120
B.1.15.17.2	O-11	LNP Standalone/TAG/TN (%)			100.00%	48				YES			100.00%	50
		FOC & Reject Response Completeness - Non-Mechanized												
B.1.16.1	O-11	Switch Ports/TN (%)											100.00%	1
B.1.16.2	O-11	Local Interoffice Transport/TN (%)												
B.1.16.3	O-11	Loop + Port Combinations/TN (%)			94.24%	330				NO			97.00%	267
B.1.16.4	O-11	Combo Other/TN (%)			98.25%	57				YES			100.00%	40
B.1.16.5	O-11	xDSL (ADSL, HDSL and UCL)/TN (%)			95.83%	24				YES			100.00%	28
B.1.16.6	O-11	ISDN Loop (UDN, UDC)/TN (%)			100.00%	17				YES			100.00%	23
B.1.16.7	O-11	Line Sharing/TN (%)			100.00%	5				YES			100.00%	7
B.1.16.8	O-11	2W Analog Loop Design/TN (%)			96.36%	55				YES			100.00%	47
B.1.16.9	O-11	2W Analog Loop Non-Design/TN (%)			100.00%	46				YES			94.87%	39
B.1.16.10	O-11	2W Analog Loop w/INP Design/TN (%)												
B.1.16.11	O-11	2W Analog Loop w/INP Non-Design/TN (%)											100.00%	1
B.1.16.12	O-11	2W Analog Loop w/LNP Design/TN (%)			100.00%	16				YES			90.91%	33
B.1.16.13	O-11	2W Analog Loop w/LNP Non-Design/TN (%)			100.00%	7				YES				
B.1.16.14	O-11	Other Design/TN (%)			97.14%	70				YES			98.51%	134
B.1.16.15	O-11	Other Non-Design/TN (%)			98.94%	662				YES			99.19%	615
B.1.16.16	O-11	INP Standalone/TN (%)			94.44%	18				NO			90.48%	21
B.1.16.17	O-11	LNP Standalone/TN (%)			99.26%	403				YES			98.51%	403
		FOC & Reject Response Completeness (Multiple Responses) - Mechanized												
B.1.17.1.1	O-11	Switch Ports/EDI/TN (%)												
B.1.17.1.2	O-11	Switch Ports/TAG/TN (%)												
B.1.17.2.1	O-11	Local Interoffice Transport/EDI/TN (%)												
B.1.17.2.2	O-11	Local Interoffice Transport/TAG/TN (%)												
B.1.17.3.1	O-11	Loop + Port Combinations/EDI/TN (%)			99.41%	8,242				YES			96.46%	2,658
B.1.17.3.2	O-11	Loop + Port Combinations/TAG/TN (%)			99.63%	4,540				YES			99.37%	3,479
B.1.17.4.1	O-11	Combo Other/EDI/TN (%)											100.00%	5
B.1.17.4.2	O-11	Combo Other/TAG/TN (%)												
B.1.17.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/TN (%)			95.73%	117				YES			97.47%	158
B.1.17.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/TN (%)			100.00%	30				YES			95.65%	92

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			ne					May						
			Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.1.14.17.1	O-11	LNP Standalone/EDI/TN (%)				YES			91.36%	81				NO
B.1.14.17.2	O-11	LNP Standalone/TAG/TN (%)				YES			100.00%	27				YES
		FOC & Reject Response Completeness - Partially Mechanized												
B.1.15.1.1	O-11	Switch Ports/EDI/TN (%)												
B.1.15.1.2	O-11	Switch Ports/TAG/TN (%)												
B.1.15.2.1	O-11	Local Interoffice Transport/EDI/TN (%)												
B.1.15.2.2	O-11	Local Interoffice Transport/TAG/TN (%)												
B.1.15.3.1	O-11	Loop + Port Combinations/EDI/TN (%)				YES			98.10%	2,743				YES
B.1.15.3.2	O-11	Loop + Port Combinations/TAG/TN (%)				YES			99.85%	2,009				YES
B.1.15.4.1	O-11	Combo Other/EDI/TN (%)				YES								
B.1.15.4.2	O-11	Combo Other/TAG/TN (%)												
B.1.15.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/TN (%)				NO			86.96%	23				NO
B.1.15.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/TN (%)				NO			100.00%	5				YES
B.1.15.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/TN (%)				YES			96.30%	27				YES
B.1.15.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/TN (%)				YES			100.00%	55				YES
B.1.15.7.1	O-11	Line Sharing/EDI/TN (%)				YES			100.00%	8				YES
B.1.15.7.2	O-11	Line Sharing/TAG/TN (%)				YES			100.00%	5				YES
B.1.15.8.1	O-11	2W Analog Loop Design/EDI/TN (%)				YES			96.59%	88				YES
B.1.15.8.2	O-11	2W Analog Loop Design/TAG/TN (%)				YES			100.00%	33				YES
B.1.15.9.1	O-11	2W Analog Loop Non-Design/EDI/TN (%)												
B.1.15.9.2	O-11	2W Analog Loop Non-Design/TAG/TN (%)				YES			100.00%	2				YES
B.1.15.10.1	O-11	2W Analog Loop w/INP Design/EDI/TN (%)												
B.1.15.10.2	O-11	2W Analog Loop w/INP Design/TAG/TN (%)												
B.1.15.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/TN (%)												
B.1.15.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/TN (%)												
B.1.15.12.1	O-11	2W Analog Loop w/LNP Design/EDI/TN (%)				YES			100.00%	68				YES
B.1.15.12.2	O-11	2W Analog Loop w/LNP Design/TAG/TN (%)				YES			100.00%	76				YES
B.1.15.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/TN (%)												
B.1.15.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/TN (%)												
B.1.15.14.1	O-11	Other Design/EDI/TN (%)				YES			94.29%	35				NO
B.1.15.14.2	O-11	Other Design/TAG/TN (%)				YES			100.00%	38				YES
B.1.15.15.1	O-11	Other Non-Design/EDI/TN (%)				YES			90.74%	54				NO
B.1.15.15.2	O-11	Other Non-Design/TAG/TN (%)				YES			95.35%	86				YES
B.1.15.16.1	O-11	INP Standalone/EDI/TN (%)												
B.1.15.16.2	O-11	INP Standalone/TAG/TN (%)												
B.1.15.17.1	O-11	LNP Standalone/EDI/TN (%)				YES			99.07%	108				YES
B.1.15.17.2	O-11	LNP Standalone/TAG/TN (%)				YES			100.00%	55				YES
		FOC & Reject Response Completeness - Non-Mechanized												
B.1.16.1	O-11	Switch Ports/TN (%)				YES								
B.1.16.2	O-11	Local Interoffice Transport/TN (%)							100.00%	26				YES
B.1.16.3	O-11	Loop + Port Combinations/TN (%)				YES			92.59%	243				NO
B.1.16.4	O-11	Combo Other/TN (%)				YES			100.00%	14				YES
B.1.16.5	O-11	xDSL (ADSL, HDSL and UCL)/TN (%)				YES			100.00%	44				YES
B.1.16.6	O-11	ISDN Loop (UDN, UDC)/TN (%)				YES			100.00%	25				YES
B.1.16.7	O-11	Line Sharing/TN (%)				YES			90.00%	10				NO
B.1.16.8	O-11	2W Analog Loop Design/TN (%)				YES			98.04%	51				YES
B.1.16.9	O-11	2W Analog Loop Non-Design/TN (%)				NO			84.29%	70				NO
B.1.16.10	O-11	2W Analog Loop w/INP Design/TN (%)												
B.1.16.11	O-11	2W Analog Loop w/INP Non-Design/TN (%)				YES								
B.1.16.12	O-11	2W Analog Loop w/LNP Design/TN (%)				NO			92.86%	42				NO
B.1.16.13	O-11	2W Analog Loop w/LNP Non-Design/TN (%)												
B.1.16.14	O-11	Other Design/TN (%)				YES			97.16%	176				YES
B.1.16.15	O-11	Other Non-Design/TN (%)				YES			97.20%	751				YES
B.1.16.16	O-11	INP Standalone/TN (%)				NO			93.33%	15				NO
B.1.16.17	O-11	LNP Standalone/TN (%)				YES			94.78%	345				NO
		FOC & Reject Response Completeness (Multiple Responses) - Mechanized												
B.1.17.1.1	O-11	Switch Ports/EDI/TN (%)												
B.1.17.1.2	O-11	Switch Ports/TAG/TN (%)												
B.1.17.2.1	O-11	Local Interoffice Transport/EDI/TN (%)												
B.1.17.2.2	O-11	Local Interoffice Transport/TAG/TN (%)												
B.1.17.3.1	O-11	Loop + Port Combinations/EDI/TN (%)				YES			99.87%	4,516				YES
B.1.17.3.2	O-11	Loop + Port Combinations/TAG/TN (%)				YES			99.62%	3,191				YES
B.1.17.4.1	O-11	Combo Other/EDI/TN (%)				YES								
B.1.17.4.2	O-11	Combo Other/TAG/TN (%)												
B.1.17.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/TN (%)				YES			98.01%	151				YES
B.1.17.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/TN (%)				YES			100.00%	34				YES

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			Benchmark/ Analog	August BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.1.17.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/TN (%)	>= 95%			91.30%	23				NO
B.1.17.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/TN (%)	>= 95%			84.00%	25				NO
B.1.17.7.1	O-11	Line Sharing/EDI/TN (%)	>= 95%			92.45%	53				NO
B.1.17.7.2	O-11	Line Sharing/TAG/TN (%)	>= 95%			93.33%	15				NO
B.1.17.8.1	O-11	2W Analog Loop Design/EDI/TN (%)	>= 95%			90.94%	276				NO
B.1.17.8.2	O-11	2W Analog Loop Design/TAG/TN (%)	>= 95%			96.57%	175				YES
B.1.17.9.1	O-11	2W Analog Loop Non-Design/EDI/TN (%)	>= 95%								
B.1.17.9.2	O-11	2W Analog Loop Non-Design/TAG/TN (%)	>= 95%			96.15%	52				YES
B.1.17.10.1	O-11	2W Analog Loop w/INP Design/EDI/TN (%)	>= 95%								
B.1.17.10.2	O-11	2W Analog Loop w/INP Design/TAG/TN (%)	>= 95%								
B.1.17.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/TN (%)	>= 95%								
B.1.17.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/TN (%)	>= 95%								
B.1.17.12.1	O-11	2W Analog Loop w/LNP Design/EDI/TN (%)	>= 95%			60.00%	5				NO
B.1.17.12.2	O-11	2W Analog Loop w/LNP Design/TAG/TN (%)	>= 95%			50.00%	4				NO
B.1.17.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/TN (%)	>= 95%								
B.1.17.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/TN (%)	>= 95%								
B.1.17.14.1	O-11	Other Design/EDI/TN (%)	>= 95%			94.74%	95				NO
B.1.17.14.2	O-11	Other Design/TAG/TN (%)	>= 95%			97.96%	98				YES
B.1.17.15.1	O-11	Other Non-Design/EDI/TN (%)	>= 95%			97.56%	205				YES
B.1.17.15.2	O-11	Other Non-Design/TAG/TN (%)	>= 95%			100.00%	255				YES
B.1.17.16.1	O-11	INP Standalone/EDI/TN (%)	>= 95%								
B.1.17.16.2	O-11	INP Standalone/TAG/TN (%)	>= 95%								
B.1.17.17.1	O-11	LNP Standalone/EDI/TN (%)	>= 95%			97.85%	93				YES
B.1.17.17.2	O-11	LNP Standalone/TAG/TN (%)	>= 95%			65.71%	70				NO
		FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized									
B.1.18.1.1	O-11	Switch Ports/EDI/TN (%)	>= 95%								
B.1.18.1.2	O-11	Switch Ports/TAG/TN (%)	>= 95%								
B.1.18.2.1	O-11	Local Interoffice Transport/EDI/TN (%)	>= 95%								
B.1.18.2.2	O-11	Local Interoffice Transport/TAG/TN (%)	>= 95%								
B.1.18.3.1	O-11	Loop + Port Combinations/EDI/TN (%)	>= 95%			92.33%	1,903				NO
B.1.18.3.2	O-11	Loop + Port Combinations/TAG/TN (%)	>= 95%			87.47%	2,043				NO
B.1.18.4.1	O-11	Combo Other/EDI/TN (%)	>= 95%								
B.1.18.4.2	O-11	Combo Other/TAG/TN (%)	>= 95%								
B.1.18.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/TN (%)	>= 95%			100.00%	10				YES
B.1.18.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/TN (%)	>= 95%			80.00%	5				NO
B.1.18.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/TN (%)	>= 95%			100.00%	6				YES
B.1.18.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/TN (%)	>= 95%			100.00%	2				YES
B.1.18.7.1	O-11	Line Sharing/EDI/TN (%)	>= 95%			100.00%	15				YES
B.1.18.7.2	O-11	Line Sharing/TAG/TN (%)	>= 95%			100.00%	5				YES
B.1.18.8.1	O-11	2W Analog Loop Design/EDI/TN (%)	>= 95%			89.29%	56				NO
B.1.18.8.2	O-11	2W Analog Loop Design/TAG/TN (%)	>= 95%			93.33%	30				NO
B.1.18.9.1	O-11	2W Analog Loop Non-Design/EDI/TN (%)	>= 95%								
B.1.18.9.2	O-11	2W Analog Loop Non-Design/TAG/TN (%)	>= 95%			85.19%	27				NO
B.1.18.10.1	O-11	2W Analog Loop w/INP Design/EDI/TN (%)	>= 95%								
B.1.18.10.2	O-11	2W Analog Loop w/INP Design/TAG/TN (%)	>= 95%								
B.1.18.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/TN (%)	>= 95%								
B.1.18.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/TN (%)	>= 95%								
B.1.18.12.1	O-11	2W Analog Loop w/LNP Design/EDI/TN (%)	>= 95%			68.00%	50				NO
B.1.18.12.2	O-11	2W Analog Loop w/LNP Design/TAG/TN (%)	>= 95%			53.95%	76				NO
B.1.18.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/TN (%)	>= 95%								
B.1.18.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/TN (%)	>= 95%								
B.1.18.14.1	O-11	Other Design/EDI/TN (%)	>= 95%			86.67%	45				NO
B.1.18.14.2	O-11	Other Design/TAG/TN (%)	>= 95%			100.00%	43				YES
B.1.18.15.1	O-11	Other Non-Design/EDI/TN (%)	>= 95%			93.55%	31				NO
B.1.18.15.2	O-11	Other Non-Design/TAG/TN (%)	>= 95%			95.79%	95				YES
B.1.18.16.1	O-11	INP Standalone/EDI/TN (%)	>= 95%								
B.1.18.16.2	O-11	INP Standalone/TAG/TN (%)	>= 95%								
B.1.18.17.1	O-11	LNP Standalone/EDI/TN (%)	>= 95%			57.14%	140				NO
B.1.18.17.2	O-11	LNP Standalone/TAG/TN (%)	>= 95%			46.15%	52				NO
		FOC & Reject Response Completeness (Multiple Responses) - Non-Mechanized									
B.1.19.1	O-11	Switch Ports/TN (%)	>= 95%								
B.1.19.2	O-11	Local Interoffice Transport/TN (%)	>= 95%			100.00%	3				YES
B.1.19.3	O-11	Loop + Port Combinations/TN (%)	>= 95%			94.97%	437				NO
B.1.19.4	O-11	Combo Other/TN (%)	>= 95%			89.80%	49				NO
B.1.19.5	O-11	xDSL (ADSL, HDSL and UCL)/TN (%)	>= 95%			100.00%	8				YES
B.1.19.6	O-11	ISDN Loop (UDN, UDC)/TN (%)	>= 95%			80.00%	5				NO
B.1.19.7	O-11	Line Sharing/TN (%)	>= 95%			100.00%	2				YES

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			July								Jun			
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.1.17.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/TN (%)			97.62%	42				YES			98.21%	56
B.1.17.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/TN (%)			100.00%	47				YES			100.00%	1
B.1.17.7.1	O-11	Line Sharing/EDI/TN (%)			97.06%	68				YES			94.92%	59
B.1.17.7.2	O-11	Line Sharing/TAG/TN (%)			100.00%	26				YES			94.12%	17
B.1.17.8.1	O-11	2W Analog Loop Design/EDI/TN (%)			95.37%	216				YES			91.87%	443
B.1.17.8.2	O-11	2W Analog Loop Design/TAG/TN (%)			96.09%	179				YES			94.21%	121
B.1.17.9.1	O-11	2W Analog Loop Non-Design/EDI/TN (%)												
B.1.17.9.2	O-11	2W Analog Loop Non-Design/TAG/TN (%)			100.00%	49				YES			91.67%	48
B.1.17.10.1	O-11	2W Analog Loop w/INP Design/EDI/TN (%)												
B.1.17.10.2	O-11	2W Analog Loop w/INP Design/TAG/TN (%)												
B.1.17.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/TN (%)												
B.1.17.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/TN (%)												
B.1.17.12.1	O-11	2W Analog Loop w/LNP Design/EDI/TN (%)			66.67%	3				NO			90.00%	10
B.1.17.12.2	O-11	2W Analog Loop w/LNP Design/TAG/TN (%)			62.50%	8				NO			85.71%	7
B.1.17.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/TN (%)												
B.1.17.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/TN (%)												
B.1.17.14.1	O-11	Other Design/EDI/TN (%)			97.44%	78				YES			94.57%	129
B.1.17.14.2	O-11	Other Design/TAG/TN (%)			98.86%	88				YES			97.47%	79
B.1.17.15.1	O-11	Other Non-Design/EDI/TN (%)			89.53%	86				NO			96.43%	84
B.1.17.15.2	O-11	Other Non-Design/TAG/TN (%)			100.00%	230				YES			100.00%	192
B.1.17.16.1	O-11	INP Standalone/EDI/TN (%)												
B.1.17.16.2	O-11	INP Standalone/TAG/TN (%)											100.00%	2
B.1.17.17.1	O-11	LNP Standalone/EDI/TN (%)			80.80%	125				NO			78.48%	79
B.1.17.17.2	O-11	LNP Standalone/TAG/TN (%)			72.22%	72				NO			63.04%	46
		FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized												
B.1.18.1.1	O-11	Switch Ports/EDI/TN (%)												
B.1.18.1.2	O-11	Switch Ports/TAG/TN (%)												
B.1.18.2.1	O-11	Local Interoffice Transport/EDI/TN (%)												
B.1.18.2.2	O-11	Local Interoffice Transport/TAG/TN (%)												
B.1.18.3.1	O-11	Loop + Port Combinations/EDI/TN (%)			94.28%	1,137				NO			86.47%	532
B.1.18.3.2	O-11	Loop + Port Combinations/TAG/TN (%)			88.70%	1,885				NO			87.23%	1,692
B.1.18.4.1	O-11	Combo Other/EDI/TN (%)			100.00%	2				YES			100.00%	3
B.1.18.4.2	O-11	Combo Other/TAG/TN (%)												
B.1.18.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/TN (%)			78.57%	14				NO			100.00%	3
B.1.18.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/TN (%)			100.00%	6				YES			100.00%	2
B.1.18.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/TN (%)			0.00%	1				NO			100.00%	5
B.1.18.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/TN (%)			100.00%	5				YES			100.00%	4
B.1.18.7.1	O-11	Line Sharing/EDI/TN (%)			95.83%	24				YES			92.86%	14
B.1.18.7.2	O-11	Line Sharing/TAG/TN (%)			100.00%	5				YES			100.00%	5
B.1.18.8.1	O-11	2W Analog Loop Design/EDI/TN (%)			85.19%	27				NO			83.87%	62
B.1.18.8.2	O-11	2W Analog Loop Design/TAG/TN (%)			95.83%	24				YES			100.00%	13
B.1.18.9.1	O-11	2W Analog Loop Non-Design/EDI/TN (%)												
B.1.18.9.2	O-11	2W Analog Loop Non-Design/TAG/TN (%)			94.74%	19				NO			91.67%	12
B.1.18.10.1	O-11	2W Analog Loop w/INP Design/EDI/TN (%)												
B.1.18.10.2	O-11	2W Analog Loop w/INP Design/TAG/TN (%)												
B.1.18.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/TN (%)												
B.1.18.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/TN (%)												
B.1.18.12.1	O-11	2W Analog Loop w/LNP Design/EDI/TN (%)			43.59%	39				NO			39.13%	69
B.1.18.12.2	O-11	2W Analog Loop w/LNP Design/TAG/TN (%)			43.62%	94				NO			48.00%	50
B.1.18.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/TN (%)												
B.1.18.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/TN (%)												
B.1.18.14.1	O-11	Other Design/EDI/TN (%)			75.00%	40				NO			92.16%	51
B.1.18.14.2	O-11	Other Design/TAG/TN (%)			95.24%	42				YES			100.00%	23
B.1.18.15.1	O-11	Other Non-Design/EDI/TN (%)			60.53%	38				NO			97.06%	34
B.1.18.15.2	O-11	Other Non-Design/TAG/TN (%)			93.83%	81				NO			95.95%	74
B.1.18.16.1	O-11	INP Standalone/EDI/TN (%)												
B.1.18.16.2	O-11	INP Standalone/TAG/TN (%)												
B.1.18.17.1	O-11	LNP Standalone/EDI/TN (%)			57.84%	102				NO			57.50%	120
B.1.18.17.2	O-11	LNP Standalone/TAG/TN (%)			56.25%	48				NO			72.00%	50
		FOC & Reject Response Completeness (Multiple Responses) - Non-Mechanized												
B.1.19.1	O-11	Switch Ports/TN (%)											100.00%	1
B.1.19.2	O-11	Local Interoffice Transport/TN (%)												
B.1.19.3	O-11	Loop + Port Combinations/TN (%)			91.96%	311				NO			95.37%	259
B.1.19.4	O-11	Combo Other/TN (%)			89.29%	56				NO			90.00%	40
B.1.19.5	O-11	xDSL (ADSL, HDSL and UCL)/TN (%)			95.65%	23				YES			96.43%	28
B.1.19.6	O-11	ISDN Loop (UDN, UDC)/TN (%)			100.00%	17				YES			95.65%	23
B.1.19.7	O-11	Line Sharing/TN (%)			80.00%	5				NO			85.71%	7

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			ne						May					
			Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.1.17.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/TN (%)				YES			96.77%	31				YES
B.1.17.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/TN (%)				YES			100.00%	4				YES
B.1.17.7.1	O-11	Line Sharing/EDI/TN (%)				NO			95.89%	73				YES
B.1.17.7.2	O-11	Line Sharing/TAG/TN (%)				NO			100.00%	8				YES
B.1.17.8.1	O-11	2W Analog Loop Design/EDI/TN (%)				NO			97.88%	519				YES
B.1.17.8.2	O-11	2W Analog Loop Design/TAG/TN (%)				NO			96.32%	136				YES
B.1.17.9.1	O-11	2W Analog Loop Non-Design/EDI/TN (%)												
B.1.17.9.2	O-11	2W Analog Loop Non-Design/TAG/TN (%)				NO			100.00%	43				YES
B.1.17.10.1	O-11	2W Analog Loop w/INP Design/EDI/TN (%)												
B.1.17.10.2	O-11	2W Analog Loop w/INP Design/TAG/TN (%)												
B.1.17.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/TN (%)												
B.1.17.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/TN (%)												
B.1.17.12.1	O-11	2W Analog Loop w/LNP Design/EDI/TN (%)				NO			85.71%	14				NO
B.1.17.12.2	O-11	2W Analog Loop w/LNP Design/TAG/TN (%)				NO			66.67%	6				NO
B.1.17.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/TN (%)												
B.1.17.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/TN (%)												
B.1.17.14.1	O-11	Other Design/EDI/TN (%)				NO			98.92%	93				YES
B.1.17.14.2	O-11	Other Design/TAG/TN (%)				YES			98.61%	72				YES
B.1.17.15.1	O-11	Other Non-Design/EDI/TN (%)				YES			100.00%	145				YES
B.1.17.15.2	O-11	Other Non-Design/TAG/TN (%)				YES			98.77%	163				YES
B.1.17.16.1	O-11	INP Standalone/EDI/TN (%)												
B.1.17.16.2	O-11	INP Standalone/TAG/TN (%)				YES								
B.1.17.17.1	O-11	LNP Standalone/EDI/TN (%)				NO			87.84%	74				NO
B.1.17.17.2	O-11	LNP Standalone/TAG/TN (%)				NO			59.26%	27				NO
		FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized												
B.1.18.1.1	O-11	Switch Ports/EDI/TN (%)												
B.1.18.1.2	O-11	Switch Ports/TAG/TN (%)												
B.1.18.2.1	O-11	Local Interoffice Transport/EDI/TN (%)												
B.1.18.2.2	O-11	Local Interoffice Transport/TAG/TN (%)												
B.1.18.3.1	O-11	Loop + Port Combinations/EDI/TN (%)				NO			97.77%	2,691				YES
B.1.18.3.2	O-11	Loop + Port Combinations/TAG/TN (%)				NO			88.88%	2,006				NO
B.1.18.4.1	O-11	Combo Other/EDI/TN (%)				YES								
B.1.18.4.2	O-11	Combo Other/TAG/TN (%)												
B.1.18.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/TN (%)				YES			95.00%	20				YES
B.1.18.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/TN (%)				YES			100.00%	5				YES
B.1.18.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/TN (%)				YES			100.00%	26				YES
B.1.18.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/TN (%)				YES			92.73%	55				NO
B.1.18.7.1	O-11	Line Sharing/EDI/TN (%)				NO			87.50%	8				NO
B.1.18.7.2	O-11	Line Sharing/TAG/TN (%)				YES			60.00%	5				NO
B.1.18.8.1	O-11	2W Analog Loop Design/EDI/TN (%)				NO			95.29%	85				YES
B.1.18.8.2	O-11	2W Analog Loop Design/TAG/TN (%)				YES			96.97%	33				YES
B.1.18.9.1	O-11	2W Analog Loop Non-Design/EDI/TN (%)												
B.1.18.9.2	O-11	2W Analog Loop Non-Design/TAG/TN (%)				NO			100.00%	2				YES
B.1.18.10.1	O-11	2W Analog Loop w/INP Design/EDI/TN (%)												
B.1.18.10.2	O-11	2W Analog Loop w/INP Design/TAG/TN (%)												
B.1.18.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/TN (%)												
B.1.18.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/TN (%)												
B.1.18.12.1	O-11	2W Analog Loop w/LNP Design/EDI/TN (%)				NO			47.06%	68				NO
B.1.18.12.2	O-11	2W Analog Loop w/LNP Design/TAG/TN (%)				NO			42.11%	76				NO
B.1.18.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/TN (%)												
B.1.18.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/TN (%)												
B.1.18.14.1	O-11	Other Design/EDI/TN (%)				NO			96.97%	33				YES
B.1.18.14.2	O-11	Other Design/TAG/TN (%)				YES			97.37%	38				YES
B.1.18.15.1	O-11	Other Non-Design/EDI/TN (%)				YES			100.00%	49				YES
B.1.18.15.2	O-11	Other Non-Design/TAG/TN (%)				YES			95.12%	82				YES
B.1.18.16.1	O-11	INP Standalone/EDI/TN (%)												
B.1.18.16.2	O-11	INP Standalone/TAG/TN (%)												
B.1.18.17.1	O-11	LNP Standalone/EDI/TN (%)				NO			77.57%	107				NO
B.1.18.17.2	O-11	LNP Standalone/TAG/TN (%)				NO			56.36%	55				NO
		FOC & Reject Response Completeness (Multiple Responses) - Non-Mechanized												
B.1.19.1	O-11	Switch Ports/TN (%)				YES								
B.1.19.2	O-11	Local Interoffice Transport/TN (%)							92.31%	26				NO
B.1.19.3	O-11	Loop + Port Combinations/TN (%)				YES			90.67%	225				NO
B.1.19.4	O-11	Combo Other/TN (%)				NO			85.71%	14				NO
B.1.19.5	O-11	xDSL (ADSL, HDSL and UCL)/TN (%)				YES			97.73%	44				YES
B.1.19.6	O-11	ISDN Loop (UDN, UDC)/TN (%)				YES			88.00%	25				NO
B.1.19.7	O-11	Line Sharing/TN (%)				NO			100.00%	9				YES

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			Benchmark/ Analog	August BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.1.19.8	O-11	2W Analog Loop Design/TN (%)	>= 95%			95.83%	24				YES
B.1.19.9	O-11	2W Analog Loop Non-Design/TN (%)	>= 95%			97.73%	44				YES
B.1.19.10	O-11	2W Analog Loop w/INP Design/TN (%)	>= 95%			100.00%	2				YES
B.1.19.11	O-11	2W Analog Loop w/INP Non-Design/TN (%)	>= 95%			50.00%	2				NO
B.1.19.12	O-11	2W Analog Loop w/LNP Design/TN (%)	>= 95%			100.00%	21				YES
B.1.19.13	O-11	2W Analog Loop w/LNP Non-Design/TN (%)	>= 95%			83.33%	6				NO
B.1.19.14	O-11	Other Design/TN (%)	>= 95%			90.22%	92				NO
B.1.19.15	O-11	Other Non-Design/TN (%)	>= 95%			97.41%	696				YES
B.1.19.16	O-11	INP Standalone/TN (%)	>= 95%			93.10%	29				NO
B.1.19.17	O-11	LNP Standalone/TN (%)	>= 95%			97.42%	426				YES
		Unbundled Network Elements - Provisioning									
		Order Completion Interval									
B.2.1.1.1.1	P-4	Switch Ports/<10 circuits/Dispatch/TN (days)	R&B (POTS)	6.01	16,886			10.165			
B.2.1.1.1.2	P-4	Switch Ports/<10 circuits/Non-Dispatch/TN (days)	R&B (POTS)	0.91	278,810			1.466			
B.2.1.1.2.1	P-4	Switch Ports/>=10 circuits/Dispatch/TN (days)	R&B (POTS)	11.45	68			14.884			
B.2.1.1.2.2	P-4	Switch Ports/>=10 circuits/Non-Dispatch/TN (days)	R&B (POTS)	1.42	4			1.751			
B.2.1.2.1.1	P-4	Local Interoffice Transport/<10 circuits/Dispatch/TN (days)	DS1/DS3	21.31	873			24.053			
B.2.1.2.1.2	P-4	Local Interoffice Transport/<10 circuits/Non-Dispatch/TN (days)	DS1/DS3								
B.2.1.2.2.1	P-4	Local Interoffice Transport/>=10 circuits/Dispatch/TN (days)	DS1/DS3								
B.2.1.2.2.2	P-4	Local Interoffice Transport/>=10 circuits/Non-Dispatch/TN (days)	DS1/DS3								
B.2.1.3.1.1	P-4	Loop + Port Combinations/<10 circuits/Dispatch/TN (days)	R&B	6.01	17,396	2.87	237	10.154	0.66405	4.7234	YES
B.2.1.3.1.2	P-4	Loop + Port Combinations/<10 circuits/Non-Dispatch/TN (days)	R&B	0.91	280,035	0.94	5,736	1.507	0.02010	-1.4385	YES
B.2.1.3.1.3	P-4	Loop + Port Combinations/<10 circuits/Switch Based Orders/TN (days)	R&B	0.33	141,024	0.33	2,726	0.000	0.00000		YES
B.2.1.3.1.4	P-4	Loop + Port Combinations/<10 circuits/Dispatch In/TN (days)	R&B	1.50	139,011	1.49	3,010	1.972	0.03633	0.2187	YES
B.2.1.3.2.1	P-4	Loop + Port Combinations/>=10 circuits/Dispatch/TN (days)	R&B	13.56	84	4.50	2	19.191	13.73080	0.6595	YES
B.2.1.3.2.2	P-4	Loop + Port Combinations/>=10 circuits/Non-Dispatch/TN (days)	R&B	7.01	65	4.00	1	16.580	16.70745	0.1802	YES
B.2.1.3.2.3	P-4	Loop + Port Combinations/>=10 circuits/Switch Based Orders/TN (days)	R&B	0.33	14			0.000			
B.2.1.3.2.4	P-4	Loop + Port Combinations/>=10 circuits/Dispatch In/TN (days)	R&B	8.84	51	4.00	1	18.330	18.50845	0.2617	YES
B.2.1.4.1.1	P-4	Combo Other/<10 circuits/Dispatch/TN (days)	R&B&D - Disp	7.29	18,774	11.38	24	12.311	2.51455	-1.6226	YES
B.2.1.4.1.4	P-4	Combo Other/<10 circuits/Dispatch In/TN (days)	R&B&D - Disp	7.29	18,774			12.311			
B.2.1.4.2.1	P-4	Combo Other/>=10 circuits/Dispatch/TN (days)	R&B&D - Disp	20.49	201			21.415			
B.2.1.4.2.4	P-4	Combo Other/>=10 circuits/Dispatch In/TN (days)	R&B&D - Disp	20.49	201			21.415			
B.2.1.6.3.1	P-4	UNE ISDN/<6 circuits/Dispatch/TN (days)	ISDN - BRI	13.56	281	11.37	27	9.737	1.96184	1.1161	YES
B.2.1.6.3.2	P-4	UNE ISDN/<6 circuits/Non-Dispatch/TN (days)	ISDN - BRI	2.61	372			3.743			
B.2.1.6.4.1	P-4	UNE ISDN/6-13 circuits/Dispatch/TN (days)	ISDN - BRI	11.00	2			4.243			
B.2.1.6.4.2	P-4	UNE ISDN/6-13 circuits/Non-Dispatch/TN (days)	ISDN - BRI	0.67	2			0.471			
B.2.1.6.5.1	P-4	UNE ISDN/>=14 circuits/Dispatch/TN (days)	ISDN - BRI								
B.2.1.6.5.2	P-4	UNE ISDN/>=14 circuits/Non-Dispatch/TN (days)	ISDN - BRI								
B.2.1.7.3.1	P-4	Line Sharing/<6 circuits/Dispatch/TN (days)	ADSL to Retail	2.70	1,556	3.00	3	2.966	1.71426	-0.1738	YES
B.2.1.7.3.2	P-4	Line Sharing/<6 circuits/Non-Dispatch/TN (days)	ADSL to Retail	2.32	2,421	2.24	49	0.635	0.09167	0.8610	YES
B.2.1.7.4.1	P-4	Line Sharing/6-13 circuits/Dispatch/TN (days)	ADSL to Retail	10.00	5			12.288			
B.2.1.7.4.2	P-4	Line Sharing/6-13 circuits/Non-Dispatch/TN (days)	ADSL to Retail	105.00	1			0.000			
B.2.1.7.5.1	P-4	Line Sharing/>=14 circuits/Dispatch/TN (days)	ADSL to Retail								
B.2.1.7.5.2	P-4	Line Sharing/>=14 circuits/Non-Dispatch/TN (days)	ADSL to Retail								
B.2.1.8.1.1	P-4	2W Analog Loop Design/<10 circuits/Dispatch/TN (days)	R&B - Disp	6.01	17,396	5.48	64	10.154	1.27159	0.4145	YES
B.2.1.8.1.2	P-4	2W Analog Loop Design/<10 circuits/Non-Dispatch/TN (days)	R&B - Disp	6.01	17,396			10.154			
B.2.1.8.2.1	P-4	2W Analog Loop Design/>=10 circuits/Dispatch/TN (days)	R&B - Disp	13.56	84			19.191			
B.2.1.8.2.2	P-4	2W Analog Loop Design/>=10 circuits/Non-Dispatch/TN (days)	R&B - Disp	13.56	84			19.191			
B.2.1.9.1.1	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch/TN (days)	R&B (POTS) excl SB Or	6.01	16,886	3.59	22	10.165	2.16852	1.1156	YES
B.2.1.9.1.4	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch In/TN (days)	R&B (POTS) excl SB Or	1.49	138,258	4.00	1	1.913	1.91305	-1.3117	YES
B.2.1.9.2.1	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch/TN (days)	R&B (POTS) excl SB Or	11.45	68			14.884			
B.2.1.9.2.4	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/TN (days)	R&B (POTS) excl SB Or	2.50	2			2.121			
B.2.1.10.1.1	P-4	2W Analog Loop w/INP Design/<10 circuits/Dispatch/TN (days)	R&B - Disp	6.01	17,396			10.154			
B.2.1.10.1.2	P-4	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/TN (days)	R&B - Disp	6.01	17,396			10.154			
B.2.1.10.2.1	P-4	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/TN (days)	R&B - Disp	13.56	84			19.191			
B.2.1.10.2.2	P-4	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/TN (days)	R&B - Disp	13.56	84			19.191			
B.2.1.11.1.1	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/TN (days)	R&B (POTS) excl SB Or	6.01	16,886			10.165			
B.2.1.11.1.4	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/TN (days)	R&B (POTS) excl SB Or	1.49	138,258			1.913			
B.2.1.11.2.1	P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/TN (days)	R&B (POTS) excl SB Or	11.45	68			14.884			
B.2.1.11.2.4	P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/TN (days)	R&B (POTS) excl SB Or	2.50	2			2.121			
B.2.1.12.1.1	P-4	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/TN (days)	R&B - Disp	6.01	17,396	5.00	25	10.154	2.03227	0.4977	YES
B.2.1.12.1.2	P-4	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/TN (days)	R&B - Disp	6.01	17,396			10.154			
B.2.1.12.2.1	P-4	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/TN (days)	R&B - Disp	13.56	84			19.191			
B.2.1.12.2.2	P-4	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/TN (days)	R&B - Disp	13.56	84			19.191			
B.2.1.13.1.1	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/TN (days)	R&B (POTS) excl SB Or	6.01	16,886			10.165			
B.2.1.13.1.4	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/TN (days)	R&B (POTS) excl SB Or	1.49	138,258			1.913			

BellSouth Monthly Performance Summary

Tennessee, August 2002 - M ay 2002

			July								Jun			
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.1.19.8	O-11	2W Analog Loop Design/TN (%)			92.45%	53				NO			95.74%	47
B.1.19.9	O-11	2W Analog Loop Non-Design/TN (%)			95.65%	46				YES			100.00%	37
B.1.19.10	O-11	2W Analog Loop w/INP Design/TN (%)												
B.1.19.11	O-11	2W Analog Loop w/INP Non-Design/TN (%)											100.00%	1
B.1.19.12	O-11	2W Analog Loop w/LNP Design/TN (%)			100.00%	16				YES			96.67%	30
B.1.19.13	O-11	2W Analog Loop w/LNP Non-Design/TN (%)			85.71%	7				NO				
B.1.19.14	O-11	Other Design/TN (%)			89.71%	68				NO			95.45%	132
B.1.19.15	O-11	Other Non-Design/TN (%)			96.49%	655				YES			96.07%	610
B.1.19.16	O-11	INP Standalone/TN (%)			100.00%	17				YES			100.00%	19
B.1.19.17	O-11	LNP Standalone/TN (%)			96.00%	400				YES			96.47%	397
		Unbundled Network Elements - Provisioning												
		Order Completion Interval												
B.2.1.1.1.1	P-4	Switch Ports/<10 circuits/Dispatch/TN (days)	5.69	15,350			5.549				5.46	13,930		
B.2.1.1.1.2	P-4	Switch Ports/<10 circuits/Non-Dispatch/TN (days)	0.91	261,763			1.309				0.89	241,005		
B.2.1.1.2.1	P-4	Switch Ports/>=10 circuits/Dispatch/TN (days)	9.06	59			10.273				19.63	60		
B.2.1.1.2.2	P-4	Switch Ports/>=10 circuits/Non-Dispatch/TN (days)	0.33	2			0.000							
B.2.1.2.1.1	P-4	Local Interoffice Transport/<10 circuits/Dispatch/TN (days)	23.98	1,019			34.159				17.01	793		
B.2.1.2.1.2	P-4	Local Interoffice Transport/<10 circuits/Non-Dispatch/TN (days)												
B.2.1.2.2.1	P-4	Local Interoffice Transport/>=10 circuits/Dispatch/TN (days)	11.00	1			0.000							
B.2.1.2.2.2	P-4	Local Interoffice Transport/>=10 circuits/Non-Dispatch/TN (days)												
B.2.1.3.1.1	P-4	Loop + Port Combinations/<10 circuits/Dispatch/TN (days)	5.72	15,677	3.14	222	5.850	0.39540	6.5331	YES	5.57	14,244	3.09	213
B.2.1.3.1.2	P-4	Loop + Port Combinations/<10 circuits/Non-Dispatch/TN (days)	0.91	263,943	0.70	4,435	1.367	0.02070	10.1207	YES	0.89	242,142	0.67	3,243
B.2.1.3.1.3	P-4	Loop + Port Combinations/<10 circuits/Switch Based Orders/TN (days)	0.33	133,261	0.33	2,791	0.000	0.00000		YES	0.33	123,077	0.33	2,088
B.2.1.3.1.4	P-4	Loop + Port Combinations/<10 circuits/Dispatch In/TN (days)	1.51	130,682	1.33	1,644	1.755	0.04356	3.9434	YES	1.46	119,065	1.29	1,155
B.2.1.3.2.1	P-4	Loop + Port Combinations/>=10 circuits/Dispatch/TN (days)	9.77	67	0.33	1	10.455	10.53238	0.8956	YES	21.18	103	2.67	3
B.2.1.3.2.2	P-4	Loop + Port Combinations/>=10 circuits/Non-Dispatch/TN (days)	6.08	44			15.675				6.44	35		
B.2.1.3.2.3	P-4	Loop + Port Combinations/>=10 circuits/Switch Based Orders/TN (days)	0.33	10			0.000				0.33	13		
B.2.1.3.2.4	P-4	Loop + Port Combinations/>=10 circuits/Dispatch In/TN (days)	7.76	34			17.528				10.05	22		
B.2.1.4.1.1	P-4	Combo Other/<10 circuits/Dispatch/TN (days)	7.24	17,260	11.67	27	10.073	1.94002	-2.2814	NO	7.06	15,679	11.89	28
B.2.1.4.1.4	P-4	Combo Other/<10 circuits/Dispatch In/TN (days)	7.24	17,260			10.073				7.06	15,679		
B.2.1.4.2.1	P-4	Combo Other/>=10 circuits/Dispatch/TN (days)	11.00	70			12.425				21.07	106		
B.2.1.4.2.4	P-4	Combo Other/>=10 circuits/Dispatch In/TN (days)	11.00	70			12.425				21.07	106		
B.2.1.6.3.1	P-4	UNE ISDN/<6 circuits/Dispatch/TN (days)	15.91	257	10.77	39	22.088	3.79583	1.3538	YES	14.65	250	10.96	49
B.2.1.6.3.2	P-4	UNE ISDN/<6 circuits/Non-Dispatch/TN (days)	1.98	435			6.495				1.44	352		
B.2.1.6.4.1	P-4	UNE ISDN/6-13 circuits/Dispatch/TN (days)	25.00	3			19.925							
B.2.1.6.4.2	P-4	UNE ISDN/6-13 circuits/Non-Dispatch/TN (days)												
B.2.1.6.5.1	P-4	UNE ISDN/>=14 circuits/Dispatch/TN (days)												
B.2.1.6.5.2	P-4	UNE ISDN/>=14 circuits/Non-Dispatch/TN (days)												
B.2.1.7.3.1	P-4	Line Sharing/<6 circuits/Dispatch/TN (days)	2.87	1,482	4.29	7	3.046	1.15403	-1.2266	YES	2.86	1,344	2.80	5
B.2.1.7.3.2	P-4	Line Sharing/<6 circuits/Non-Dispatch/TN (days)	2.45	2,159	3.77	47	0.690	0.10168	-12.9768	NO	2.49	2,035	3.70	37
B.2.1.7.4.1	P-4	Line Sharing/6-13 circuits/Dispatch/TN (days)	27.50	4			48.336				18.00	3		
B.2.1.7.4.2	P-4	Line Sharing/6-13 circuits/Non-Dispatch/TN (days)	53.00	5			27.019				35.29	7		
B.2.1.7.5.1	P-4	Line Sharing/>=14 circuits/Dispatch/TN (days)												
B.2.1.7.5.2	P-4	Line Sharing/>=14 circuits/Non-Dispatch/TN (days)												
B.2.1.8.1.1	P-4	2W Analog Loop Design/<10 circuits/Dispatch/TN (days)	5.72	15,677	5.04	68	5.850	0.71096	0.9483	YES	5.57	14,244	4.75	100
B.2.1.8.1.2	P-4	2W Analog Loop Design/<10 circuits/Non-Dispatch/TN (days)	5.72	15,677			5.850				5.57	14,244		
B.2.1.8.2.1	P-4	2W Analog Loop Design/>=10 circuits/Dispatch/TN (days)	9.77	67			10.455				21.18	103		
B.2.1.8.2.2	P-4	2W Analog Loop Design/>=10 circuits/Non-Dispatch/TN (days)	9.77	67			10.455				21.18	103		
B.2.1.9.1.1	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch/TN (days)	5.69	15,350	3.74	14	5.549	1.48370	1.3131	YES	5.46	13,930	4.08	12
B.2.1.9.1.4	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch In/TN (days)	1.50	128,971			1.670				1.46	118,420		
B.2.1.9.2.1	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch/TN (days)	9.06	59			10.273				19.63	60		
B.2.1.9.2.4	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/TN (days)												
B.2.1.10.1.1	P-4	2W Analog Loop w/INP Design/<10 circuits/Dispatch/TN (days)	5.72	15,677			5.850				5.57	14,244		
B.2.1.10.1.2	P-4	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/TN (days)	5.72	15,677			5.850				5.57	14,244		
B.2.1.10.2.1	P-4	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/TN (days)	9.77	67			10.455				21.18	103		
B.2.1.10.2.2	P-4	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/TN (days)	9.77	67			10.455				21.18	103		
B.2.1.11.1.1	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/TN (days)	5.69	15,350			5.549				5.46	13,930		
B.2.1.11.1.4	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/TN (days)	1.50	128,971			1.670				1.46	118,420		
B.2.1.11.2.1	P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/TN (days)	9.06	59			10.273				19.63	60		
B.2.1.11.2.4	P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/TN (days)												
B.2.1.12.1.1	P-4	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/TN (days)	5.72	15,677	5.80	30	5.850	1.06909	-0.0764	YES	5.57	14,244	5.00	41
B.2.1.12.1.2	P-4	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/TN (days)	5.72	15,677			5.850				5.57	14,244		
B.2.1.12.2.1	P-4	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/TN (days)	9.77	67			10.455				21.18	103		
B.2.1.12.2.2	P-4	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/TN (days)	9.77	67			10.455				21.18	103		
B.2.1.13.1.1	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/TN (days)	5.69	15,350			5.549				5.46	13,930		
B.2.1.13.1.4	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/TN (days)	1.50	128,971	4.00	1	1.670	1.67036	-1.4969	YES	1.46	118,420		

BellSouth Monthly Performance Summary

Tennessee, August 2002 - May 2002

			ne				May							
			Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.1.19.8	O-11	2W Analog Loop Design/TN (%)				YES			88.00%	50				NO
B.1.19.9	O-11	2W Analog Loop Non-Design/TN (%)				YES			96.61%	59				YES
B.1.19.10	O-11	2W Analog Loop w/INP Design/TN (%)												
B.1.19.11	O-11	2W Analog Loop w/INP Non-Design/TN (%)				YES								
B.1.19.12	O-11	2W Analog Loop w/LNP Design/TN (%)				YES			89.74%	39				NO
B.1.19.13	O-11	2W Analog Loop w/LNP Non-Design/TN (%)												
B.1.19.14	O-11	Other Design/TN (%)				YES			90.06%	171				NO
B.1.19.15	O-11	Other Non-Design/TN (%)				YES			96.58%	730				YES
B.1.19.16	O-11	INP Standalone/TN (%)				YES			100.00%	14				YES
B.1.19.17	O-11	LNP Standalone/TN (%)				YES			95.72%	327				YES
		Unbundled Network Elements - Provisioning												
		Order Completion Interval												
B.2.1.1.1.1	P-4	Switch Ports/<10 circuits/Dispatch/TN (days)	6.355				4.71	17,710			6.578			
B.2.1.1.1.2	P-4	Switch Ports/<10 circuits/Non-Dispatch/TN (days)	1.179				0.89	257,954			2.094			
B.2.1.1.2.1	P-4	Switch Ports/>=10 circuits/Dispatch/TN (days)	40.163				10.76	84			12.615			
B.2.1.1.2.2	P-4	Switch Ports/>=10 circuits/Non-Dispatch/TN (days)					0.73	10			0.344			
B.2.1.2.1.1	P-4	Local Interoffice Transport/<10 circuits/Dispatch/TN (days)	14.655				18.13	888	11.00	4	13.670	6.85058	1.0412	YES
B.2.1.2.1.2	P-4	Local Interoffice Transport/<10 circuits/Non-Dispatch/TN (days)												
B.2.1.2.2.1	P-4	Local Interoffice Transport/>=10 circuits/Dispatch/TN (days)					12.00	1			0.000			
B.2.1.2.2.2	P-4	Local Interoffice Transport/>=10 circuits/Non-Dispatch/TN (days)												
B.2.1.3.1.1	P-4	Loop + Port Combinations/<10 circuits/Dispatch/TN (days)	6.733	0.46479	5.3410	YES	4.75	17,995	2.96	225	6.639	0.44539	4.0188	YES
B.2.1.3.1.2	P-4	Loop + Port Combinations/<10 circuits/Non-Dispatch/TN (days)	1.216	0.02149	10.0013	YES	0.90	259,194	0.64	3,704	2.127	0.03519	7.4809	YES
B.2.1.3.1.3	P-4	Loop + Port Combinations/<10 circuits/Switch Based Orders/TN (days)	0.000	0.00000		YES	0.33	132,607	0.33	2,479	0.000	0.00000		YES
B.2.1.3.1.4	P-4	Loop + Port Combinations/<10 circuits/Dispatch In/TN (days)	1.535	0.04539	3.8148	YES	1.49	126,587	1.25	1,225	2.928	0.08405	2.8931	YES
B.2.1.3.2.1	P-4	Loop + Port Combinations/>=10 circuits/Dispatch/TN (days)	32.231	18.87746	0.9809	YES	11.74	95	3.50	6	13.376	5.63056	1.4629	YES
B.2.1.3.2.2	P-4	Loop + Port Combinations/>=10 circuits/Non-Dispatch/TN (days)	12.836				4.49	58	3.00	2	7.522	5.40948	0.2762	YES
B.2.1.3.2.3	P-4	Loop + Port Combinations/>=10 circuits/Switch Based Orders/TN (days)	0.000				0.33	11			0.000			
B.2.1.3.2.4	P-4	Loop + Port Combinations/>=10 circuits/Dispatch In/TN (days)	15.167				5.47	47	3.00	2	8.062	5.82062	0.4240	YES
B.2.1.4.1.1	P-4	Combo Other/<10 circuits/Dispatch/TN (days)	10.370	1.96145	-2.4618	NO	5.91	19,372	11.30	27	10.701	2.06091	-2.6121	NO
B.2.1.4.1.4	P-4	Combo Other/<10 circuits/Dispatch In/TN (days)	10.370				5.91	19,372			10.701			
B.2.1.4.2.1	P-4	Combo Other/>=10 circuits/Dispatch/TN (days)	31.840				11.76	96			13.307			
B.2.1.4.2.4	P-4	Combo Other/>=10 circuits/Dispatch In/TN (days)	31.840				11.76	96			13.307			
B.2.1.6.3.1	P-4	UNE ISDN/<6 circuits/Dispatch/TN (days)	24.516	3.83022	0.9627	YES	13.95	294	10.70	54	11.481	1.69983	1.9112	YES
B.2.1.6.3.2	P-4	UNE ISDN/<6 circuits/Non-Dispatch/TN (days)	2.825				1.49	327			3.190			
B.2.1.6.4.1	P-4	UNE ISDN/6-13 circuits/Dispatch/TN (days)												
B.2.1.6.4.2	P-4	UNE ISDN/6-13 circuits/Non-Dispatch/TN (days)												
B.2.1.6.5.1	P-4	UNE ISDN/>=14 circuits/Dispatch/TN (days)												
B.2.1.6.5.2	P-4	UNE ISDN/>=14 circuits/Non-Dispatch/TN (days)												
B.2.1.7.3.1	P-4	Line Sharing/<6 circuits/Dispatch/TN (days)	2.223	0.99612	0.0643	YES	4.04	1,063	4.20	5	2.905	1.30214	-0.1261	YES
B.2.1.7.3.2	P-4	Line Sharing/<6 circuits/Non-Dispatch/TN (days)	1.775	0.29438	-4.1292	NO	3.66	1,592	3.98	43	0.808	0.12488	-2.4997	NO
B.2.1.7.4.1	P-4	Line Sharing/6-13 circuits/Dispatch/TN (days)	25.120				12.40	5			7.266			
B.2.1.7.4.2	P-4	Line Sharing/6-13 circuits/Non-Dispatch/TN (days)	19.593				12.60	5			11.149			
B.2.1.7.5.1	P-4	Line Sharing/>=14 circuits/Dispatch/TN (days)												
B.2.1.7.5.2	P-4	Line Sharing/>=14 circuits/Non-Dispatch/TN (days)												
B.2.1.8.1.1	P-4	2W Analog Loop Design/<10 circuits/Dispatch/TN (days)	6.733	0.67568	1.2160	YES	4.75	17,995	4.60	112	6.639	0.62932	0.2411	YES
B.2.1.8.1.2	P-4	2W Analog Loop Design/<10 circuits/Non-Dispatch/TN (days)	6.733				4.75	17,995			6.639			
B.2.1.8.2.1	P-4	2W Analog Loop Design/>=10 circuits/Dispatch/TN (days)	32.231				11.74	95			13.376			
B.2.1.8.2.2	P-4	2W Analog Loop Design/>=10 circuits/Non-Dispatch/TN (days)	32.231				11.74	95			13.376			
B.2.1.9.1.1	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch/TN (days)	6.355	1.83532	0.7517	YES	4.71	17,710	4.50	2	6.578	4.65158	0.0451	YES
B.2.1.9.1.4	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch In/TN (days)	1.479				1.48	125,846			2.883			
B.2.1.9.2.1	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch/TN (days)	40.163				10.76	84			12.615			
B.2.1.9.2.4	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/TN (days)					1.00	6			0.000			
B.2.1.10.1.1	P-4	2W Analog Loop w/INP Design/<10 circuits/Dispatch/TN (days)	6.733				4.75	17,995			6.639			
B.2.1.10.1.2	P-4	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/TN (days)	6.733				4.75	17,995			6.639			
B.2.1.10.2.1	P-4	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/TN (days)	32.231				11.74	95			13.376			
B.2.1.10.2.2	P-4	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/TN (days)	32.231				11.74	95			13.376			
B.2.1.11.1.1	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/TN (days)	6.355				4.71	17,710			6.578			
B.2.1.11.1.4	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/TN (days)	1.479				1.48	125,846			2.883			
B.2.1.11.2.1	P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/TN (days)	40.163				10.76	84			12.615			
B.2.1.11.2.4	P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/TN (days)					1.00	6			0.000			
B.2.1.12.1.1	P-4	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/TN (days)	6.733	1.05306	0.5428	YES	4.75	17,995	5.59	46	6.639	0.98018	-0.8509	YES
B.2.1.12.1.2	P-4	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/TN (days)	6.733				4.75	17,995			6.639			
B.2.1.12.2.1	P-4	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/TN (days)	32.231				11.74	95			13.376			
B.2.1.12.2.2	P-4	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/TN (days)	32.231				11.74	95			13.376			
B.2.1.13.1.1	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/TN (days)	6.355				4.71	17,710			6.578			
B.2.1.13.1.4	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/TN (days)	1.479				1.48	125,846			2.883			

BellSouth Monthly Performance Summary

Tennessee, August 2002 - M ay 2002

			Benchmark/ Analog	August BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.1.13.2.1	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/TN (days)	R&B (POTS) excl SB Or	11.45	68			14.884			
B.2.1.13.2.4	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/TN (days)	R&B (POTS) excl SB Or	2.50	2			2.121			
B.2.1.14.1.1	P-4	Other Design/<10 circuits/Dispatch/TN (days)	Design	23.50	1,378			21.914			
B.2.1.14.1.2	P-4	Other Design/<10 circuits/Non-Dispatch/TN (days)	Design	13.32	220			9.648			
B.2.1.14.2.1	P-4	Other Design/>=10 circuits/Dispatch/TN (days)	Design	25.46	117			21.620			
B.2.1.14.2.2	P-4	Other Design/>=10 circuits/Non-Dispatch/TN (days)	Design	8.25	12			6.032			
B.2.1.15.1.1	P-4	Other Non-Design/<10 circuits/Dispatch/TN (days)	R&B	6.01	17,396			10.154			
B.2.1.15.1.2	P-4	Other Non-Design/<10 circuits/Non-Dispatch/TN (days)	R&B	0.91	280,035			1.507			
B.2.1.15.2.1	P-4	Other Non-Design/>=10 circuits/Dispatch/TN (days)	R&B	13.56	84			19.191			
B.2.1.15.2.2	P-4	Other Non-Design/>=10 circuits/Non-Dispatch/TN (days)	R&B	7.01	65			16.580			
B.2.1.16.1.1	P-4	INP (Standalone)/<10 circuits/Dispatch/TN (days)	R&B (POTS)	6.01	16,886	0.33	3	10.165	5.86908	0.9672	YES
B.2.1.16.1.2	P-4	INP (Standalone)/<10 circuits/Non-Dispatch/TN (days)	R&B (POTS)	0.91	278,810	1.11	3	1.466	0.84650	-0.2408	YES
B.2.1.16.2.1	P-4	INP (Standalone)/>=10 circuits/Dispatch/TN (days)	R&B (POTS)	11.45	68			14.884			
B.2.1.16.2.2	P-4	INP (Standalone)/>=10 circuits/Non-Dispatch/TN (days)	R&B (POTS)	1.42	4			1.751			
B.2.1.17.1.1	P-4	LNP (Standalone)/<10 circuits/Dispatch/TN (days)	R&B (POTS)	6.01	16,886	0.33	2	10.165	7.18792	0.7898	YES
B.2.1.17.1.2	P-4	LNP (Standalone)/<10 circuits/Non-Dispatch/TN (days)	R&B (POTS)	0.91	278,810	0.50	487	1.466	0.06650	6.0684	YES
B.2.1.17.2.1	P-4	LNP (Standalone)/>=10 circuits/Dispatch/TN (days)	R&B (POTS)	11.45	68			14.884			
B.2.1.17.2.2	P-4	LNP (Standalone)/>=10 circuits/Non-Dispatch/TN (days)	R&B (POTS)	1.42	4	0.33	2	1.751	1.51612	0.7145	YES
B.2.1.18.1.1	P-4	Digital Loop < DS1/<10 circuits/Dispatch/TN (days)	Digital Loop < DS1	5.44	2,166	7.27	82	7.795	0.87695	-2.0880	NO
B.2.1.18.1.2	P-4	Digital Loop < DS1/<10 circuits/Non-Dispatch/TN (days)	Digital Loop < DS1	2.71	3,076			2.884			
B.2.1.18.2.1	P-4	Digital Loop < DS1/>=10 circuits/Dispatch/TN (days)	Digital Loop < DS1	4.75	4			4.193			
B.2.1.18.2.2	P-4	Digital Loop < DS1/>=10 circuits/Non-Dispatch/TN (days)	Digital Loop < DS1	105.00	1			0.000			
B.2.1.19.1.1	P-4	Digital Loop >= DS1/<10 circuits/Dispatch/TN (days)	Digital Loop >= DS1	19.39	252	6.37	142	22.181	2.32747	5.5969	YES
B.2.1.19.1.2	P-4	Digital Loop >= DS1/<10 circuits/Non-Dispatch/TN (days)	Digital Loop >= DS1	9.64	136			12.364			
B.2.1.19.2.1	P-4	Digital Loop >= DS1/>=10 circuits/Dispatch/TN (days)	Digital Loop >= DS1	32.19	43			22.949			
B.2.1.19.2.2	P-4	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/TN (days)	Digital Loop >= DS1	10.99	29			15.053			
		Order Completion Interval within X days									
B.2.2.1	P-4	xDSL (ADSL, HDSL and UCL) Loop with Conditioning/<6 circuits/Dispatch/TN (days)	14 days								
B.2.2.2	P-4	xDSL (ADSL, HDSL and UCL) Loop w/o Conditioning/<6 circuits/Dispatch/TN (days)	7 days			5.31	59				YES
		Held Orders									
B.2.3.1.1.1	P-1	Switch Ports/<10 circuits/Facility/TN (days)	R&B (POTS)	7.59	126			5.717			
B.2.3.1.1.2	P-1	Switch Ports/<10 circuits/Equipment/TN (days)	R&B (POTS)	0.00	0						
B.2.3.1.1.3	P-1	Switch Ports/<10 circuits/Other/TN (days)	R&B (POTS)	16.42	19			21.070			
B.2.3.1.2.1	P-1	Switch Ports/>=10 circuits/Facility/TN (days)	R&B (POTS)	0.00	0						
B.2.3.1.2.2	P-1	Switch Ports/>=10 circuits/Equipment/TN (days)	R&B (POTS)	0.00	0						
B.2.3.1.2.3	P-1	Switch Ports/>=10 circuits/Other/TN (days)	R&B (POTS)	0.00	0						
B.2.3.2.1.1	P-1	Local Interoffice Transport/<10 circuits/Facility/TN (days)	DS1/ DS3 - Interoffice	0.00	0						
B.2.3.2.1.2	P-1	Local Interoffice Transport/<10 circuits/Equipment/TN (days)	DS1/ DS3 - Interoffice	0.00	0						
B.2.3.2.1.3	P-1	Local Interoffice Transport/<10 circuits/Other/TN (days)	DS1/ DS3 - Interoffice	7.67	3			8.145			
B.2.3.2.2.1	P-1	Local Interoffice Transport/>=10 circuits/Facility/TN (days)	DS1/ DS3 - Interoffice								
B.2.3.2.2.2	P-1	Local Interoffice Transport/>=10 circuits/Equipment/TN (days)	DS1/ DS3 - Interoffice								
B.2.3.2.2.3	P-1	Local Interoffice Transport/>=10 circuits/Other/TN (days)	DS1/ DS3 - Interoffice								
B.2.3.3.1.1	P-1	Loop + Port Combinations/<10 circuits/Facility/TN (days)	R&B	7.59	128	11.00	1	5.717	5.73929	-0.5935	YES
B.2.3.3.1.2	P-1	Loop + Port Combinations/<10 circuits/Equipment/TN (days)	R&B	0.00	0	0.00	0				YES
B.2.3.3.1.3	P-1	Loop + Port Combinations/<10 circuits/Other/TN (days)	R&B	16.42	19	6.00	1	21.070	21.61685	0.4821	YES
B.2.3.3.2.1	P-1	Loop + Port Combinations/>=10 circuits/Facility/TN (days)	R&B	0.00	0	0.00	0				YES
B.2.3.3.2.2	P-1	Loop + Port Combinations/>=10 circuits/Equipment/TN (days)	R&B	0.00	0	0.00	0				YES
B.2.3.3.2.3	P-1	Loop + Port Combinations/>=10 circuits/Other/TN (days)	R&B	0.00	0	0.00	0				YES
B.2.3.4.1.1	P-1	Combo Other/<10 circuits/Facility/TN (days)	R&B&D - Disp	7.73	134	0.00	0	5.956			YES
B.2.3.4.1.2	P-1	Combo Other/<10 circuits/Equipment/TN (days)	R&B&D - Disp	0.00	0	0.00	0				YES
B.2.3.4.1.3	P-1	Combo Other/<10 circuits/Other/TN (days)	R&B&D - Disp	15.90	20	0.00	0	20.640			YES
B.2.3.4.2.1	P-1	Combo Other/>=10 circuits/Facility/TN (days)	R&B&D - Disp	0.00	0						
B.2.3.4.2.2	P-1	Combo Other/>=10 circuits/Equipment/TN (days)	R&B&D - Disp	0.00	0						
B.2.3.4.2.3	P-1	Combo Other/>=10 circuits/Other/TN (days)	R&B&D - Disp	0.00	0						
B.2.3.5.1.1	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Facility/TN (days)	ADSL to Retail	6.22	9	0.00	0	6.610			YES
B.2.3.5.1.2	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Equipment/TN (days)	ADSL to Retail	0.00	0	0.00	0				YES
B.2.3.5.1.3	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Other/TN (days)	ADSL to Retail	3.00	1	0.00	0	0.000			YES
B.2.3.5.2.1	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Facility/TN (days)	ADSL to Retail	0.00	0						
B.2.3.5.2.2	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Equipment/TN (days)	ADSL to Retail	0.00	0						
B.2.3.5.2.3	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Other/TN (days)	ADSL to Retail	0.00	0						
B.2.3.6.1.1	P-1	UNE ISDN/<10 circuits/Facility/TN (days)	ISDN - BRI	11.00	5	0.00	0	11.292			YES
B.2.3.6.1.2	P-1	UNE ISDN/<10 circuits/Equipment/TN (days)	ISDN - BRI	0.00	0	0.00	0				YES
B.2.3.6.1.3	P-1	UNE ISDN/<10 circuits/Other/TN (days)	ISDN - BRI	0.00	0	0.00	0				YES
B.2.3.6.2.1	P-1	UNE ISDN/>=10 circuits/Facility/TN (days)	ISDN - BRI								
B.2.3.6.2.2	P-1	UNE ISDN/>=10 circuits/Equipment/TN (days)	ISDN - BRI								
B.2.3.6.2.3	P-1	UNE ISDN/>=10 circuits/Other/TN (days)	ISDN - BRI								
B.2.3.7.1.1	P-1	Line Sharing/<10 circuits/Facility/TN (days)	ADSL to Retail	6.22	9	0.00	0	6.610			YES

BellSouth Monthly Performance Summary

Tennessee, August 2002 - M ay 2002

			July								Jur			
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.2.1.13.2.1	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/TN (days)	9.06	59			10.273				19.63	60		
B.2.1.13.2.4	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/TN (days)												
B.2.1.14.1.1	P-4	Other Design/<10 circuits/Dispatch/TN (days)	22.32	1,583			22.745				21.88	1,435		
B.2.1.14.1.2	P-4	Other Design/<10 circuits/Non-Dispatch/TN (days)	13.12	211			17.211				6.82	375		
B.2.1.14.2.1	P-4	Other Design/>=10 circuits/Dispatch/TN (days)	38.67	3			22.811				17.00	3		
B.2.1.14.2.2	P-4	Other Design/>=10 circuits/Non-Dispatch/TN (days)												
B.2.1.15.1.1	P-4	Other Non-Design/<10 circuits/Dispatch/TN (days)	5.72	15,677			5.850				5.57	14,244		
B.2.1.15.1.2	P-4	Other Non-Design/<10 circuits/Non-Dispatch/TN (days)	0.91	263,943			1.367				0.89	242,142		
B.2.1.15.2.1	P-4	Other Non-Design/>=10 circuits/Dispatch/TN (days)	9.77	67			10.455				21.18	103		
B.2.1.15.2.2	P-4	Other Non-Design/>=10 circuits/Non-Dispatch/TN (days)	6.08	44			15.675				6.44	35		
B.2.1.16.1.1	P-4	INP (Standalone)/<10 circuits/Dispatch/TN (days)	5.69	15,350			5.549				5.46	13,930		
B.2.1.16.1.2	P-4	INP (Standalone)/<10 circuits/Non-Dispatch/TN (days)	0.91	261,763			1.309				0.89	241,005		
B.2.1.16.2.1	P-4	INP (Standalone)/>=10 circuits/Dispatch/TN (days)	9.06	59			10.273				19.63	60		
B.2.1.16.2.2	P-4	INP (Standalone)/>=10 circuits/Non-Dispatch/TN (days)	0.33	2			0.000							
B.2.1.17.1.1	P-4	LNP (Standalone)/<10 circuits/Dispatch/TN (days)	5.69	15,350	1.00	1	5.549	5.54915	0.8445	YES	5.46	13,930		
B.2.1.17.1.2	P-4	LNP (Standalone)/<10 circuits/Non-Dispatch/TN (days)	0.91	261,763	0.52	462	1.309	0.06098	6.4190	YES	0.89	241,005	0.49	445
B.2.1.17.2.1	P-4	LNP (Standalone)/>=10 circuits/Dispatch/TN (days)	9.06	59			10.273				19.63	60		
B.2.1.17.2.2	P-4	LNP (Standalone)/>=10 circuits/Non-Dispatch/TN (days)	0.33	2	0.56	3	0.000	0.00000		NO			0.67	2
B.2.1.18.1.1	P-4	Digital Loop < DS1/<10 circuits/Dispatch/TN (days)	5.71	1,879	6.97	97	11.096	1.15530	-1.0905	YES	6.50	1,806	7.23	119
B.2.1.18.1.2	P-4	Digital Loop < DS1/<10 circuits/Non-Dispatch/TN (days)	2.72	2,866			5.217				2.81	2,784		
B.2.1.18.2.1	P-4	Digital Loop < DS1/>=10 circuits/Dispatch/TN (days)	6.00	4			5.354				3.50	2		
B.2.1.18.2.2	P-4	Digital Loop < DS1/>=10 circuits/Non-Dispatch/TN (days)	66.50	2			0.707				20.50	2		
B.2.1.19.1.1	P-4	Digital Loop >= DS1/<10 circuits/Dispatch/TN (days)	17.35	386	6.60	124	19.009	1.96219	5.4796	YES	18.83	325	6.47	187
B.2.1.19.1.2	P-4	Digital Loop >= DS1/<10 circuits/Non-Dispatch/TN (days)	6.91	219			12.286				2.26	261		
B.2.1.19.2.1	P-4	Digital Loop >= DS1/>=10 circuits/Dispatch/TN (days)	43.00	1			0.000							
B.2.1.19.2.2	P-4	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/TN (days)	4.81	14			5.773				6.00	7		
		Order Completion Interval within X days												
B.2.2.1	P-4	xDSL (ADSL, HDSL and UCL) Loop with Conditioning/<6 circuits/Dispatch/TN (days)												
B.2.2.2	P-4	xDSL (ADSL, HDSL and UCL) Loop w/o Conditioning/<6 circuits/Dispatch/TN (days)			4.52	63				YES			4.74	82
		Held Orders												
B.2.3.1.1.1	P-1	Switch Ports/<10 circuits/Facility/TN (days)	8.12	98			7.839				8.73	104		
B.2.3.1.1.2	P-1	Switch Ports/<10 circuits/Equipment/TN (days)	0.00	0							0.00	0		
B.2.3.1.1.3	P-1	Switch Ports/<10 circuits/Other/TN (days)	15.74	19			16.918				12.52	23		
B.2.3.1.2.1	P-1	Switch Ports/>=10 circuits/Facility/TN (days)	0.00	0							0.00	0		
B.2.3.1.2.2	P-1	Switch Ports/>=10 circuits/Equipment/TN (days)	0.00	0							0.00	0		
B.2.3.1.2.3	P-1	Switch Ports/>=10 circuits/Other/TN (days)	0.00	0							0.00	0		
B.2.3.2.1.1	P-1	Local Interoffice Transport/<10 circuits/Facility/TN (days)	1.00	1			0.000				0.00	0		
B.2.3.2.1.2	P-1	Local Interoffice Transport/<10 circuits/Equipment/TN (days)	0.00	0							0.00	0		
B.2.3.2.1.3	P-1	Local Interoffice Transport/<10 circuits/Other/TN (days)	5.00	2			5.657				5.00	1		
B.2.3.2.2.1	P-1	Local Interoffice Transport/>=10 circuits/Facility/TN (days)	0.00	0										
B.2.3.2.2.2	P-1	Local Interoffice Transport/>=10 circuits/Equipment/TN (days)	0.00	0										
B.2.3.2.2.3	P-1	Local Interoffice Transport/>=10 circuits/Other/TN (days)	0.00	0										
B.2.3.3.1.1	P-1	Loop + Port Combinations/<10 circuits/Facility/TN (days)	8.12	98	0.00	0	7.839			YES	8.74	105	0.00	0
B.2.3.3.1.2	P-1	Loop + Port Combinations/<10 circuits/Equipment/TN (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.3.3.1.3	P-1	Loop + Port Combinations/<10 circuits/Other/TN (days)	15.74	19	13.00	1	16.918	17.35708	0.1577	YES	12.52	23	0.00	0
B.2.3.3.2.1	P-1	Loop + Port Combinations/>=10 circuits/Facility/TN (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.3.3.2.2	P-1	Loop + Port Combinations/>=10 circuits/Equipment/TN (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.3.3.2.3	P-1	Loop + Port Combinations/>=10 circuits/Other/TN (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.3.4.1.1	P-1	Combo Other/<10 circuits/Facility/TN (days)	8.76	102	0.00	0	8.897			YES	9.28	116	11.00	1
B.2.3.4.1.2	P-1	Combo Other/<10 circuits/Equipment/TN (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.3.4.1.3	P-1	Combo Other/<10 circuits/Other/TN (days)	14.75	24	0.00	0	15.419			YES	12.17	24	0.00	0
B.2.3.4.2.1	P-1	Combo Other/>=10 circuits/Facility/TN (days)	0.00	0							0.00	0		
B.2.3.4.2.2	P-1	Combo Other/>=10 circuits/Equipment/TN (days)	0.00	0							0.00	0		
B.2.3.4.2.3	P-1	Combo Other/>=10 circuits/Other/TN (days)	0.00	0							0.00	0		
B.2.3.5.1.1	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Facility/TN (days)	5.83	6	13.00	1	3.061	3.30572	-2.1680	NO	26.75	4	6.00	1
B.2.3.5.1.2	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Equipment/TN (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.3.5.1.3	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Other/TN (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.3.5.2.1	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Facility/TN (days)	0.00	0							0.00	0		
B.2.3.5.2.2	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Equipment/TN (days)	0.00	0							0.00	0		
B.2.3.5.2.3	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Other/TN (days)	0.00	0							0.00	0		
B.2.3.6.1.1	P-1	UNE ISDN/<10 circuits/Facility/TN (days)	29.33	3	6.00	1	19.009	21.94944	1.0630	YES	19.00	6	0.00	0
B.2.3.6.1.2	P-1	UNE ISDN/<10 circuits/Equipment/TN (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.3.6.1.3	P-1	UNE ISDN/<10 circuits/Other/TN (days)	2.00	1	0.00	0	0.000			YES	0.00	0	0.00	0
B.2.3.6.2.1	P-1	UNE ISDN/>=10 circuits/Facility/TN (days)	0.00	0										
B.2.3.6.2.2	P-1	UNE ISDN/>=10 circuits/Equipment/TN (days)	0.00	0										
B.2.3.6.2.3	P-1	UNE ISDN/>=10 circuits/Other/TN (days)	0.00	0										
B.2.3.7.1.1	P-1	Line Sharing/<10 circuits/Facility/TN (days)	5.83	6	0.00	0	3.061			YES	26.75	4	0.00	0

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			ne				May							
			Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.1.13.2.1	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/TN (days)	40.163				10.76	84			12.615			
B.2.1.13.2.4	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/TN (days)					1.00	6			0.000			
B.2.1.14.1.1	P-4	Other Design/<10 circuits/Dispatch/TN (days)	21.991				21.07	1,377			28.073			
B.2.1.14.1.2	P-4	Other Design/<10 circuits/Non-Dispatch/TN (days)	9.554				6.18	250			7.503			
B.2.1.14.2.1	P-4	Other Design/>=10 circuits/Dispatch/TN (days)	14.731				14.00	1			0.000			
B.2.1.14.2.2	P-4	Other Design/>=10 circuits/Non-Dispatch/TN (days)					1.00	1			0.000			
B.2.1.15.1.1	P-4	Other Non-Design/<10 circuits/Dispatch/TN (days)	6.733				4.75	17,995	6.00	1	6.639	6.63960	-0.1878	YES
B.2.1.15.1.2	P-4	Other Non-Design/<10 circuits/Non-Dispatch/TN (days)	1.216				0.90	259,194			2.127			
B.2.1.15.2.1	P-4	Other Non-Design/>=10 circuits/Dispatch/TN (days)	32.231				11.74	95			13.376			
B.2.1.15.2.2	P-4	Other Non-Design/>=10 circuits/Non-Dispatch/TN (days)	12.836				4.49	58			7.522			
B.2.1.16.1.1	P-4	INP (Standalone)/<10 circuits/Dispatch/TN (days)	6.355				4.71	17,710			6.578			
B.2.1.16.1.2	P-4	INP (Standalone)/<10 circuits/Non-Dispatch/TN (days)	1.179				0.89	257,954			2.094			
B.2.1.16.2.1	P-4	INP (Standalone)/>=10 circuits/Dispatch/TN (days)	40.163				10.76	84			12.615			
B.2.1.16.2.2	P-4	INP (Standalone)/>=10 circuits/Non-Dispatch/TN (days)					0.73	10			0.344			
B.2.1.17.1.1	P-4	LNP (Standalone)/<10 circuits/Dispatch/TN (days)	6.355				4.71	17,710			6.578			
B.2.1.17.1.2	P-4	LNP (Standalone)/<10 circuits/Non-Dispatch/TN (days)	1.179	0.05593	7.0418	YES	0.89	257,954	0.51	342	2.094	0.11329	3.4115	YES
B.2.1.17.2.1	P-4	LNP (Standalone)/>=10 circuits/Dispatch/TN (days)	40.163				10.76	84			12.615			
B.2.1.17.2.2	P-4	LNP (Standalone)/>=10 circuits/Non-Dispatch/TN (days)					0.73	10	0.33	2	0.344	0.26667	1.5000	YES
B.2.1.18.1.1	P-4	Digital Loop < DS1/<10 circuits/Dispatch/TN (days)	12.045	1.13996	-0.6334	YES	6.92	1,552	7.20	120	7.687	0.72832	-0.3902	YES
B.2.1.18.1.2	P-4	Digital Loop < DS1/<10 circuits/Non-Dispatch/TN (days)	4.269				3.29	2,288			2.510			
B.2.1.18.2.1	P-4	Digital Loop < DS1/>=10 circuits/Dispatch/TN (days)	0.707				4.00	1			0.000			
B.2.1.18.2.2	P-4	Digital Loop < DS1/>=10 circuits/Non-Dispatch/TN (days)	19.092				4.00	2			4.243			
B.2.1.19.1.1	P-4	Digital Loop >= DS1/<10 circuits/Dispatch/TN (days)	19.184	1.76076	7.0199	YES	19.05	202	6.10	115	18.246	2.13147	6.0797	YES
B.2.1.19.1.2	P-4	Digital Loop >= DS1/<10 circuits/Non-Dispatch/TN (days)	5.226				4.98	130			11.243			
B.2.1.19.2.1	P-4	Digital Loop >= DS1/>=10 circuits/Dispatch/TN (days)												
B.2.1.19.2.2	P-4	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/TN (days)	8.280				7.71	15			10.594			
		Order Completion Interval within X days												
B.2.2.1	P-4	xDSL (ADSL, HDSL and UCL) Loop with Conditioning/<6 circuits/Dispatch/TN (days)												
B.2.2.2	P-4	xDSL (ADSL, HDSL and UCL) Loop w/o Conditioning/<6 circuits/Dispatch/TN (days)				YES			4.41	69				YES
		Held Orders												
B.2.3.1.1.1	P-1	Switch Ports/<10 circuits/Facility/TN (days)	5.781				8.97	134			7.260			
B.2.3.1.1.2	P-1	Switch Ports/<10 circuits/Equipment/TN (days)					0.00	0						
B.2.3.1.1.3	P-1	Switch Ports/<10 circuits/Other/TN (days)	11.904				7.72	29			10.545			
B.2.3.1.2.1	P-1	Switch Ports/>=10 circuits/Facility/TN (days)					0.00	0						
B.2.3.1.2.2	P-1	Switch Ports/>=10 circuits/Equipment/TN (days)					0.00	0						
B.2.3.1.2.3	P-1	Switch Ports/>=10 circuits/Other/TN (days)					0.00	0						
B.2.3.2.1.1	P-1	Local Interoffice Transport/<10 circuits/Facility/TN (days)					0.00	0	0.00	0				YES
B.2.3.2.1.2	P-1	Local Interoffice Transport/<10 circuits/Equipment/TN (days)					0.00	0	0.00	0				YES
B.2.3.2.1.3	P-1	Local Interoffice Transport/<10 circuits/Other/TN (days)	0.000				0.00	0	0.00	0				YES
B.2.3.2.2.1	P-1	Local Interoffice Transport/>=10 circuits/Facility/TN (days)					0.00	0						
B.2.3.2.2.2	P-1	Local Interoffice Transport/>=10 circuits/Equipment/TN (days)					0.00	0						
B.2.3.2.2.3	P-1	Local Interoffice Transport/>=10 circuits/Other/TN (days)					0.00	0						
B.2.3.3.1.1	P-1	Loop + Port Combinations/<10 circuits/Facility/TN (days)	5.755			YES	8.97	134	0.00	0	7.260			YES
B.2.3.3.1.2	P-1	Loop + Port Combinations/<10 circuits/Equipment/TN (days)				YES	0.00	0	0.00	0				YES
B.2.3.3.1.3	P-1	Loop + Port Combinations/<10 circuits/Other/TN (days)	11.904			YES	7.72	29	0.00	0	10.545			YES
B.2.3.3.2.1	P-1	Loop + Port Combinations/>=10 circuits/Facility/TN (days)				YES	0.00	0	0.00	0				YES
B.2.3.3.2.2	P-1	Loop + Port Combinations/>=10 circuits/Equipment/TN (days)				YES	0.00	0	0.00	0				YES
B.2.3.3.2.3	P-1	Loop + Port Combinations/>=10 circuits/Other/TN (days)				YES	0.00	0	0.00	0				YES
B.2.3.4.1.1	P-1	Combo Other/<10 circuits/Facility/TN (days)	7.440	7.47171	-0.2308	YES	12.15	142	0.00	0	26.570			YES
B.2.3.4.1.2	P-1	Combo Other/<10 circuits/Equipment/TN (days)				YES	0.00	0	0.00	0				YES
B.2.3.4.1.3	P-1	Combo Other/<10 circuits/Other/TN (days)	11.772			YES	7.69	29	0.00	0	10.566			YES
B.2.3.4.2.1	P-1	Combo Other/>=10 circuits/Facility/TN (days)					0.00	0						
B.2.3.4.2.2	P-1	Combo Other/>=10 circuits/Equipment/TN (days)					0.00	0						
B.2.3.4.2.3	P-1	Combo Other/>=10 circuits/Other/TN (days)					0.00	0						
B.2.3.5.1.1	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Facility/TN (days)	18.554	20.74398	1.0003	YES	7.50	16	4.00	1	5.910	6.09234	0.5745	YES
B.2.3.5.1.2	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Equipment/TN (days)				YES	0.00	0	0.00	0				YES
B.2.3.5.1.3	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Other/TN (days)				YES	5.25	4	0.00	0	8.500			YES
B.2.3.5.2.1	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Facility/TN (days)					0.00	0						
B.2.3.5.2.2	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Equipment/TN (days)					0.00	0						
B.2.3.5.2.3	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Other/TN (days)					0.00	0						
B.2.3.6.1.1	P-1	UNE ISDN/<10 circuits/Facility/TN (days)	21.392			YES	120.75	4	0.00	0	120.649			YES
B.2.3.6.1.2	P-1	UNE ISDN/<10 circuits/Equipment/TN (days)				YES	0.00	0	0.00	0				YES
B.2.3.6.1.3	P-1	UNE ISDN/<10 circuits/Other/TN (days)				YES	0.00	0	0.00	0				YES
B.2.3.6.2.1	P-1	UNE ISDN/>=10 circuits/Facility/TN (days)												
B.2.3.6.2.2	P-1	UNE ISDN/>=10 circuits/Equipment/TN (days)												
B.2.3.6.2.3	P-1	UNE ISDN/>=10 circuits/Other/TN (days)												
B.2.3.7.1.1	P-1	Line Sharing/<10 circuits/Facility/TN (days)	18.554			YES	7.50	16	0.00	0	5.910			YES

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			Benchmark/ Analog	August BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.3.7.1.2	P-1	Line Sharing/<10 circuits/Equipment/TN (days)	ADSL to Retail	0.00	0	0.00	0				YES
B.2.3.7.1.3	P-1	Line Sharing/<10 circuits/Other/TN (days)	ADSL to Retail	3.00	1	0.00	0	0.000			YES
B.2.3.7.2.1	P-1	Line Sharing/>=10 circuits/Facility/TN (days)	ADSL to Retail	0.00	0						
B.2.3.7.2.2	P-1	Line Sharing/>=10 circuits/Equipment/TN (days)	ADSL to Retail	0.00	0						
B.2.3.7.2.3	P-1	Line Sharing/>=10 circuits/Other/TN (days)	ADSL to Retail	0.00	0						
B.2.3.8.1.1	P-1	2W Analog Loop Design/<10 circuits/Facility/TN (days)	R&B - Disp	7.59	128	0.00	0	5.717			YES
B.2.3.8.1.2	P-1	2W Analog Loop Design/<10 circuits/Equipment/TN (days)	R&B - Disp	0.00	0	0.00	0				YES
B.2.3.8.1.3	P-1	2W Analog Loop Design/<10 circuits/Other/TN (days)	R&B - Disp	16.42	19	0.00	0	21.070			YES
B.2.3.8.2.1	P-1	2W Analog Loop Design/>=10 circuits/Facility/TN (days)	R&B - Disp	0.00	0						
B.2.3.8.2.2	P-1	2W Analog Loop Design/>=10 circuits/Equipment/TN (days)	R&B - Disp	0.00	0						
B.2.3.8.2.3	P-1	2W Analog Loop Design/>=10 circuits/Other/TN (days)	R&B - Disp	0.00	0						
B.2.3.9.1.1	P-1	2W Analog Loop Non-Design/<10 circuits/Facility/TN (days)	R&B (POTS) excl SB Or	7.59	128	0.00	0	5.717			YES
B.2.3.9.1.2	P-1	2W Analog Loop Non-Design/<10 circuits/Equipment/TN (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES
B.2.3.9.1.3	P-1	2W Analog Loop Non-Design/<10 circuits/Other/TN (days)	R&B (POTS) excl SB Or	16.42	19	0.00	0	21.070			YES
B.2.3.9.2.1	P-1	2W Analog Loop Non-Design/>=10 circuits/Facility/TN (days)	R&B (POTS) excl SB Or	0.00	0						
B.2.3.9.2.2	P-1	2W Analog Loop Non-Design/>=10 circuits/Equipment/TN (days)	R&B (POTS) excl SB Or	0.00	0						
B.2.3.9.2.3	P-1	2W Analog Loop Non-Design/>=10 circuits/Other/TN (days)	R&B (POTS) excl SB Or	0.00	0						
B.2.3.10.1.1	P-1	2W Analog Loop w/INP Design/<10 circuits/Facility/TN (days)	R&B - Disp	7.59	128			5.717			
B.2.3.10.1.2	P-1	2W Analog Loop w/INP Design/<10 circuits/Equipment/TN (days)	R&B - Disp	0.00	0						
B.2.3.10.1.3	P-1	2W Analog Loop w/INP Design/<10 circuits/Other/TN (days)	R&B - Disp	16.42	19			21.070			
B.2.3.10.2.1	P-1	2W Analog Loop w/INP Design/>=10 circuits/Facility/TN (days)	R&B - Disp	0.00	0						
B.2.3.10.2.2	P-1	2W Analog Loop w/INP Design/>=10 circuits/Equipment/TN (days)	R&B - Disp	0.00	0						
B.2.3.10.2.3	P-1	2W Analog Loop w/INP Design/>=10 circuits/Other/TN (days)	R&B - Disp	0.00	0						
B.2.3.11.1.1	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Facility/TN (days)	R&B (POTS) excl SB Or	7.59	128	0.00	0	5.717			YES
B.2.3.11.1.2	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Equipment/TN (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES
B.2.3.11.1.3	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Other/TN (days)	R&B (POTS) excl SB Or	16.42	19	0.00	0	21.070			YES
B.2.3.11.2.1	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Facility/TN (days)	R&B (POTS) excl SB Or	0.00	0						
B.2.3.11.2.2	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Equipment/TN (days)	R&B (POTS) excl SB Or	0.00	0						
B.2.3.11.2.3	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Other/TN (days)	R&B (POTS) excl SB Or	0.00	0						
B.2.3.12.1.1	P-1	2W Analog Loop w/LNP Design/<10 circuits/Facility/TN (days)	R&B - Disp	7.59	128	0.00	0	5.717			YES
B.2.3.12.1.2	P-1	2W Analog Loop w/LNP Design/<10 circuits/Equipment/TN (days)	R&B - Disp	0.00	0	0.00	0				YES
B.2.3.12.1.3	P-1	2W Analog Loop w/LNP Design/<10 circuits/Other/TN (days)	R&B - Disp	16.42	19	0.00	0	21.070			YES
B.2.3.12.2.1	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Facility/TN (days)	R&B - Disp	0.00	0						
B.2.3.12.2.2	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Equipment/TN (days)	R&B - Disp	0.00	0						
B.2.3.12.2.3	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Other/TN (days)	R&B - Disp	0.00	0						
B.2.3.13.1.1	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Facility/TN (days)	R&B (POTS) excl SB Or	7.59	128	0.00	0	5.717			YES
B.2.3.13.1.2	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Equipment/TN (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES
B.2.3.13.1.3	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Other/TN (days)	R&B (POTS) excl SB Or	16.42	19	0.00	0	21.070			YES
B.2.3.13.2.1	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Facility/TN (days)	R&B (POTS) excl SB Or	0.00	0						
B.2.3.13.2.2	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Equipment/TN (days)	R&B (POTS) excl SB Or	0.00	0						
B.2.3.13.2.3	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Other/TN (days)	R&B (POTS) excl SB Or	0.00	0						
B.2.3.14.1.1	P-1	Other Design/<10 circuits/Facility/TN (days)	Design	10.67	6			10.132			
B.2.3.14.1.2	P-1	Other Design/<10 circuits/Equipment/TN (days)	Design	0.00	0						
B.2.3.14.1.3	P-1	Other Design/<10 circuits/Other/TN (days)	Design	6.00	1			0.000			
B.2.3.14.2.1	P-1	Other Design/>=10 circuits/Facility/TN (days)	Design	0.00	0						
B.2.3.14.2.2	P-1	Other Design/>=10 circuits/Equipment/TN (days)	Design	0.00	0						
B.2.3.14.2.3	P-1	Other Design/>=10 circuits/Other/TN (days)	Design	0.00	0						
B.2.3.15.1.1	P-1	Other Non-Design/<10 circuits/Facility/TN (days)	R&B	7.59	128			5.717			
B.2.3.15.1.2	P-1	Other Non-Design/<10 circuits/Equipment/TN (days)	R&B	0.00	0						
B.2.3.15.1.3	P-1	Other Non-Design/<10 circuits/Other/TN (days)	R&B	16.42	19			21.070			
B.2.3.15.2.1	P-1	Other Non-Design/>=10 circuits/Facility/TN (days)	R&B	0.00	0						
B.2.3.15.2.2	P-1	Other Non-Design/>=10 circuits/Equipment/TN (days)	R&B	0.00	0						
B.2.3.15.2.3	P-1	Other Non-Design/>=10 circuits/Other/TN (days)	R&B	0.00	0						
B.2.3.16.1.1	P-1	INP (Standalone)/<10 circuits/Facility/TN (days)	R&B (POTS)	7.59	128	0.00	0	5.717			YES
B.2.3.16.1.2	P-1	INP (Standalone)/<10 circuits/Equipment/TN (days)	R&B (POTS)	0.00	0	0.00	0				YES
B.2.3.16.1.3	P-1	INP (Standalone)/<10 circuits/Other/TN (days)	R&B (POTS)	16.42	19	0.00	0	21.070			YES
B.2.3.16.2.1	P-1	INP (Standalone)/>=10 circuits/Facility/TN (days)	R&B (POTS)	0.00	0						
B.2.3.16.2.2	P-1	INP (Standalone)/>=10 circuits/Equipment/TN (days)	R&B (POTS)	0.00	0						
B.2.3.16.2.3	P-1	INP (Standalone)/>=10 circuits/Other/TN (days)	R&B (POTS)	0.00	0						
B.2.3.17.1.1	P-1	LNP (Standalone)/<10 circuits/Facility/TN (days)	R&B (POTS)	7.59	128	0.00	0	5.717			YES
B.2.3.17.1.2	P-1	LNP (Standalone)/<10 circuits/Equipment/TN (days)	R&B (POTS)	0.00	0	0.00	0				YES
B.2.3.17.1.3	P-1	LNP (Standalone)/<10 circuits/Other/TN (days)	R&B (POTS)	16.42	19	0.00	0	21.070			YES
B.2.3.17.2.1	P-1	LNP (Standalone)/>=10 circuits/Facility/TN (days)	R&B (POTS)	0.00	0	0.00	0				YES
B.2.3.17.2.2	P-1	LNP (Standalone)/>=10 circuits/Equipment/TN (days)	R&B (POTS)	0.00	0	0.00	0				YES
B.2.3.17.2.3	P-1	LNP (Standalone)/>=10 circuits/Other/TN (days)	R&B (POTS)	0.00	0	0.00	0				YES
B.2.3.18.1.1	P-1	Digital Loop < DS1/<10 circuits/Facility/TN (days)	Digital Loop < DS1	7.93	14	0.00	0	8.471			YES
B.2.3.18.1.2	P-1	Digital Loop < DS1/<10 circuits/Equipment/TN (days)	Digital Loop < DS1	0.00	0	0.00	0				YES
B.2.3.18.1.3	P-1	Digital Loop < DS1/<10 circuits/Other/TN (days)	Digital Loop < DS1	3.00	1	0.00	0	0.000			YES

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			July								Jur			
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.2.3.7.1.2	P-1	Line Sharing/<10 circuits/Equipment/TN (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.3.7.1.3	P-1	Line Sharing/<10 circuits/Other/TN (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.3.7.2.1	P-1	Line Sharing/>=10 circuits/Facility/TN (days)	0.00	0							0.00	0		
B.2.3.7.2.2	P-1	Line Sharing/>=10 circuits/Equipment/TN (days)	0.00	0							0.00	0		
B.2.3.7.2.3	P-1	Line Sharing/>=10 circuits/Other/TN (days)	0.00	0							0.00	0		
B.2.3.8.1.1	P-1	2W Analog Loop Design/<10 circuits/Facility/TN (days)	8.12	98	0.00	0	7.839			YES	8.74	105	0.00	0
B.2.3.8.1.2	P-1	2W Analog Loop Design/<10 circuits/Equipment/TN (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.3.8.1.3	P-1	2W Analog Loop Design/<10 circuits/Other/TN (days)	15.74	19	0.00	0	16.918			YES	12.52	23	0.00	0
B.2.3.8.2.1	P-1	2W Analog Loop Design/>=10 circuits/Facility/TN (days)	0.00	0							0.00	0		
B.2.3.8.2.2	P-1	2W Analog Loop Design/>=10 circuits/Equipment/TN (days)	0.00	0							0.00	0		
B.2.3.8.2.3	P-1	2W Analog Loop Design/>=10 circuits/Other/TN (days)	0.00	0							0.00	0		
B.2.3.9.1.1	P-1	2W Analog Loop Non-Design/<10 circuits/Facility/TN (days)	8.12	98	0.00	0	7.839			YES	8.73	104	0.00	0
B.2.3.9.1.2	P-1	2W Analog Loop Non-Design/<10 circuits/Equipment/TN (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.3.9.1.3	P-1	2W Analog Loop Non-Design/<10 circuits/Other/TN (days)	15.74	19	0.00	0	16.918			YES	12.52	23	0.00	0
B.2.3.9.2.1	P-1	2W Analog Loop Non-Design/>=10 circuits/Facility/TN (days)	0.00	0							0.00	0		
B.2.3.9.2.2	P-1	2W Analog Loop Non-Design/>=10 circuits/Equipment/TN (days)	0.00	0							0.00	0		
B.2.3.9.2.3	P-1	2W Analog Loop Non-Design/>=10 circuits/Other/TN (days)	0.00	0							0.00	0		
B.2.3.10.1.1	P-1	2W Analog Loop w/INP Design/<10 circuits/Facility/TN (days)	8.12	98			7.839				8.74	105		
B.2.3.10.1.2	P-1	2W Analog Loop w/INP Design/<10 circuits/Equipment/TN (days)	0.00	0							0.00	0		
B.2.3.10.1.3	P-1	2W Analog Loop w/INP Design/<10 circuits/Other/TN (days)	15.74	19			16.918				12.52	23		
B.2.3.10.2.1	P-1	2W Analog Loop w/INP Design/>=10 circuits/Facility/TN (days)	0.00	0							0.00	0		
B.2.3.10.2.2	P-1	2W Analog Loop w/INP Design/>=10 circuits/Equipment/TN (days)	0.00	0							0.00	0		
B.2.3.10.2.3	P-1	2W Analog Loop w/INP Design/>=10 circuits/Other/TN (days)	0.00	0							0.00	0		
B.2.3.11.1.1	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Facility/TN (days)	8.12	98			7.839				8.73	104		
B.2.3.11.1.2	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Equipment/TN (days)	0.00	0							0.00	0		
B.2.3.11.1.3	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Other/TN (days)	15.74	19			16.918				12.52	23		
B.2.3.11.2.1	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Facility/TN (days)	0.00	0							0.00	0		
B.2.3.11.2.2	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Equipment/TN (days)	0.00	0							0.00	0		
B.2.3.11.2.3	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Other/TN (days)	0.00	0							0.00	0		
B.2.3.12.1.1	P-1	2W Analog Loop w/LNP Design/<10 circuits/Facility/TN (days)	8.12	98	0.00	0	7.839			YES	8.74	105	0.00	0
B.2.3.12.1.2	P-1	2W Analog Loop w/LNP Design/<10 circuits/Equipment/TN (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.3.12.1.3	P-1	2W Analog Loop w/LNP Design/<10 circuits/Other/TN (days)	15.74	19	0.00	0	16.918			YES	12.52	23	0.00	0
B.2.3.12.2.1	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Facility/TN (days)	0.00	0							0.00	0		
B.2.3.12.2.2	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Equipment/TN (days)	0.00	0							0.00	0		
B.2.3.12.2.3	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Other/TN (days)	0.00	0							0.00	0		
B.2.3.13.1.1	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Facility/TN (days)	8.12	98	0.00	0	7.839			YES	8.73	104		
B.2.3.13.1.2	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Equipment/TN (days)	0.00	0	0.00	0				YES	0.00	0		
B.2.3.13.1.3	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Other/TN (days)	15.74	19	0.00	0	16.918			YES	12.52	23		
B.2.3.13.2.1	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Facility/TN (days)	0.00	0							0.00	0		
B.2.3.13.2.2	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Equipment/TN (days)	0.00	0							0.00	0		
B.2.3.13.2.3	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Other/TN (days)	0.00	0							0.00	0		
B.2.3.14.1.1	P-1	Other Design/<10 circuits/Facility/TN (days)	24.50	4			18.285				13.69	13		
B.2.3.14.1.2	P-1	Other Design/<10 circuits/Equipment/TN (days)	0.00	0							0.00	0		
B.2.3.14.1.3	P-1	Other Design/<10 circuits/Other/TN (days)	10.50	6			6.863				4.00	1		
B.2.3.14.2.1	P-1	Other Design/>=10 circuits/Facility/TN (days)	0.00	0							0.00	0		
B.2.3.14.2.2	P-1	Other Design/>=10 circuits/Equipment/TN (days)	0.00	0							0.00	0		
B.2.3.14.2.3	P-1	Other Design/>=10 circuits/Other/TN (days)	0.00	0							0.00	0		
B.2.3.15.1.1	P-1	Other Non-Design/<10 circuits/Facility/TN (days)	8.12	98			7.839				8.74	105		
B.2.3.15.1.2	P-1	Other Non-Design/<10 circuits/Equipment/TN (days)	0.00	0							0.00	0		
B.2.3.15.1.3	P-1	Other Non-Design/<10 circuits/Other/TN (days)	15.74	19			16.918				12.52	23		
B.2.3.15.2.1	P-1	Other Non-Design/>=10 circuits/Facility/TN (days)	0.00	0							0.00	0		
B.2.3.15.2.2	P-1	Other Non-Design/>=10 circuits/Equipment/TN (days)	0.00	0							0.00	0		
B.2.3.15.2.3	P-1	Other Non-Design/>=10 circuits/Other/TN (days)	0.00	0							0.00	0		
B.2.3.16.1.1	P-1	INP (Standalone)/<10 circuits/Facility/TN (days)	8.12	98			7.839				8.73	104		
B.2.3.16.1.2	P-1	INP (Standalone)/<10 circuits/Equipment/TN (days)	0.00	0							0.00	0		
B.2.3.16.1.3	P-1	INP (Standalone)/<10 circuits/Other/TN (days)	15.74	19			16.918				12.52	23		
B.2.3.16.2.1	P-1	INP (Standalone)/>=10 circuits/Facility/TN (days)	0.00	0							0.00	0		
B.2.3.16.2.2	P-1	INP (Standalone)/>=10 circuits/Equipment/TN (days)	0.00	0							0.00	0		
B.2.3.16.2.3	P-1	INP (Standalone)/>=10 circuits/Other/TN (days)	0.00	0							0.00	0		
B.2.3.17.1.1	P-1	LNP (Standalone)/<10 circuits/Facility/TN (days)	8.12	98	0.00	0	7.839			YES	8.73	104	0.00	0
B.2.3.17.1.2	P-1	LNP (Standalone)/<10 circuits/Equipment/TN (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.3.17.1.3	P-1	LNP (Standalone)/<10 circuits/Other/TN (days)	15.74	19	0.00	0	16.918			YES	12.52	23	0.00	0
B.2.3.17.2.1	P-1	LNP (Standalone)/>=10 circuits/Facility/TN (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.3.17.2.2	P-1	LNP (Standalone)/>=10 circuits/Equipment/TN (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.3.17.2.3	P-1	LNP (Standalone)/>=10 circuits/Other/TN (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.3.18.1.1	P-1	Digital Loop < DS1/<10 circuits/Facility/TN (days)	13.67	9	9.50	2	15.305	11.96465	0.3482	YES	20.17	12	6.00	1
B.2.3.18.1.2	P-1	Digital Loop < DS1/<10 circuits/Equipment/TN (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.3.18.1.3	P-1	Digital Loop < DS1/<10 circuits/Other/TN (days)	2.00	1	0.00	0	0.000			YES	0.00	0	0.00	0

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			ne				May							
			Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.3.7.1.2	P-1	Line Sharing/<10 circuits/Equipment/TN (days)				YES	0.00	0	0.00	0				YES
B.2.3.7.1.3	P-1	Line Sharing/<10 circuits/Other/TN (days)				YES	5.25	4	0.00	0	8.500			YES
B.2.3.7.2.1	P-1	Line Sharing/>=10 circuits/Facility/TN (days)					0.00	0						
B.2.3.7.2.2	P-1	Line Sharing/>=10 circuits/Equipment/TN (days)					0.00	0						
B.2.3.7.2.3	P-1	Line Sharing/>=10 circuits/Other/TN (days)					0.00	0						
B.2.3.8.1.1	P-1	2W Analog Loop Design/<10 circuits/Facility/TN (days)	5.755			YES	8.97	134	0.00	0	7.260			YES
B.2.3.8.1.2	P-1	2W Analog Loop Design/<10 circuits/Equipment/TN (days)				YES	0.00	0	0.00	0				YES
B.2.3.8.1.3	P-1	2W Analog Loop Design/<10 circuits/Other/TN (days)	11.904			YES	7.72	29	0.00	0	10.545			YES
B.2.3.8.2.1	P-1	2W Analog Loop Design/>=10 circuits/Facility/TN (days)					0.00	0						
B.2.3.8.2.2	P-1	2W Analog Loop Design/>=10 circuits/Equipment/TN (days)					0.00	0						
B.2.3.8.2.3	P-1	2W Analog Loop Design/>=10 circuits/Other/TN (days)					0.00	0						
B.2.3.9.1.1	P-1	2W Analog Loop Non-Design/<10 circuits/Facility/TN (days)	5.781			YES	8.97	134	0.00	0	7.260			YES
B.2.3.9.1.2	P-1	2W Analog Loop Non-Design/<10 circuits/Equipment/TN (days)				YES	0.00	0	0.00	0				YES
B.2.3.9.1.3	P-1	2W Analog Loop Non-Design/<10 circuits/Other/TN (days)	11.904			YES	7.72	29	0.00	0	10.545			YES
B.2.3.9.2.1	P-1	2W Analog Loop Non-Design/>=10 circuits/Facility/TN (days)					0.00	0						
B.2.3.9.2.2	P-1	2W Analog Loop Non-Design/>=10 circuits/Equipment/TN (days)					0.00	0						
B.2.3.9.2.3	P-1	2W Analog Loop Non-Design/>=10 circuits/Other/TN (days)					0.00	0						
B.2.3.10.1.1	P-1	2W Analog Loop w/INP Design/<10 circuits/Facility/TN (days)	5.755				8.97	134			7.260			
B.2.3.10.1.2	P-1	2W Analog Loop w/INP Design/<10 circuits/Equipment/TN (days)					0.00	0						
B.2.3.10.1.3	P-1	2W Analog Loop w/INP Design/<10 circuits/Other/TN (days)	11.904				7.72	29			10.545			
B.2.3.10.2.1	P-1	2W Analog Loop w/INP Design/>=10 circuits/Facility/TN (days)					0.00	0						
B.2.3.10.2.2	P-1	2W Analog Loop w/INP Design/>=10 circuits/Equipment/TN (days)					0.00	0						
B.2.3.10.2.3	P-1	2W Analog Loop w/INP Design/>=10 circuits/Other/TN (days)					0.00	0						
B.2.3.11.1.1	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Facility/TN (days)	5.781				8.97	134			7.260			
B.2.3.11.1.2	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Equipment/TN (days)					0.00	0						
B.2.3.11.1.3	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Other/TN (days)	11.904				7.72	29			10.545			
B.2.3.11.2.1	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Facility/TN (days)					0.00	0						
B.2.3.11.2.2	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Equipment/TN (days)					0.00	0						
B.2.3.11.2.3	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Other/TN (days)					0.00	0						
B.2.3.12.1.1	P-1	2W Analog Loop w/LNP Design/<10 circuits/Facility/TN (days)	5.755			YES	8.97	134	0.00	0	7.260			YES
B.2.3.12.1.2	P-1	2W Analog Loop w/LNP Design/<10 circuits/Equipment/TN (days)				YES	0.00	0	0.00	0				YES
B.2.3.12.1.3	P-1	2W Analog Loop w/LNP Design/<10 circuits/Other/TN (days)	11.904			YES	7.72	29	0.00	0	10.545			YES
B.2.3.12.2.1	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Facility/TN (days)					0.00	0						
B.2.3.12.2.2	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Equipment/TN (days)					0.00	0						
B.2.3.12.2.3	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Other/TN (days)					0.00	0						
B.2.3.13.1.1	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Facility/TN (days)	5.781				8.97	134			7.260			
B.2.3.13.1.2	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Equipment/TN (days)					0.00	0						
B.2.3.13.1.3	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Other/TN (days)	11.904				7.72	29			10.545			
B.2.3.13.2.1	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Facility/TN (days)					0.00	0						
B.2.3.13.2.2	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Equipment/TN (days)					0.00	0						
B.2.3.13.2.3	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Other/TN (days)					0.00	0						
B.2.3.14.1.1	P-1	Other Design/<10 circuits/Facility/TN (days)	15.102				65.38	8			98.933			
B.2.3.14.1.2	P-1	Other Design/<10 circuits/Equipment/TN (days)					0.00	0						
B.2.3.14.1.3	P-1	Other Design/<10 circuits/Other/TN (days)	0.000				1.00	1			0.000			
B.2.3.14.2.1	P-1	Other Design/>=10 circuits/Facility/TN (days)					0.00	0						
B.2.3.14.2.2	P-1	Other Design/>=10 circuits/Equipment/TN (days)					0.00	0						
B.2.3.14.2.3	P-1	Other Design/>=10 circuits/Other/TN (days)					0.00	0						
B.2.3.15.1.1	P-1	Other Non-Design/<10 circuits/Facility/TN (days)	5.755				8.97	134	0.00	0	7.260			YES
B.2.3.15.1.2	P-1	Other Non-Design/<10 circuits/Equipment/TN (days)					0.00	0	0.00	0				YES
B.2.3.15.1.3	P-1	Other Non-Design/<10 circuits/Other/TN (days)	11.904				7.72	29	0.00	0	10.545			YES
B.2.3.15.2.1	P-1	Other Non-Design/>=10 circuits/Facility/TN (days)					0.00	0						
B.2.3.15.2.2	P-1	Other Non-Design/>=10 circuits/Equipment/TN (days)					0.00	0						
B.2.3.15.2.3	P-1	Other Non-Design/>=10 circuits/Other/TN (days)					0.00	0						
B.2.3.16.1.1	P-1	INP (Standalone)/<10 circuits/Facility/TN (days)	5.781				8.97	134			7.260			
B.2.3.16.1.2	P-1	INP (Standalone)/<10 circuits/Equipment/TN (days)					0.00	0						
B.2.3.16.1.3	P-1	INP (Standalone)/<10 circuits/Other/TN (days)	11.904				7.72	29			10.545			
B.2.3.16.2.1	P-1	INP (Standalone)/>=10 circuits/Facility/TN (days)					0.00	0						
B.2.3.16.2.2	P-1	INP (Standalone)/>=10 circuits/Equipment/TN (days)					0.00	0						
B.2.3.16.2.3	P-1	INP (Standalone)/>=10 circuits/Other/TN (days)					0.00	0						
B.2.3.17.1.1	P-1	LNP (Standalone)/<10 circuits/Facility/TN (days)	5.781			YES	8.97	134	0.00	0	7.260			YES
B.2.3.17.1.2	P-1	LNP (Standalone)/<10 circuits/Equipment/TN (days)				YES	0.00	0	0.00	0				YES
B.2.3.17.1.3	P-1	LNP (Standalone)/<10 circuits/Other/TN (days)	11.904			YES	7.72	29	0.00	0	10.545			YES
B.2.3.17.2.1	P-1	LNP (Standalone)/>=10 circuits/Facility/TN (days)				YES	0.00	0	0.00	0				YES
B.2.3.17.2.2	P-1	LNP (Standalone)/>=10 circuits/Equipment/TN (days)				YES	0.00	0	0.00	0				YES
B.2.3.17.2.3	P-1	LNP (Standalone)/>=10 circuits/Other/TN (days)				YES	0.00	0	0.00	0				YES
B.2.3.18.1.1	P-1	Digital Loop < DS1/<10 circuits/Facility/TN (days)	18.522	19.27820	0.7349	YES	30.15	20	4.00	1	66.978	68.63185	0.3810	YES
B.2.3.18.1.2	P-1	Digital Loop < DS1/<10 circuits/Equipment/TN (days)				YES	0.00	0	0.00	0				YES
B.2.3.18.1.3	P-1	Digital Loop < DS1/<10 circuits/Other/TN (days)				YES	5.25	4	0.00	0	8.500			YES

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			Benchmark/ Analog	August BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.3.18.2.1	P-1	Digital Loop < DS1/>=10 circuits/Facility/TN (days)	Digital Loop < DS1	0.00	0						
B.2.3.18.2.2	P-1	Digital Loop < DS1/>=10 circuits/Equipment/TN (days)	Digital Loop < DS1	0.00	0						
B.2.3.18.2.3	P-1	Digital Loop < DS1/>=10 circuits/Other/TN (days)	Digital Loop < DS1	0.00	0						
B.2.3.19.1.1	P-1	Digital Loop >= DS1/<10 circuits/Facility/TN (days)	Digital Loop >= DS1	0.00	0	9.80	5				NO
B.2.3.19.1.2	P-1	Digital Loop >= DS1/<10 circuits/Equipment/TN (days)	Digital Loop >= DS1	0.00	0	0.00	0				YES
B.2.3.19.1.3	P-1	Digital Loop >= DS1/<10 circuits/Other/TN (days)	Digital Loop >= DS1	6.00	1	0.00	0	0.000			YES
B.2.3.19.2.1	P-1	Digital Loop >= DS1/>=10 circuits/Facility/TN (days)	Digital Loop >= DS1	0.00	0						
B.2.3.19.2.2	P-1	Digital Loop >= DS1/>=10 circuits/Equipment/TN (days)	Digital Loop >= DS1	0.00	0						
B.2.3.19.2.3	P-1	Digital Loop >= DS1/>=10 circuits/Other/TN (days)	Digital Loop >= DS1	0.00	0						
		% Jeopardies - Mechanized									
B.2.5.1	P-2	Switch Ports/TN (%)	R&B (POTS)	0.49%	310,637						
B.2.5.2	P-2	Local Interoffice Transport/TN (%)	DS1/ DS3 - Interoffice	41.10%	1,056						
B.2.5.3	P-2	Loop + Port Combinations/TN (%)	R&B	0.49%	312,455	0.11%	20,967		0.00050	7.5978	YES
B.2.5.4	P-2	Combo Other/TN (%)	R&B&D - Disp	9.13%	21,793						
B.2.5.5	P-2	xDSL (ADSL, HDSL and UCL)/TN (%)	ADSL to Retail	3.13%	4,125	0.00%	61		0.02245	1.3930	YES
B.2.5.6	P-2	UNE ISDN/TN (%)	ISDN - BRI	5.52%	1,014	16.13%	31		0.04165	-2.5466	NO
B.2.5.7	P-2	Line Sharing/TN (%)	ADSL to Retail	3.13%	4,125	0.00%	1		0.17407	0.1797	YES
B.2.5.8	P-2	2W Analog Loop Design/TN (%)	R&B - Disp	0.49%	312,455	9.82%	112		0.00663	-14.0644	NO
B.2.5.9	P-2	2W Analog Loop Non-Design/TN (%)	R&B (POTS) excl SB Or	0.90%	170,145	8.16%	49		0.01348	-5.3900	NO
B.2.5.10	P-2	2W Analog Loop w/INP Design/TN (%)	R&B - Disp	0.49%	312,455						
B.2.5.11	P-2	2W Analog Loop w/INP Non-Design/TN (%)	R&B (POTS) excl SB Or	0.90%	170,145						
B.2.5.12	P-2	2W Analog Loop w/LNP Design/TN (%)	R&B - Disp	0.49%	312,455	1.18%	85		0.00761	-0.8956	YES
B.2.5.13	P-2	2W Analog Loop w/LNP Non-Design/TN (%)	R&B (POTS) excl SB Or	0.90%	170,145						
B.2.5.14	P-2	Other Design/TN (%)	Design	23.28%	1,929						
B.2.5.15	P-2	Other Non-Design/TN (%)	R&B	0.49%	312,455						
B.2.5.16	P-2	INP (Standalone)/TN (%)	R&B (POTS)	0.49%	310,637						
B.2.5.17	P-2	LNP (Standalone)/TN (%)	R&B (POTS)	0.49%	310,637	0.00%	249		0.00444	1.1090	YES
B.2.5.18	P-2	Digital Loop < DS1/TN (%)	Digital Loop < DS1	4.53%	5,385	5.62%	89		0.02223	-0.4890	YES
B.2.5.19	P-2	Digital Loop >= DS1/TN (%)	Digital Loop >= DS1	29.17%	569	72.78%	180		0.03887	-11.2171	NO
		% Jeopardies - Non-Mechanized									
B.2.6.1	P-2	Switch Ports/TN (%)	Diagnostic								Diagnostic
B.2.6.2	P-2	Local Interoffice Transport/TN (%)	Diagnostic								Diagnostic
B.2.6.3	P-2	Loop + Port Combinations/TN (%)	Diagnostic			2.75%	327				Diagnostic
B.2.6.4	P-2	Combo Other/TN (%)	Diagnostic			43.33%	30				Diagnostic
B.2.6.5	P-2	xDSL (ADSL, HDSL and UCL)/TN (%)	Diagnostic			13.33%	15				Diagnostic
B.2.6.6	P-2	UNE ISDN/TN (%)	Diagnostic			0.00%	4				Diagnostic
B.2.6.7	P-2	Line Sharing/TN (%)	Diagnostic			0.00%	50				Diagnostic
B.2.6.8	P-2	2W Analog Loop Design/TN (%)	Diagnostic			7.69%	13				Diagnostic
B.2.6.9	P-2	2W Analog Loop Non-Design/TN (%)	Diagnostic			0.00%	2				Diagnostic
B.2.6.10	P-2	2W Analog Loop w/INP Design/TN (%)	Diagnostic								Diagnostic
B.2.6.11	P-2	2W Analog Loop w/INP Non-Design/TN (%)	Diagnostic			0.00%	1				Diagnostic
B.2.6.12	P-2	2W Analog Loop w/LNP Design/TN (%)	Diagnostic			0.00%	6				Diagnostic
B.2.6.13	P-2	2W Analog Loop w/LNP Non-Design/TN (%)	Diagnostic			0.00%	1				Diagnostic
B.2.6.14	P-2	Other Design/TN (%)	Diagnostic								Diagnostic
B.2.6.15	P-2	Other Non-Design/TN (%)	Diagnostic								Diagnostic
B.2.6.16	P-2	INP (Standalone)/TN (%)	Diagnostic			0.00%	7				Diagnostic
B.2.6.17	P-2	LNP (Standalone)/TN (%)	Diagnostic			0.00%	256				Diagnostic
B.2.6.18	P-2	Digital Loop < DS1/TN (%)	Diagnostic			11.11%	18				Diagnostic
B.2.6.19	P-2	Digital Loop >= DS1/TN (%)	Diagnostic			46.15%	26				Diagnostic
		Average Jeopardy Notice Interval - Mechanized									
B.2.8.1	P-2	Switch Ports/TN (hours)	>= 48 hrs								
B.2.8.2	P-2	Local Interoffice Transport/TN (hours)	>= 48 hrs								
B.2.8.3	P-2	Loop + Port Combinations/TN (hours)	>= 48 hrs			102.07	20				YES
B.2.8.4	P-2	Combo Other/TN (hours)	>= 48 hrs								
B.2.8.5	P-2	xDSL (ADSL, HDSL and UCL)/TN (hours)	>= 48 hrs								
B.2.8.6	P-2	UNE ISDN/TN (hours)	>= 48 hrs			389.49	5				YES
B.2.8.7	P-2	Line Sharing/TN (hours)	>= 48 hrs								
B.2.8.8	P-2	2W Analog Loop Design/TN (hours)	>= 48 hrs			222.31	11				YES
B.2.8.9	P-2	2W Analog Loop Non-Design/TN (hours)	>= 48 hrs			155.29	3				YES
B.2.8.10	P-2	2W Analog Loop w/INP Design/TN (hours)	>= 48 hrs								
B.2.8.11	P-2	2W Analog Loop w/INP Non-Design/TN (hours)	>= 48 hrs								
B.2.8.12	P-2	2W Analog Loop w/LNP Design/TN (hours)	>= 48 hrs			168.52	1				YES
B.2.8.13	P-2	2W Analog Loop w/LNP Non-Design/TN (hours)	>= 48 hrs								
B.2.8.14	P-2	Other Design/TN (hours)	>= 48 hrs								
B.2.8.15	P-2	Other Non-Design/TN (hours)	>= 48 hrs								
B.2.8.16	P-2	INP (Standalone)/TN (hours)	>= 48 hrs								