BellSouth Monthly Performance Summary

Tennessee, August 2002 - M ay 2002

			July								Jun			
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.2.3.18.2.1	P-1	Digital Loop < DS1/>=10 circuits/Facility/TN (days)	0.00	0							0.00	0		
B.2.3.18.2.2	P-1	Digital Loop < DS1/>=10 circuits/Equipment/TN (days)	0.00	0							0.00	0		
B.2.3.18.2.3	P-1	Digital Loop < DS1/>=10 circuits/Other/TN (days)	0.00	0							0.00	0		
B.2.3.19.1.1	P-1	Digital Loop >= DS1/<10 circuits/Facility/TN (days)	0.00	0	5.00	2				NO	12.00	1	11.40	5
B.2.3.19.1.2	P-1	Digital Loop >= DS1/<10 circuits/Equipment/TN (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.3.19.1.3	P-1	Digital Loop >= DS1/<10 circuits/Other/TN (days)	8.00	1	0.00	0	0.000			YES	0.00	0	0.00	0
B.2.3.19.2.1	P-1	Digital Loop >= DS1/>=10 circuits/Facility/TN (days)	0.00	0							0.00	0		
B.2.3.19.2.2	P-1	Digital Loop >= DS1/>=10 circuits/Equipment/TN (days)	0.00	0							0.00	0		
B.2.3.19.2.3	P-1	Digital Loop >= DS1/>=10 circuits/Other/TN (days)	0.00	0							0.00	0		
		% Jeopardies - Mechanized												
B.2.5.1	P-2	Switch Ports/TN (%)	0.39%	291,438							0.41%	266,497		
B.2.5.2	P-2	Local Interoffice Transport/TN (%)	45.47%	895							40.54%	819		
B.2.5.3	P-2	Loop + Port Combinations/TN (%)	0.39%	294,020	0.09%	9,957		0.00063	4.6887	YES	0.43%	268,036	0.22%	5,887
B.2.5.4	P-2	Combo Other/TN (%)	7.76%	19,757	100.00%	2		0.16924	-4.8741	NO	9.49%	18,032	100.00%	4
B.2.5.5	P-2	xDSL (ADSL, HDSL and UCL)/TN (%)	3.80%	3,847	0.00%	41		0.03000	1.2650	YES	4.61%	3,513	4.11%	73
B.2.5.6	P-2	UNE ISDN/TN (%)	7.04%	866	19.51%	41		0.04090	-3.0487	NO	6.36%	786	4.55%	44
B.2.5.7	P-2	Line Sharing/TN (%)	3.80%	3,847							4.61%	3,513		
B.2.5.8	P-2	2W Analog Loop Design/TN (%)	0.39%	294,020	5.79%	121		0.00565	-9.5610	NO	0.43%	268,036	7.50%	160
B.2.5.9	P-2	2W Analog Loop Non-Design/TN (%)	0.71%	158,688	8.16%	49		0.01198	-6.2250	NO	0.76%	144,019	13.89%	36
B.2.5.10	P-2	2W Analog Loop w/INP Design/TN (%)	0.39%	294,020							0.43%	268,036		
B.2.5.11	P-2	2W Analog Loop w/INP Non-Design/TN (%)	0.71%	158,688							0.76%	144,019		
B.2.5.12	P-2	2W Analog Loop w/LNP Design/TN (%)	0.39%	294,020	2.44%	82		0.00686	-2.9921	NO	0.43%	268,036	3.61%	83
B.2.5.13	P-2	2W Analog Loop w/LNP Non-Design/TN (%)	0.71%	158,688							0.76%	144,019		
B.2.5.14	P-2	Other Design/TN (%)	21.95%	1,845							29.07%	1,985		
B.2.5.15	P-2	Other Non-Design/TN (%)	0.39%	294,020							0.43%	268,036		
B.2.5.16	P-2	INP (Standalone)/TN (%)	0.39%	291,438							0.41%	266,497		
B.2.5.17	P-2	LNP (Standalone)/TN (%)	0.39%	291,438	0.00%	247		0.00394	0.9771	YES	0.41%	266,497	0.00%	208
B.2.5.18	P-2	Digital Loop < DS1/TN (%)	4.77%	4,927	10.39%	77		0.02448	-2.2961	NO	7.13%	4,715	4.55%	110
B.2.5.19	P-2	Digital Loop >= DS1/TN (%)	21.81%	651	64.79%	142		0.03825	-11.2358	NO	29.62%	628	71.43%	203
		% Jeopardies - Non-Mechanized												
B.2.6.1	P-2	Switch Ports/TN (%)								Diagnostic				
B.2.6.2	P-2	Local Interoffice Transport/TN (%)								Diagnostic				
B.2.6.3	P-2	Loop + Port Combinations/TN (%)			2.09%	239				Diagnostic			0.31%	327
B.2.6.4	P-2	Combo Other/TN (%)			50.00%	36				Diagnostic			45.16%	31
B.2.6.5	P-2	xDSL (ADSL, HDSL and UCL)/TN (%)			2.56%	39				Diagnostic			7.84%	51
B.2.6.6	P-2	UNE ISDN/TN (%)			8.70%	23				Diagnostic			10.00%	10
B.2.6.7	P-2	Line Sharing/TN (%)			0.00%	55				Diagnostic			0.00%	43
B.2.6.8	P-2	2W Analog Loop Design/TN (%)			0.00%	14				Diagnostic			0.00%	24
B.2.6.9	P-2	2W Analog Loop Non-Design/TN (%)			0.00%	1				Diagnostic				
B.2.6.10	P-2	2W Analog Loop w/INP Design/TN (%)								Diagnostic				
B.2.6.11	P-2	2W Analog Loop w/INP Non-Design/TN (%)								Diagnostic				
B.2.6.12	P-2	2W Analog Loop w/LNP Design/TN (%)			0.00%	6				Diagnostic			0.00%	25
B.2.6.13	P-2	2W Analog Loop w/LNP Non-Design/TN (%)			0.00%	1				Diagnostic				
B.2.6.14	P-2	Other Design/TN (%)								Diagnostic				
B.2.6.15	P-2	Other Non-Design/TN (%)								Diagnostic				
B.2.6.16	P-2	INP (Standalone)/TN (%)								Diagnostic				
B.2.6.17	P-2	LNP (Standalone)/TN (%)			0.00%	229				Diagnostic			0.00%	253
B.2.6.18	P-2	Digital Loop < DS1/TN (%)			5.00%	60				Diagnostic			7.55%	53
B.2.6.19	P-2	Digital Loop >= DS1/TN (%)			58.14%	43				Diagnostic			61.29%	31
		Average Jeopardy Notice Interval - Mechanized												
B.2.8.1	P-2	Switch Ports/TN (hours)												
B.2.8.2	P-2	Local Interoffice Transport/TN (hours)												
B.2.8.3	P-2	Loop + Port Combinations/TN (hours)			218.82	8				YES			114.13	8
B.2.8.4	P-2	Combo Other/TN (hours)			485.08	2				YES			365.78	4
B.2.8.5	P-2	xDSL (ADSL, HDSL and UCL)/TN (hours)											181.60	3
B.2.8.6	P-2	UNE ISDN/TN (hours)			274.56	7				YES			181.88	2
B.2.8.7	P-2	Line Sharing/TN (hours)												
B.2.8.8	P-2	2W Analog Loop Design/TN (hours)			298.61	7				YES			224.62	12
B.2.8.9	P-2	2W Analog Loop Non-Design/TN (hours)			179.35	4				YES			147.40	4
B.2.8.10	P-2	2W Analog Loop w/INP Design/TN (hours)												
B.2.8.11	P-2	2W Analog Loop w/INP Non-Design/TN (hours)												
B.2.8.12	P-2	2W Analog Loop w/LNP Design/TN (hours)			161.76	2				YES			189.39	3
B.2.8.13	P-2	2W Analog Loop w/LNP Non-Design/TN (hours)												
B.2.8.14	P-2	Other Design/TN (hours)												
B.2.8.15	P-2	Other Non-Design/TN (hours)												
B.2.8.16	P-2	INP (Standalone)/TN (hours)												

BellSouth Monthly Performance Summary

Tennessee, August 2002 - May 2002

			ne				May							
			Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.3.18.2.1	P-1	Digital Loop < DS1/>=10 circuits/Facility/TN (days)					0.00	0						
B.2.3.18.2.2	P-1	Digital Loop < DS1/>=10 circuits/Equipment/TN (days)					0.00	0						
B.2.3.18.2.3	P-1	Digital Loop < DS1/>=10 circuits/Other/TN (days)					0.00	0						
B.2.3.19.1.1	P-1	Digital Loop >= DS1/<10 circuits/Facility/TN (days)	0.000	0.00000		YES	12.67	3	4.00	1	10.693	12.34684	0.7019	YES
B.2.3.19.1.2	P-1	Digital Loop >= DS1/<10 circuits/Equipment/TN (days)				YES	0.00	0	0.00	0				YES
B.2.3.19.1.3	P-1	Digital Loop >= DS1/<10 circuits/Other/TN (days)				YES	0.00	0	0.00	0				YES
B.2.3.19.2.1	P-1	Digital Loop >= DS1/>=10 circuits/Facility/TN (days)					0.00	0						
B.2.3.19.2.2	P-1	Digital Loop >= DS1/>=10 circuits/Equipment/TN (days)					0.00	0						
B.2.3.19.2.3	P-1	Digital Loop >= DS1/>=10 circuits/Other/TN (days)					0.00	0						
		% Jeopardies - Mechanized												
B.2.5.1	P-2	Switch Ports/TN (%)					0.42%	292,644						
B.2.5.2	P-2	Local Interoffice Transport/TN (%)					42.48%	984						
B.2.5.3	P-2	Loop + Port Combinations/TN (%)		0.00086	2.3981	YES	0.42%	294,296	0.24%	7,501		0.00076	2.4264	YES
B.2.5.4	P-2	Combo Other/TN (%)		0.14658	-6.1743	NO	7.27%	22,027						
B.2.5.5	P-2	xDSL (ADSL, HDSL and UCL)/TN (%)		0.02480	0.2024	YES	6.52%	3,529						
B.2.5.6	P-2	UNE ISDN/TN (%)		0.03781	0.4803	YES	7.97%	841	20.45%	44		0.04188	-2.9821	NO
B.2.5.7	P-2	Line Sharing/TN (%)					6.52%	3,529	0.00%	1		0.24687	0.2640	YES
B.2.5.8	P-2	2W Analog Loop Design/TN (%)		0.00516	-13.7201	NO	0.42%	294,296	8.48%	165		0.00506	-15.9226	NO
B.2.5.9	P-2	2W Analog Loop Non-Design/TN (%)		0.01444	-9.0949	NO	0.77%	160,594	7.14%	28		0.01649	-3.8663	NO
B.2.5.10	P-2	2W Analog Loop w/INP Design/TN (%)					0.42%	294,296						
B.2.5.11	P-2	2W Analog Loop w/INP Non-Design/TN (%)					0.77%	160,594						
B.2.5.12	P-2	2W Analog Loop w/LNP Design/TN (%)		0.00716	-4.4540	NO	0.42%	294,296	0.00%	91		0.00682	0.6227	YES
B.2.5.13	P-2	2W Analog Loop w/LNP Non-Design/TN (%)					0.77%	160,594						
B.2.5.14	P-2	Other Design/TN (%)					21.61%	1,694						
B.2.5.15	P-2	Other Non-Design/TN (%)					0.42%	294,296						
B.2.5.16	P-2	INP (Standalone)/TN (%)					0.42%	292,644						
B.2.5.17	P-2	LNP (Standalone)/TN (%)		0.00443	0.9235	YES	0.42%	292,644	0.00%	148		0.00532	0.7908	YES
B.2.5.18	P-2	Digital Loop < DS1/TN (%)		0.02481	1.0401	YES	7.85%	4,724	20.45%	44		0.04074	-3.0928	NO
B.2.5.19	P-2	Digital Loop >= DS1/TN (%)		0.03686	-11.3425	NO	30.70%	417	70.27%	148		0.04413	-8.9676	NO
		% Jeopardies - Non-Mechanized												
B.2.6.1	P-2	Switch Ports/TN (%)				Diagnostic								Diagnostic
B.2.6.2	P-2	Local Interoffice Transport/TN (%)				Diagnostic			0.00%	11				Diagnostic
B.2.6.3	P-2	Loop + Port Combinations/TN (%)				Diagnostic			1.51%	332				Diagnostic
B.2.6.4	P-2	Combo Other/TN (%)				Diagnostic			72.09%	43				Diagnostic
B.2.6.5	P-2	xDSL (ADSL, HDSL and UCL)/TN (%)				Diagnostic			4.40%	91				Diagnostic
B.2.6.6	P-2	UNE ISDN/TN (%)				Diagnostic			0.00%	16				Diagnostic
B.2.6.7	P-2	Line Sharing/TN (%)				Diagnostic			0.00%	48				Diagnostic
B.2.6.8	P-2	2W Analog Loop Design/TN (%)				Diagnostic			5.88%	17				Diagnostic
B.2.6.9	P-2	2W Analog Loop Non-Design/TN (%)				Diagnostic								Diagnostic
B.2.6.10	P-2	2W Analog Loop w/INP Design/TN (%)				Diagnostic								Diagnostic
B.2.6.11	P-2	2W Analog Loop w/INP Non-Design/TN (%)				Diagnostic								Diagnostic
B.2.6.12	P-2	2W Analog Loop w/LNP Design/TN (%)				Diagnostic			0.00%	10				Diagnostic
B.2.6.13	P-2	2W Analog Loop w/LNP Non-Design/TN (%)				Diagnostic								Diagnostic
B.2.6.14	P-2	Other Design/TN (%)				Diagnostic								Diagnostic
B.2.6.15	P-2	Other Non-Design/TN (%)				Diagnostic			0.00%	1				Diagnostic
B.2.6.16	P-2	INP (Standalone)/TN (%)				Diagnostic								Diagnostic
B.2.6.17	P-2	LNP (Standalone)/TN (%)				Diagnostic			0.00%	212				Diagnostic
B.2.6.18	P-2	Digital Loop < DS1/TN (%)				Diagnostic			3.92%	102				Diagnostic
B.2.6.19	P-2	Digital Loop >= DS1/TN (%)				Diagnostic			66.67%	45				Diagnostic
		Average Jeopardy Notice Interval - Mechanized												
B.2.8.1	P-2	Switch Ports/TN (hours)												
B.2.8.2	P-2	Local Interoffice Transport/TN (hours)												
B.2.8.3	P-2	Loop + Port Combinations/TN (hours)				YES			178.62	14				YES
B.2.8.4	P-2	Combo Other/TN (hours)				YES								
B.2.8.5	P-2	xDSL (ADSL, HDSL and UCL)/TN (hours)				YES								
B.2.8.6	P-2	UNE ISDN/TN (hours)				YES			321.73	9				YES
B.2.8.7	P-2	Line Sharing/TN (hours)												
B.2.8.8	P-2	2W Analog Loop Design/TN (hours)				YES			187.30	14				YES
B.2.8.9	P-2	2W Analog Loop Non-Design/TN (hours)				YES			239.77	1				YES
B.2.8.10	P-2	2W Analog Loop w/INP Design/TN (hours)												
B.2.8.11	P-2	2W Analog Loop w/INP Non-Design/TN (hours)												
B.2.8.12	P-2	2W Analog Loop w/LNP Design/TN (hours)				YES								
B.2.8.13	P-2	2W Analog Loop w/LNP Non-Design/TN (hours)												
B.2.8.14	P-2	Other Design/TN (hours)												
B.2.8.15	P-2	Other Non-Design/TN (hours)												
B.2.8.16	P-2	INP (Standalone)/TN (hours)												

BellSouth Monthly Performance Summary

Tennessee, August 2002 - M ay 2002

			Benchmark/ Analog	August BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.8.17	P-2	LNP (Standalone)/TN (hours)	>= 48 hrs								
B.2.8.18	P-2	Digital Loop < DS1/TN (hours)	>= 48 hrs			389.49	5				YES
B.2.8.19	P-2	Digital Loop >= DS1/TN (hours)	>= 48 hrs			193.13	131				YES
		Average Jeopardy Notice Interval - Non-Mechanized									
B.2.9.1	P-2	Switch Ports/TN (hours)	Diagnostic								Diagnostic
B.2.9.2	P-2	Local Interoffice Transport/TN (hours)	Diagnostic								Diagnostic
B.2.9.3	P-2	Loop + Port Combinations/TN (hours)	Diagnostic			181.11	6				Diagnostic
B.2.9.4	P-2	Combo Other/TN (hours)	Diagnostic			332.52	11				Diagnostic
B.2.9.5	P-2	xDSL (ADSL, HDSL and UCL)/TN (hours)	Diagnostic			211.18	2				Diagnostic
B.2.9.6	P-2	UNE ISDN/TN (hours)	Diagnostic								Diagnostic
B.2.9.7	P-2	Line Sharing/TN (hours)	Diagnostic								Diagnostic
B.2.9.8	P-2	2W Analog Loop Design/TN (hours)	Diagnostic			132.03	1				Diagnostic
B.2.9.9	P-2	2W Analog Loop Non-Design/TN (hours)	Diagnostic								Diagnostic
B.2.9.10	P-2	2W Analog Loop w/INP Design/TN (hours)	Diagnostic								Diagnostic
B.2.9.11	P-2	2W Analog Loop w/INP Non-Design/TN (hours)	Diagnostic								Diagnostic
B.2.9.12	P-2	2W Analog Loop w/LNP Design/TN (hours)	Diagnostic								Diagnostic
B.2.9.13	P-2	2W Analog Loop w/LNP Non-Design/TN (hours)	Diagnostic								Diagnostic
B.2.9.14	P-2	Other Design/TN (hours)	Diagnostic								Diagnostic
B.2.9.15	P-2	Other Non-Design/TN (hours)	Diagnostic								Diagnostic
B.2.9.16	P-2	INP (Standalone)/TN (hours)	Diagnostic								Diagnostic
B.2.9.17	P-2	LNP (Standalone)/TN (hours)	Diagnostic								Diagnostic
B.2.9.18	P-2	Digital Loop < DS1/TN (hours)	Diagnostic			211.18	2				Diagnostic
B.2.9.19	P-2	Digital Loop >= DS1/TN (hours)	Diagnostic			201.67	12				Diagnostic
		% Jeopardy Notice >= 48 hours - Mechanized									
B.2.10.1	P-2	Switch Ports/TN (%)	95% >= 48 hrs								
B.2.10.2	P-2	Local Interoffice Transport/TN (%)	95% >= 48 hrs								
B.2.10.3	P-2	Loop + Port Combinations/TN (%)	95% >= 48 hrs			100.00%	20				YES
B.2.10.4	P-2	Combo Other/TN (%)	95% >= 48 hrs								
B.2.10.5	P-2	xDSL (ADSL, HDSL and UCL)/TN (%)	95% >= 48 hrs								
B.2.10.6	P-2	UNE ISDN/TN (%)	95% >= 48 hrs			100.00%	5				YES
B.2.10.7	P-2	Line Sharing/TN (%)	95% >= 48 hrs								
B.2.10.8	P-2	2W Analog Loop Design/TN (%)	95% >= 48 hrs			81.82%	11				NO
B.2.10.9	P-2	2W Analog Loop Non-Design/TN (%)	95% >= 48 hrs			100.00%	3				YES
B.2.10.10	P-2	2W Analog Loop w/INP Design/TN (%)	95% >= 48 hrs								
B.2.10.11	P-2	2W Analog Loop w/INP Non-Design/TN (%)	95% >= 48 hrs								
B.2.10.12	P-2	2W Analog Loop w/LNP Design/TN (%)	95% >= 48 hrs			100.00%	1				YES
B.2.10.13	P-2	2W Analog Loop w/LNP Non-Design/TN (%)	95% >= 48 hrs								
B.2.10.14	P-2	Other Design/TN (%)	95% >= 48 hrs								
B.2.10.15	P-2	Other Non-Design/TN (%)	95% >= 48 hrs								
B.2.10.16	P-2	INP (Standalone)/TN (%)	95% >= 48 hrs								
B.2.10.17	P-2	LNP (Standalone)/TN (%)	95% >= 48 hrs								
B.2.10.18	P-2	Digital Loop < DS1/TN (%)	95% >= 48 hrs			100.00%	5				YES
B.2.10.19	P-2	Digital Loop >= DS1/TN (%)	95% >= 48 hrs			100.00%	131				YES
		% Jeopardy Notice >= 48 hours - Non-Mechanized									
B.2.11.1	P-2	Switch Ports/TN (%)	Diagnostic								Diagnostic
B.2.11.2	P-2	Local Interoffice Transport/TN (%)	Diagnostic								Diagnostic
B.2.11.3	P-2	Loop + Port Combinations/TN (%)	Diagnostic			66.67%	6				Diagnostic
B.2.11.4	P-2	Combo Other/TN (%)	Diagnostic			100.00%	11				Diagnostic
B.2.11.5	P-2	xDSL (ADSL, HDSL and UCL)/TN (%)	Diagnostic			100.00%	2				Diagnostic
B.2.11.6	P-2	UNE ISDN/TN (%)	Diagnostic								Diagnostic
B.2.11.7	P-2	Line Sharing/TN (%)	Diagnostic								Diagnostic
B.2.11.8	P-2	2W Analog Loop Design/TN (%)	Diagnostic			100.00%	1				Diagnostic
B.2.11.9	P-2	2W Analog Loop Non-Design/TN (%)	Diagnostic								Diagnostic
B.2.11.10	P-2	2W Analog Loop w/INP Design/TN (%)	Diagnostic								Diagnostic
B.2.11.11	P-2	2W Analog Loop w/INP Non-Design/TN (%)	Diagnostic								Diagnostic
B.2.11.12	P-2	2W Analog Loop w/LNP Design/TN (%)	Diagnostic								Diagnostic
B.2.11.13	P-2	2W Analog Loop w/LNP Non-Design/TN (%)	Diagnostic								Diagnostic
B.2.11.14	P-2	Other Design/TN (%)	Diagnostic								Diagnostic
B.2.11.15	P-2	Other Non-Design/TN (%)	Diagnostic								Diagnostic
B.2.11.16	P-2	INP (Standalone)/TN (%)	Diagnostic								Diagnostic
B.2.11.17	P-2	LNP (Standalone)/TN (%)	Diagnostic								Diagnostic
B.2.11.18	P-2	Digital Loop < DS1/TN (%)	Diagnostic			100.00%	2				Diagnostic
B.2.11.19	P-2	Digital Loop >= DS1/TN (%)	Diagnostic			100.00%	12				Diagnostic
		Coordinated Customers Conversions									
B.2.12.1	P-7	Loops with INP/TN (%)	>= 95% w in 15 min								
B.2.12.2	P-7	Loops with LNP/TN (%)	>= 95% w in 15 min			96.76%	247				YES

BellSouth Monthly Performance Summary

Tennessee, August 2002 - May 2002

			July								Jul			
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.2.8.17	P-2	LNP (Standalone)/TN (hours)												
B.2.8.18	P-2	Digital Loop < DS1/TN (hours)			274.56	7				YES			181.71	5
B.2.8.19	P-2	Digital Loop >= DS1/TN (hours)			178.73	92				YES			185.46	145
		Average Jeopardy Notice Interval - Non-Mechanized												
B.2.9.1	P-2	Switch Ports/TN (hours)								Diagnostic				
B.2.9.2	P-2	Local Interoffice Transport/TN (hours)								Diagnostic				
B.2.9.3	P-2	Loop + Port Combinations/TN (hours)			121.35	5				Diagnostic			57.55	1
B.2.9.4	P-2	Combo Other/TN (hours)			319.24	17				Diagnostic			338.82	13
B.2.9.5	P-2	xDSL (ADSL, HDSL and UCL)/TN (hours)			443.60	1				Diagnostic			167.58	3
B.2.9.6	P-2	UNE ISDN/TN (hours)			341.93	2				Diagnostic			323.43	1
B.2.9.7	P-2	Line Sharing/TN (hours)								Diagnostic				
B.2.9.8	P-2	2W Analog Loop Design/TN (hours)								Diagnostic				
B.2.9.9	P-2	2W Analog Loop Non-Design/TN (hours)								Diagnostic				
B.2.9.10	P-2	2W Analog Loop w/INP Design/TN (hours)								Diagnostic				
B.2.9.11	P-2	2W Analog Loop w/INP Non-Design/TN (hours)								Diagnostic				
B.2.9.12	P-2	2W Analog Loop w/LNP Design/TN (hours)								Diagnostic				
B.2.9.13	P-2	2W Analog Loop w/LNP Non-Design/TN (hours)								Diagnostic				
B.2.9.14	P-2	Other Design/TN (hours)								Diagnostic				
B.2.9.15	P-2	Other Non-Design/TN (hours)								Diagnostic				
B.2.9.16	P-2	INP (Standalone)/TN (hours)								Diagnostic				
B.2.9.17	P-2	LNP (Standalone)/TN (hours)								Diagnostic				
B.2.9.18	P-2	Digital Loop < DS1/TN (hours)			375.82	3				Diagnostic			206.55	4
B.2.9.19	P-2	Digital Loop >= DS1/TN (hours)			254.59	24				Diagnostic			200.89	19
		% Jeopardy Notice >= 48 hours - Mechanized												
B.2.10.1	P-2	Switch Ports/TN (%)												
B.2.10.2	P-2	Local Interoffice Transport/TN (%)												
B.2.10.3	P-2	Loop + Port Combinations/TN (%)			100.00%	8				YES			100.00%	8
B.2.10.4	P-2	Combo Other/TN (%)			100.00%	2				YES			100.00%	4
B.2.10.5	P-2	xDSL (ADSL, HDSL and UCL)/TN (%)											100.00%	3
B.2.10.6	P-2	UNE ISDN/TN (%)			100.00%	7				YES			50.00%	2
B.2.10.7	P-2	Line Sharing/TN (%)												
B.2.10.8	P-2	2W Analog Loop Design/TN (%)			100.00%	7				YES			100.00%	12
B.2.10.9	P-2	2W Analog Loop Non-Design/TN (%)			100.00%	4				YES			100.00%	4
B.2.10.10	P-2	2W Analog Loop w/INP Design/TN (%)												
B.2.10.11	P-2	2W Analog Loop w/INP Non-Design/TN (%)												
B.2.10.12	P-2	2W Analog Loop w/LNP Design/TN (%)			100.00%	2				YES			100.00%	3
B.2.10.13	P-2	2W Analog Loop w/LNP Non-Design/TN (%)												
B.2.10.14	P-2	Other Design/TN (%)												
B.2.10.15	P-2	Other Non-Design/TN (%)												
B.2.10.16	P-2	INP (Standalone)/TN (%)												
B.2.10.17	P-2	LNP (Standalone)/TN (%)												
B.2.10.18	P-2	Digital Loop < DS1/TN (%)			100.00%	7				YES			80.00%	5
B.2.10.19	P-2	Digital Loop >= DS1/TN (%)			100.00%	92				YES			100.00%	145
		% Jeopardy Notice >= 48 hours - Non-Mechanized												
B.2.11.1	P-2	Switch Ports/TN (%)								Diagnostic				
B.2.11.2	P-2	Local Interoffice Transport/TN (%)								Diagnostic				
B.2.11.3	P-2	Loop + Port Combinations/TN (%)			100.00%	5				Diagnostic			100.00%	1
B.2.11.4	P-2	Combo Other/TN (%)			100.00%	17				Diagnostic			100.00%	13
B.2.11.5	P-2	xDSL (ADSL, HDSL and UCL)/TN (%)			100.00%	1				Diagnostic			100.00%	3
B.2.11.6	P-2	UNE ISDN/TN (%)			100.00%	2				Diagnostic			100.00%	1
B.2.11.7	P-2	Line Sharing/TN (%)								Diagnostic				
B.2.11.8	P-2	2W Analog Loop Design/TN (%)								Diagnostic				
B.2.11.9	P-2	2W Analog Loop Non-Design/TN (%)								Diagnostic				
B.2.11.10	P-2	2W Analog Loop w/INP Design/TN (%)								Diagnostic				
B.2.11.11	P-2	2W Analog Loop w/INP Non-Design/TN (%)								Diagnostic				
B.2.11.12	P-2	2W Analog Loop w/LNP Design/TN (%)								Diagnostic				
B.2.11.13	P-2	2W Analog Loop w/LNP Non-Design/TN (%)								Diagnostic				
B.2.11.14	P-2	Other Design/TN (%)								Diagnostic				
B.2.11.15	P-2	Other Non-Design/TN (%)								Diagnostic				
B.2.11.16	P-2	INP (Standalone)/TN (%)								Diagnostic				
B.2.11.17	P-2	LNP (Standalone)/TN (%)								Diagnostic				
B.2.11.18	P-2	Digital Loop < DS1/TN (%)			100.00%	3				Diagnostic			100.00%	4
B.2.11.19	P-2	Digital Loop >= DS1/TN (%)			100.00%	24				Diagnostic			100.00%	19
		Coordinated Customers Conversions												
B.2.12.1	P-7	Loops with INP/TN (%)												
B.2.12.2	P-7	Loops with LNP/TN (%)			100.00%	221				YES			100.00%	302

BellSouth Monthly Performance Summary

Tennessee, August 2002 - May 2002

			1e				May							
			Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.8.17	P-2	LNP (Standalone)/TN (hours)												
B.2.8.18	P-2	Digital Loop < DS1/TN (hours)				YES			321.73	9				YES
B.2.8.19	P-2	Digital Loop >= DS1/TN (hours)				YES			194.06	104				YES
	Average Jeopardy Notice Interval - Non-Mechanized													
B.2.9.1	P-2	Switch Ports/TN (hours)				Diagnostic								Diagnostic
B.2.9.2	P-2	Local Interoffice Transport/TN (hours)				Diagnostic								Diagnostic
B.2.9.3	P-2	Loop + Port Combinations/TN (hours)				Diagnostic			107.76	4				Diagnostic
B.2.9.4	P-2	Combo Other/TN (hours)				Diagnostic			318.76	30				Diagnostic
B.2.9.5	P-2	xDSL (ADSL, HDSL and UCL)/TN (hours)				Diagnostic			170.69	3				Diagnostic
B.2.9.6	P-2	UNE ISDN/TN (hours)				Diagnostic								Diagnostic
B.2.9.7	P-2	Line Sharing/TN (hours)				Diagnostic								Diagnostic
B.2.9.8	P-2	2W Analog Loop Design/TN (hours)				Diagnostic			84.57	1				Diagnostic
B.2.9.9	P-2	2W Analog Loop Non-Design/TN (hours)				Diagnostic								Diagnostic
B.2.9.10	P-2	2W Analog Loop w/INP Design/TN (hours)				Diagnostic								Diagnostic
B.2.9.11	P-2	2W Analog Loop w/INP Non-Design/TN (hours)				Diagnostic								Diagnostic
B.2.9.12	P-2	2W Analog Loop w/LNP Design/TN (hours)				Diagnostic								Diagnostic
B.2.9.13	P-2	2W Analog Loop w/LNP Non-Design/TN (hours)				Diagnostic								Diagnostic
B.2.9.14	P-2	Other Design/TN (hours)				Diagnostic								Diagnostic
B.2.9.15	P-2	Other Non-Design/TN (hours)				Diagnostic								Diagnostic
B.2.9.16	P-2	INP (Standalone)/TN (hours)				Diagnostic								Diagnostic
B.2.9.17	P-2	LNP (Standalone)/TN (hours)				Diagnostic								Diagnostic
B.2.9.18	P-2	Digital Loop < DS1/TN (hours)				Diagnostic			170.69	3				Diagnostic
B.2.9.19	P-2	Digital Loop >= DS1/TN (hours)				Diagnostic			294.42	30				Diagnostic
	% Jeopardy Notice >= 48 hours - Mechanized													
B.2.10.1	P-2	Switch Ports/TN (%)												
B.2.10.2	P-2	Local Interoffice Transport/TN (%)												
B.2.10.3	P-2	Loop + Port Combinations/TN (%)				YES			100.00%	14				YES
B.2.10.4	P-2	Combo Other/TN (%)				YES								
B.2.10.5	P-2	xDSL (ADSL, HDSL and UCL)/TN (%)				YES								
B.2.10.6	P-2	UNE ISDN/TN (%)				NO			100.00%	9				YES
B.2.10.7	P-2	Line Sharing/TN (%)												
B.2.10.8	P-2	2W Analog Loop Design/TN (%)				YES			100.00%	14				YES
B.2.10.9	P-2	2W Analog Loop Non-Design/TN (%)				YES			100.00%	1				YES
B.2.10.10	P-2	2W Analog Loop w/INP Design/TN (%)												
B.2.10.11	P-2	2W Analog Loop w/INP Non-Design/TN (%)												
B.2.10.12	P-2	2W Analog Loop w/LNP Design/TN (%)				YES								
B.2.10.13	P-2	2W Analog Loop w/LNP Non-Design/TN (%)												
B.2.10.14	P-2	Other Design/TN (%)												
B.2.10.15	P-2	Other Non-Design/TN (%)												
B.2.10.16	P-2	INP (Standalone)/TN (%)												
B.2.10.17	P-2	LNP (Standalone)/TN (%)												
B.2.10.18	P-2	Digital Loop < DS1/TN (%)				NO			100.00%	9				YES
B.2.10.19	P-2	Digital Loop >= DS1/TN (%)				YES			100.00%	104				YES
	% Jeopardy Notice >= 48 hours - Non-Mechanized													
B.2.11.1	P-2	Switch Ports/TN (%)				Diagnostic								Diagnostic
B.2.11.2	P-2	Local Interoffice Transport/TN (%)				Diagnostic								Diagnostic
B.2.11.3	P-2	Loop + Port Combinations/TN (%)				Diagnostic			75.00%	4				Diagnostic
B.2.11.4	P-2	Combo Other/TN (%)				Diagnostic			100.00%	30				Diagnostic
B.2.11.5	P-2	xDSL (ADSL, HDSL and UCL)/TN (%)				Diagnostic			100.00%	3				Diagnostic
B.2.11.6	P-2	UNE ISDN/TN (%)				Diagnostic								Diagnostic
B.2.11.7	P-2	Line Sharing/TN (%)				Diagnostic								Diagnostic
B.2.11.8	P-2	2W Analog Loop Design/TN (%)				Diagnostic			100.00%	1				Diagnostic
B.2.11.9	P-2	2W Analog Loop Non-Design/TN (%)				Diagnostic								Diagnostic
B.2.11.10	P-2	2W Analog Loop w/INP Design/TN (%)				Diagnostic								Diagnostic
B.2.11.11	P-2	2W Analog Loop w/INP Non-Design/TN (%)				Diagnostic								Diagnostic
B.2.11.12	P-2	2W Analog Loop w/LNP Design/TN (%)				Diagnostic								Diagnostic
B.2.11.13	P-2	2W Analog Loop w/LNP Non-Design/TN (%)				Diagnostic								Diagnostic
B.2.11.14	P-2	Other Design/TN (%)				Diagnostic								Diagnostic
B.2.11.15	P-2	Other Non-Design/TN (%)				Diagnostic								Diagnostic
B.2.11.16	P-2	INP (Standalone)/TN (%)				Diagnostic								Diagnostic
B.2.11.17	P-2	LNP (Standalone)/TN (%)				Diagnostic								Diagnostic
B.2.11.18	P-2	Digital Loop < DS1/TN (%)				Diagnostic			100.00%	3				Diagnostic
B.2.11.19	P-2	Digital Loop >= DS1/TN (%)				Diagnostic			100.00%	30				Diagnostic
	Coordinated Customers Conversions													
B.2.12.1	P-7	Loops with INP/TN (%)												
B.2.12.2	P-7	Loops with LNP/TN (%)				YES			100.00%	286				YES

BellSouth Monthly Performance Summary

Tennessee, August 2002 - M ay 2002

			Benchmark/ Analog	August BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
		% Hot Cuts > 15 minutes Early									
B.2.13.1	P-7A	Time-Specific SL1/TN (%)	<= 5%								
B.2.13.2	P-7A	Time-Specific SL2/TN (%)	<= 5%			0.00%	64				YES
B.2.13.3	P-7A	Non-Time Specific SL1/TN (%)	<= 5%								
B.2.13.4	P-7A	Non-Time Specific SL2/TN (%)	<= 5%			0.00%	9				YES
		Hot Cut Timeliness									
B.2.14.1	P-7A	Time-Specific SL1/TN (%)	>= 95% w in 15 min								
B.2.14.2	P-7A	Time-Specific SL2/TN (%)	>= 95% w in 15 min			100.00%	64				YES
B.2.14.3	P-7A	Non-Time Specific SL1/TN (%)	>= 95% w in 15 min								
B.2.14.4	P-7A	Non-Time Specific SL2/TN (%)	>= 95% w in 15 min			100.00%	9				YES
		% Hot Cuts > 15 minutes Late									
B.2.15.1	P-7A	Time-Specific SL1/TN (%)	<= 5%								
B.2.15.2	P-7A	Time-Specific SL2/TN (%)	<= 5%			0.00%	64				YES
B.2.15.3	P-7A	Non-Time Specific SL1/TN (%)	<= 5%								
B.2.15.4	P-7A	Non-Time Specific SL2/TN (%)	<= 5%			0.00%	9				YES
		Average Recovery Time - CCC									
B.2.16.1	P-7B	Loops with INP/TN (minutes)	Diagnostic								Diagnostic
B.2.16.2	P-7B	Loops with LNP/TN (minutes)	Diagnostic			117.20	5				Diagnostic
		% Provisioning Troubles within 7 Days - Hot Cuts									
B.2.17.1.1	P-7C	UNE Loop Design/Dispatch/TN (%)	<= 5%			1.37%	219				YES
B.2.17.1.2	P-7C	UNE Loop Design/Non-Dispatch/TN (%)	<= 5%			0.00%	2				YES
B.2.17.2.1	P-7C	UNE Loop Non-Design/Dispatch/TN (%)	<= 5%								
B.2.17.2.2	P-7C	UNE Loop Non-Design/Non-Dispatch/TN (%)	<= 5%			0.00%	1				YES
		% Missed Installation Appointments									
B.2.18.1.1.1	P-3	Switch Ports/<10 circuits/Dispatch/TN (%)	R&B (POTS)	6.13%	19,243						
B.2.18.1.1.2	P-3	Switch Ports/<10 circuits/Non-Dispatch/TN (%)	R&B (POTS)	0.04%	291,471						
B.2.18.1.2.1	P-3	Switch Ports/>=10 circuits/Dispatch/TN (%)	R&B (POTS)	7.87%	89						
B.2.18.1.2.2	P-3	Switch Ports/>=10 circuits/Non-Dispatch/TN (%)	R&B (POTS)	0.00%	4						
B.2.18.2.1.1	P-3	Local Interoffice Transport/<10 circuits/Dispatch/TN (%)	DS1/DS3	1.37%	873						
B.2.18.2.1.2	P-3	Local Interoffice Transport/<10 circuits/Non-Dispatch/TN (%)	DS1/DS3								
B.2.18.2.2.1	P-3	Local Interoffice Transport/>=10 circuits/Dispatch/TN (%)	DS1/DS3								
B.2.18.2.2.2	P-3	Local Interoffice Transport/>=10 circuits/Non-Dispatch/TN (%)	DS1/DS3								
B.2.18.3.1.1	P-3	Loop + Port Combinations/<10 circuits/Dispatch/TN (%)	R&B	6.00%	19,759	5.35%	374		0.01239	0.5242	YES
B.2.18.3.1.2	P-3	Loop + Port Combinations/<10 circuits/Non-Dispatch/TN (%)	R&B	0.04%	292,699	0.05%	20,915		0.00015	-0.3453	YES
B.2.18.3.1.3	P-3	Loop + Port Combinations/<10 circuits/Switch Based Orders/TN (%)	R&B	0.00%	141,103	0.00%	2,747		0.00000		YES
B.2.18.3.1.4	P-3	Loop + Port Combinations/<10 circuits/Dispatch In/TN (%)	R&B	0.08%	151,596	0.06%	18,168		0.00023	1.2165	YES
B.2.18.3.2.1	P-3	Loop + Port Combinations/>=10 circuits/Dispatch/TN (%)	R&B	7.62%	105	0.00%	2		0.18938	0.4023	YES
B.2.18.3.2.2	P-3	Loop + Port Combinations/>=10 circuits/Non-Dispatch/TN (%)	R&B	0.00%	66	0.00%	1		0.00000		YES
B.2.18.3.2.3	P-3	Loop + Port Combinations/>=10 circuits/Switch Based Orders/TN (%)	R&B	0.00%	14						
B.2.18.3.2.4	P-3	Loop + Port Combinations/>=10 circuits/Dispatch In/TN (%)	R&B	0.00%	52	0.00%	1		0.00000		YES
B.2.18.4.1.1	P-3	Combo Other/<10 circuits/Dispatch/TN (%)	R&B&D - Disp	5.92%	21,196	0.00%	30		0.04310	1.3725	YES
B.2.18.4.1.4	P-3	Combo Other/<10 circuits/Dispatch In/TN (%)	R&B&D - Disp	5.92%	21,196						
B.2.18.4.2.1	P-3	Combo Other/>=10 circuits/Dispatch/TN (%)	R&B&D - Disp	4.82%	228						
B.2.18.4.2.4	P-3	Combo Other/>=10 circuits/Dispatch In/TN (%)	R&B&D - Disp	4.82%	228						
B.2.18.5.1.1	P-3	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/TN (%)	ADSL to Retail	5.12%	1,659	1.35%	74		0.02620	1.4400	YES
B.2.18.5.1.2	P-3	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/TN (%)	ADSL to Retail	0.00%	2,460						
B.2.18.5.2.1	P-3	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/TN (%)	ADSL to Retail	25.00%	4						
B.2.18.5.2.2	P-3	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/TN (%)	ADSL to Retail	0.00%	1						
B.2.18.6.1.1	P-3	UNE ISDN/<10 circuits/Dispatch/TN (%)	ISDN - BRI	4.11%	487	2.86%	35		0.03473	0.3598	YES
B.2.18.6.1.2	P-3	UNE ISDN/<10 circuits/Non-Dispatch/TN (%)	ISDN - BRI	0.19%	528						
B.2.18.6.2.1	P-3	UNE ISDN/>=10 circuits/Dispatch/TN (%)	ISDN - BRI								
B.2.18.6.2.2	P-3	UNE ISDN/>=10 circuits/Non-Dispatch/TN (%)	ISDN - BRI								
B.2.18.7.1.1	P-3	Line Sharing/<10 circuits/Dispatch/TN (%)	ADSL to Retail	5.12%	1,659	0.00%	3		0.12741	0.4021	YES
B.2.18.7.1.2	P-3	Line Sharing/<10 circuits/Non-Dispatch/TN (%)	ADSL to Retail	0.00%	2,460	1.96%	51		0.00000		NO
B.2.18.7.2.1	P-3	Line Sharing/>=10 circuits/Dispatch/TN (%)	ADSL to Retail	25.00%	4						
B.2.18.7.2.2	P-3	Line Sharing/>=10 circuits/Non-Dispatch/TN (%)	ADSL to Retail	0.00%	1						
B.2.18.8.1.1	P-3	2W Analog Loop Design/<10 circuits/Dispatch/TN (%)	R&B - Disp	6.00%	19,759	0.86%	116		0.02211	2.3226	YES
B.2.18.8.1.2	P-3	2W Analog Loop Design/<10 circuits/Non-Dispatch/TN (%)	R&B - Disp	6.00%	19,759						
B.2.18.8.2.1	P-3	2W Analog Loop Design/>=10 circuits/Dispatch/TN (%)	R&B - Disp	7.62%	105						
B.2.18.8.2.2	P-3	2W Analog Loop Design/>=10 circuits/Non-Dispatch/TN (%)	R&B - Disp	7.62%	105						
B.2.18.9.1.1	P-3	2W Analog Loop Non-Design/<10 circuits/Dispatch/TN (%)	R&B (POTS) excl SB Or	6.13%	19,243	2.00%	50		0.03397	1.2163	YES
B.2.18.9.1.4	P-3	2W Analog Loop Non-Design/<10 circuits/Dispatch In/TN (%)	R&B (POTS) excl SB Or	0.08%	150,840	0.00%	1		0.02878	0.0288	YES
B.2.18.9.2.1	P-3	2W Analog Loop Non-Design/>=10 circuits/Dispatch/TN (%)	R&B (POTS) excl SB Or	7.87%	89						
B.2.18.9.2.4	P-3	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/TN (%)	R&B (POTS) excl SB Or	0.00%	2						
B.2.18.10.1.1	P-3	2W Analog Loop w/INP Design/<10 circuits/Dispatch/TN (%)	R&B - Disp	6.00%	19,759						

BellSouth Monthly Performance Summary

Tennessee, August 2002 - M ay 2002

			July								Jul			
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume
		% Hot Cuts > 15 minutes Early												
B.2.13.1	P-7A	Time-Specific SL1/TN (%)												
B.2.13.2	P-7A	Time-Specific SL2/TN (%)			0.00%	50				YES			0.00%	80
B.2.13.3	P-7A	Non-Time Specific SL1/TN (%)												
B.2.13.4	P-7A	Non-Time Specific SL2/TN (%)			0.00%	17				YES			0.00%	18
		Hot Cut Timeliness												
B.2.14.1	P-7A	Time-Specific SL1/TN (%)												
B.2.14.2	P-7A	Time-Specific SL2/TN (%)			100.00%	50				YES			97.50%	80
B.2.14.3	P-7A	Non-Time Specific SL1/TN (%)												
B.2.14.4	P-7A	Non-Time Specific SL2/TN (%)			100.00%	17				YES			100.00%	18
		% Hot Cuts > 15 minutes Late												
B.2.15.1	P-7A	Time-Specific SL1/TN (%)												
B.2.15.2	P-7A	Time-Specific SL2/TN (%)			0.00%	50				YES			2.50%	80
B.2.15.3	P-7A	Non-Time Specific SL1/TN (%)												
B.2.15.4	P-7A	Non-Time Specific SL2/TN (%)			0.00%	17				YES			0.00%	18
		Average Recovery Time - CCC												
B.2.16.1	P-7B	Loops with INP/TN (minutes)								Diagnostic				
B.2.16.2	P-7B	Loops with LNP/TN (minutes)			34.50	2				Diagnostic			38.67	3
		% Provisioning Troubles within 7 Days - Hot Cuts												
B.2.17.1.1	P-7C	UNE Loop Design/Dispatch/TN (%)			6.27%	303				NO			1.75%	286
B.2.17.1.2	P-7C	UNE Loop Design/Non-Dispatch/TN (%)												
B.2.17.2.1	P-7C	UNE Loop Non-Design/Dispatch/TN (%)												
B.2.17.2.2	P-7C	UNE Loop Non-Design/Non-Dispatch/TN (%)												
		% Missed Installation Appointments												
B.2.18.1.1.1	P-3	Switch Ports/<10 circuits/Dispatch/TN (%)	8.52%	17,528							6.79%	16,027		
B.2.18.1.1.2	P-3	Switch Ports/<10 circuits/Non-Dispatch/TN (%)	0.04%	274,116							0.02%	250,870		
B.2.18.1.2.1	P-3	Switch Ports/>=10 circuits/Dispatch/TN (%)	10.96%	73							2.86%	70		
B.2.18.1.2.2	P-3	Switch Ports/>=10 circuits/Non-Dispatch/TN (%)	0.00%	2										
B.2.18.2.1.1	P-3	Local Interoffice Transport/<10 circuits/Dispatch/TN (%)	1.96%	1,019							0.76%	793		
B.2.18.2.1.2	P-3	Local Interoffice Transport/<10 circuits/Non-Dispatch/TN (%)												
B.2.18.2.2.1	P-3	Local Interoffice Transport/>=10 circuits/Dispatch/TN (%)	0.00%	1										
B.2.18.2.2.2	P-3	Local Interoffice Transport/>=10 circuits/Non-Dispatch/TN (%)												
B.2.18.3.1.1	P-3	Loop + Port Combinations/<10 circuits/Dispatch/TN (%)	8.45%	17,863	4.97%	342		0.01518	2.2902	YES	6.67%	16,359	3.95%	329
B.2.18.3.1.2	P-3	Loop + Port Combinations/<10 circuits/Non-Dispatch/TN (%)	0.04%	276,311	0.05%	9,853		0.00020	-0.5347	YES	0.02%	252,009	0.03%	5,882
B.2.18.3.1.3	P-3	Loop + Port Combinations/<10 circuits/Switch Based Orders/TN (%)	0.00%	133,339	0.00%	2,797		0.00000		YES	0.00%	123,120	0.00%	2,094
B.2.18.3.1.4	P-3	Loop + Port Combinations/<10 circuits/Dispatch In/TN (%)	0.08%	142,972	0.07%	7,056		0.00034	0.1797	YES	0.05%	128,889	0.05%	3,788
B.2.18.3.2.1	P-3	Loop + Port Combinations/>=10 circuits/Dispatch/TN (%)	9.88%	81	0.00%	3		0.17541	0.5631	YES	4.39%	114	0.00%	8
B.2.18.3.2.2	P-3	Loop + Port Combinations/>=10 circuits/Non-Dispatch/TN (%)	0.00%	47							0.00%	35		
B.2.18.3.2.3	P-3	Loop + Port Combinations/>=10 circuits/Switch Based Orders/TN (%)	0.00%	10							0.00%	13		
B.2.18.3.2.4	P-3	Loop + Port Combinations/>=10 circuits/Dispatch In/TN (%)	0.00%	37							0.00%	22		
B.2.18.4.1.1	P-3	Combo Other/<10 circuits/Dispatch/TN (%)	8.13%	19,481	10.53%	38		0.04438	-0.5397	YES	6.52%	17,835	8.33%	36
B.2.18.4.1.4	P-3	Combo Other/<10 circuits/Dispatch In/TN (%)	8.13%	19,481							6.52%	17,835		
B.2.18.4.2.1	P-3	Combo Other/>=10 circuits/Dispatch/TN (%)	9.52%	84							5.13%	117		
B.2.18.4.2.4	P-3	Combo Other/>=10 circuits/Dispatch In/TN (%)	9.52%	84							5.13%	117		
B.2.18.5.1.1	P-3	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/TN (%)	6.77%	1,655	0.00%	82		0.02842	2.3814	YES	5.56%	1,620	0.00%	113
B.2.18.5.1.2	P-3	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/TN (%)	0.00%	2,187							0.00%	2,107		
B.2.18.5.2.1	P-3	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/TN (%)	0.00%	3							0.00%	2		
B.2.18.5.2.2	P-3	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/TN (%)	0.00%	2							0.00%	2		
B.2.18.6.1.1	P-3	UNE ISDN/<10 circuits/Dispatch/TN (%)	7.80%	262	4.84%	62		0.03762	0.7876	YES	6.34%	268	3.64%	55
B.2.18.6.1.2	P-3	UNE ISDN/<10 circuits/Non-Dispatch/TN (%)	0.83%	600							1.48%	540		
B.2.18.6.2.1	P-3	UNE ISDN/>=10 circuits/Dispatch/TN (%)	0.00%	1										
B.2.18.6.2.2	P-3	UNE ISDN/>=10 circuits/Non-Dispatch/TN (%)												
B.2.18.7.1.1	P-3	Line Sharing/<10 circuits/Dispatch/TN (%)	6.77%	1,655	0.00%	7		0.09514	0.7113	YES	5.56%	1,620	0.00%	5
B.2.18.7.1.2	P-3	Line Sharing/<10 circuits/Non-Dispatch/TN (%)	0.00%	2,187	0.00%	48		0.00000		YES	0.00%	2,107	2.63%	38
B.2.18.7.2.1	P-3	Line Sharing/>=10 circuits/Dispatch/TN (%)	0.00%	3							0.00%	2		
B.2.18.7.2.2	P-3	Line Sharing/>=10 circuits/Non-Dispatch/TN (%)	0.00%	2							0.00%	2		
B.2.18.8.1.1	P-3	2W Analog Loop Design/<10 circuits/Dispatch/TN (%)	8.45%	17,863	2.31%	130		0.02448	2.5082	YES	6.67%	16,359	2.20%	182
B.2.18.8.1.2	P-3	2W Analog Loop Design/<10 circuits/Non-Dispatch/TN (%)	8.45%	17,863							6.67%	16,359		
B.2.18.8.2.1	P-3	2W Analog Loop Design/>=10 circuits/Dispatch/TN (%)	9.88%	81							4.39%	114		
B.2.18.8.2.2	P-3	2W Analog Loop Design/>=10 circuits/Non-Dispatch/TN (%)	9.88%	81							4.39%	114		
B.2.18.9.1.1	P-3	2W Analog Loop Non-Design/<10 circuits/Dispatch/TN (%)	8.52%	17,528	4.08%	49		0.03995	1.1120	YES	6.79%	16,027	5.56%	36
B.2.18.9.1.4	P-3	2W Analog Loop Non-Design/<10 circuits/Dispatch In/TN (%)	0.08%	141,246							0.04%	128,242		
B.2.18.9.2.1	P-3	2W Analog Loop Non-Design/>=10 circuits/Dispatch/TN (%)	10.96%	73							2.86%	70		
B.2.18.9.2.4	P-3	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/TN (%)												
B.2.18.10.1.1	P-3	2W Analog Loop w/INP Design/<10 circuits/Dispatch/TN (%)	8.45%	17,863							6.67%	16,359		

BellSouth Monthly Performance Summary
Tennessee, August 2002 - May 2002

			ne				May							
			Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
	% Hot Cuts > 15 minutes Early													
B.2.13.1	P-7A	Time-Specific SL1/TN (%)												
B.2.13.2	P-7A	Time-Specific SL2/TN (%)				YES			2.99%	67				YES
B.2.13.3	P-7A	Non-Time Specific SL1/TN (%)												
B.2.13.4	P-7A	Non-Time Specific SL2/TN (%)				YES			0.00%	11				YES
	Hot Cut Timeliness													
B.2.14.1	P-7A	Time-Specific SL1/TN (%)												
B.2.14.2	P-7A	Time-Specific SL2/TN (%)				YES			97.01%	67				YES
B.2.14.3	P-7A	Non-Time Specific SL1/TN (%)												
B.2.14.4	P-7A	Non-Time Specific SL2/TN (%)				YES			100.00%	11				YES
	% Hot Cuts > 15 minutes Late													
B.2.15.1	P-7A	Time-Specific SL1/TN (%)												
B.2.15.2	P-7A	Time-Specific SL2/TN (%)				YES			0.00%	67				YES
B.2.15.3	P-7A	Non-Time Specific SL1/TN (%)												
B.2.15.4	P-7A	Non-Time Specific SL2/TN (%)				YES			0.00%	11				YES
	Average Recovery Time - CCC													
B.2.16.1	P-7B	Loops with INP/TN (minutes)				Diagnostic								Diagnostic
B.2.16.2	P-7B	Loops with LNP/TN (minutes)				Diagnostic								Diagnostic
	% Provisioning Troubles within 7 Days - Hot Cuts													
B.2.17.1.1	P-7C	UNE Loop Design/Dispatch/TN (%)				YES			3.14%	223				YES
B.2.17.1.2	P-7C	UNE Loop Design/Non-Dispatch/TN (%)												
B.2.17.2.1	P-7C	UNE Loop Non-Design/Dispatch/TN (%)												
B.2.17.2.2	P-7C	UNE Loop Non-Design/Non-Dispatch/TN (%)												
	% Missed Installation Appointments													
B.2.18.1.1.1	P-3	Switch Ports/<10 circuits/Dispatch/TN (%)					5.93%	20,002						
B.2.18.1.1.2	P-3	Switch Ports/<10 circuits/Non-Dispatch/TN (%)					0.03%	272,690						
B.2.18.1.2.1	P-3	Switch Ports/>=10 circuits/Dispatch/TN (%)					9.80%	102						
B.2.18.1.2.2	P-3	Switch Ports/>=10 circuits/Non-Dispatch/TN (%)					0.00%	10						
B.2.18.2.1.1	P-3	Local Interoffice Transport/<10 circuits/Dispatch/TN (%)					1.46%	888	0.00%	11		0.03644	0.4018	YES
B.2.18.2.1.2	P-3	Local Interoffice Transport/<10 circuits/Non-Dispatch/TN (%)												
B.2.18.2.2.1	P-3	Local Interoffice Transport/>=10 circuits/Dispatch/TN (%)					0.00%	1						
B.2.18.2.2.2	P-3	Local Interoffice Transport/>=10 circuits/Non-Dispatch/TN (%)												
B.2.18.3.1.1	P-3	Loop + Port Combinations/<10 circuits/Dispatch/TN (%)		0.01389	1.9563	YES	5.88%	20,295	4.87%	349		0.01270	0.7967	YES
B.2.18.3.1.2	P-3	Loop + Port Combinations/<10 circuits/Non-Dispatch/TN (%)		0.00020	-0.5009	YES	0.03%	273,948	0.09%	7,476		0.00019	-3.5445	NO
B.2.18.3.1.3	P-3	Loop + Port Combinations/<10 circuits/Switch Based Orders/TN (%)		0.00000		YES	0.00%	132,674	0.00%	2,490		0.00000		YES
B.2.18.3.1.4	P-3	Loop + Port Combinations/<10 circuits/Dispatch In/TN (%)		0.00036	-0.1757	YES	0.05%	141,274	0.14%	4,986		0.00033	-2.7498	NO
B.2.18.3.2.1	P-3	Loop + Port Combinations/>=10 circuits/Dispatch/TN (%)		0.07490	0.5856	YES	8.85%	113	14.29%	7		0.11062	-0.4914	YES
B.2.18.3.2.2	P-3	Loop + Port Combinations/>=10 circuits/Non-Dispatch/TN (%)					0.00%	65	0.00%	3		0.00000		YES
B.2.18.3.2.3	P-3	Loop + Port Combinations/>=10 circuits/Switch Based Orders/TN (%)					0.00%	12						
B.2.18.3.2.4	P-3	Loop + Port Combinations/>=10 circuits/Dispatch In/TN (%)					0.00%	53	0.00%	3		0.00000		YES
B.2.18.4.1.1	P-3	Combo Other/<10 circuits/Dispatch/TN (%)		0.04119	-0.4400	YES	5.78%	21,694	2.44%	41		0.03647	0.9150	YES
B.2.18.4.1.4	P-3	Combo Other/<10 circuits/Dispatch In/TN (%)					5.78%	21,694						
B.2.18.4.2.1	P-3	Combo Other/>=10 circuits/Dispatch/TN (%)					8.77%	114						
B.2.18.4.2.4	P-3	Combo Other/>=10 circuits/Dispatch In/TN (%)					8.77%	114						
B.2.18.5.1.1	P-3	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/TN (%)		0.02229	2.4927	YES	4.24%	1,555	1.12%	89		0.02197	1.4203	YES
B.2.18.5.1.2	P-3	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/TN (%)					0.00%	1,955						
B.2.18.5.2.1	P-3	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/TN (%)					0.00%	4						
B.2.18.5.2.2	P-3	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/TN (%)					0.00%	1						
B.2.18.6.1.1	P-3	UNE ISDN/<10 circuits/Dispatch/TN (%)		0.03608	0.7502	YES	7.23%	332	4.84%	62		0.03583	0.6671	YES
B.2.18.6.1.2	P-3	UNE ISDN/<10 circuits/Non-Dispatch/TN (%)					0.99%	506						
B.2.18.6.2.1	P-3	UNE ISDN/>=10 circuits/Dispatch/TN (%)												
B.2.18.6.2.2	P-3	UNE ISDN/>=10 circuits/Non-Dispatch/TN (%)												
B.2.18.7.1.1	P-3	Line Sharing/<10 circuits/Dispatch/TN (%)		0.10260	0.5415	YES	4.24%	1,555	20.00%	5		0.09030	-1.7448	NO
B.2.18.7.1.2	P-3	Line Sharing/<10 circuits/Non-Dispatch/TN (%)		0.00000		NO	0.00%	1,955	0.00%	45		0.00000		YES
B.2.18.7.2.1	P-3	Line Sharing/>=10 circuits/Dispatch/TN (%)					0.00%	4						
B.2.18.7.2.2	P-3	Line Sharing/>=10 circuits/Non-Dispatch/TN (%)					0.00%	1						
B.2.18.8.1.1	P-3	2W Analog Loop Design/<10 circuits/Dispatch/TN (%)		0.01860	2.4045	YES	5.88%	20,295	2.40%	167		0.01828	1.9077	YES
B.2.18.8.1.2	P-3	2W Analog Loop Design/<10 circuits/Non-Dispatch/TN (%)					5.88%	20,295						
B.2.18.8.2.1	P-3	2W Analog Loop Design/>=10 circuits/Dispatch/TN (%)					8.85%	113						
B.2.18.8.2.2	P-3	2W Analog Loop Design/>=10 circuits/Non-Dispatch/TN (%)					8.85%	113						
B.2.18.9.1.1	P-3	2W Analog Loop Non-Design/<10 circuits/Dispatch/TN (%)		0.04197	0.2938	YES	5.93%	20,002	7.14%	28		0.04468	-0.2705	YES
B.2.18.9.1.4	P-3	2W Analog Loop Non-Design/<10 circuits/Dispatch In/TN (%)					0.05%	140,516						
B.2.18.9.2.1	P-3	2W Analog Loop Non-Design/>=10 circuits/Dispatch/TN (%)					9.80%	102						
B.2.18.9.2.4	P-3	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/TN (%)					0.00%	6						
B.2.18.10.1.1	P-3	2W Analog Loop w/INP Design/<10 circuits/Dispatch/TN (%)					5.88%	20,295						

BellSouth Monthly Performance Summary

Tennessee, August 2002 - M ay 2002

			Benchmark/ Analog	August BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.18.10.1.2	P-3	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/TN (%)	R&B - Disp	6.00%	19,759						
B.2.18.10.2.1	P-3	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/TN (%)	R&B - Disp	7.62%	105						
B.2.18.10.2.2	P-3	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/TN (%)	R&B - Disp	7.62%	105						
B.2.18.11.1.1	P-3	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/TN (%)	R&B (POTS) excl SB Or	6.13%	19,243						
B.2.18.11.1.4	P-3	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/TN (%)	R&B (POTS) excl SB Or	0.08%	150,840	0.00%	1		0.02878	0.0288	YES
B.2.18.11.2.1	P-3	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/TN (%)	R&B (POTS) excl SB Or	7.87%	89						
B.2.18.11.2.4	P-3	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/TN (%)	R&B (POTS) excl SB Or	0.00%	2						
B.2.18.12.1.1	P-12	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/TN (%)	R&B - Disp	6.00%	19,759	1.14%	88		0.02537	1.9162	YES
B.2.18.12.1.2	P-12	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/TN (%)	R&B - Disp	6.00%	19,759						
B.2.18.12.2.1	P-12	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/TN (%)	R&B - Disp	7.62%	105						
B.2.18.12.2.2	P-12	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/TN (%)	R&B - Disp	7.62%	105						
B.2.18.13.1.1	P-12	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/TN (%)	R&B (POTS) excl SB Or	6.13%	19,243	0.00%	1		0.23992	0.2556	YES
B.2.18.13.1.4	P-12	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/TN (%)	R&B (POTS) excl SB Or	0.08%	150,840						
B.2.18.13.2.1	P-12	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/TN (%)	R&B (POTS) excl SB Or	7.87%	89						
B.2.18.13.2.4	P-12	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/TN (%)	R&B (POTS) excl SB Or	0.00%	2						
B.2.18.14.1.1	P-3	Other Design/<10 circuits/Dispatch/TN (%)	Design	4.60%	1,437						
B.2.18.14.1.2	P-3	Other Design/<10 circuits/Non-Dispatch/TN (%)	Design	2.67%	225						
B.2.18.14.2.1	P-3	Other Design/>=10 circuits/Dispatch/TN (%)	Design	2.44%	123						
B.2.18.14.2.2	P-3	Other Design/>=10 circuits/Non-Dispatch/TN (%)	Design	0.00%	12						
B.2.18.15.1.1	P-3	Other Non-Design/<10 circuits/Dispatch/TN (%)	R&B	6.00%	19,759						
B.2.18.15.1.2	P-3	Other Non-Design/<10 circuits/Non-Dispatch/TN (%)	R&B	0.04%	292,699						
B.2.18.15.2.1	P-3	Other Non-Design/>=10 circuits/Dispatch/TN (%)	R&B	7.62%	105						
B.2.18.15.2.2	P-3	Other Non-Design/>=10 circuits/Non-Dispatch/TN (%)	R&B	0.00%	66						
B.2.18.16.1.1	P-3	INP (Standalone)/<10 circuits/Dispatch/TN (%)	R&B (POTS)	6.13%	19,243	0.00%	3		0.13853	0.4427	YES
B.2.18.16.1.2	P-3	INP (Standalone)/<10 circuits/Non-Dispatch/TN (%)	R&B (POTS)	0.04%	291,471	0.00%	3		0.01195	0.0359	YES
B.2.18.16.2.1	P-3	INP (Standalone)/>=10 circuits/Dispatch/TN (%)	R&B (POTS)	7.87%	89						
B.2.18.16.2.2	P-3	INP (Standalone)/>=10 circuits/Non-Dispatch/TN (%)	R&B (POTS)	0.00%	4						
B.2.18.17.1.1	P-12	LNP (Standalone)/<10 circuits/Dispatch/TN (%)	R&B (POTS)	6.13%	19,243	0.00%	3		0.13853	0.4427	YES
B.2.18.17.1.2	P-12	LNP (Standalone)/<10 circuits/Non-Dispatch/TN (%)	R&B (POTS)	0.04%	291,471	0.00%	495		0.00093	0.4605	YES
B.2.18.17.2.1	P-12	LNP (Standalone)/>=10 circuits/Dispatch/TN (%)	R&B (POTS)	7.87%	89						
B.2.18.17.2.2	P-12	LNP (Standalone)/>=10 circuits/Non-Dispatch/TN (%)	R&B (POTS)	0.00%	4	0.00%	8		0.00000		YES
B.2.18.18.1.1	P-3	Digital Loop < DS1/<10 circuits/Dispatch/TN (%)	Digital Loop < DS1	4.77%	2,283	1.92%	104		0.02138	1.3337	YES
B.2.18.18.1.2	P-3	Digital Loop < DS1/<10 circuits/Non-Dispatch/TN (%)	Digital Loop < DS1	0.03%	3,105						
B.2.18.18.2.1	P-3	Digital Loop < DS1/>=10 circuits/Dispatch/TN (%)	Digital Loop < DS1	25.00%	4						
B.2.18.18.2.2	P-3	Digital Loop < DS1/>=10 circuits/Non-Dispatch/TN (%)	Digital Loop < DS1	0.00%	1						
B.2.18.19.1.1	P-3	Digital Loop >= DS1/<10 circuits/Dispatch/TN (%)	Digital Loop >= DS1	3.14%	255	7.25%	193		0.01663	-2.4751	NO
B.2.18.19.1.2	P-3	Digital Loop >= DS1/<10 circuits/Non-Dispatch/TN (%)	Digital Loop >= DS1	1.46%	137						
B.2.18.19.2.1	P-3	Digital Loop >= DS1/>=10 circuits/Dispatch/TN (%)	Digital Loop >= DS1	4.44%	45						
B.2.18.19.2.2	P-3	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/TN (%)	Digital Loop >= DS1	0.00%	30						
		% Provisioning Troubles within 30 Days									
B.2.19.1.1.1	P-9	Switch Ports/<10 circuits/Dispatch/TN (%)	R&B (POTS)	11.37%	17,528						
B.2.19.1.1.2	P-9	Switch Ports/<10 circuits/Non-Dispatch/TN (%)	R&B (POTS)	3.68%	274,116						
B.2.19.1.2.1	P-9	Switch Ports/>=10 circuits/Dispatch/TN (%)	R&B (POTS)	12.33%	73						
B.2.19.1.2.2	P-9	Switch Ports/>=10 circuits/Non-Dispatch/TN (%)	R&B (POTS)	0.00%	2						
B.2.19.2.1.1	P-9	Local Interoffice Transport/<10 circuits/Dispatch/TN (%)	DS1/DS3	8.34%	1,019						
B.2.19.2.1.2	P-9	Local Interoffice Transport/<10 circuits/Non-Dispatch/TN (%)	DS1/DS3								
B.2.19.2.2.1	P-9	Local Interoffice Transport/>=10 circuits/Dispatch/TN (%)	DS1/DS3	0.00%	1						
B.2.19.2.2.2	P-9	Local Interoffice Transport/>=10 circuits/Non-Dispatch/TN (%)	DS1/DS3								
B.2.19.3.1.1	P-9	Loop + Port Combinations/<10 circuits/Dispatch/TN (%)	R&B	11.35%	17,863	14.33%	342		0.01731	-1.7211	NO
B.2.19.3.1.2	P-9	Loop + Port Combinations/<10 circuits/Non-Dispatch/TN (%)	R&B	3.68%	276,311	3.39%	9,853		0.00193	1.5011	YES
B.2.19.3.1.3	P-9	Loop + Port Combinations/<10 circuits/Switch Based Orders/TN (%)	R&B	3.37%	133,339	3.15%	2,797		0.00345	0.6395	YES
B.2.19.3.1.4	P-9	Loop + Port Combinations/<10 circuits/Dispatch In/TN (%)	R&B	3.97%	142,972	3.49%	7,056		0.00238	2.0366	YES
B.2.19.3.2.1	P-9	Loop + Port Combinations/>=10 circuits/Dispatch/TN (%)	R&B	14.81%	81	0.00%	3		0.20887	0.7093	YES
B.2.19.3.2.2	P-9	Loop + Port Combinations/>=10 circuits/Non-Dispatch/TN (%)	R&B	10.64%	47						
B.2.19.3.2.3	P-9	Loop + Port Combinations/>=10 circuits/Switch Based Orders/TN (%)	R&B	20.00%	10						
B.2.19.3.2.4	P-9	Loop + Port Combinations/>=10 circuits/Dispatch In/TN (%)	R&B	8.11%	37						
B.2.19.4.1.1	P-9	Combo Other/<10 circuits/Dispatch/TN (%)	R&B&D - Disp	10.83%	19,481	15.79%	38		0.05046	-0.9826	YES
B.2.19.4.1.4	P-9	Combo Other/<10 circuits/Dispatch In/TN (%)	R&B&D - Disp	10.83%	19,481						
B.2.19.4.2.1	P-9	Combo Other/>=10 circuits/Dispatch/TN (%)	R&B&D - Disp	14.29%	84						
B.2.19.4.2.4	P-9	Combo Other/>=10 circuits/Dispatch In/TN (%)	R&B&D - Disp	14.29%	84						
B.2.19.5.1.1	P-9	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/TN (%)	ADSL to Retail	4.65%	1,655	3.66%	82		0.02383	0.4172	YES
B.2.19.5.1.2	P-9	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/TN (%)	ADSL to Retail	2.24%	2,187						
B.2.19.5.2.1	P-9	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/TN (%)	ADSL to Retail	0.00%	3						
B.2.19.5.2.2	P-9	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/TN (%)	ADSL to Retail	50.00%	2						
B.2.19.6.1.1	P-9	UNE ISDN/<10 circuits/Dispatch/TN (%)	ISDN - BRI	7.45%	282	8.06%	62		0.03682	-0.1677	YES
B.2.19.6.1.2	P-9	UNE ISDN/<10 circuits/Non-Dispatch/TN (%)	ISDN - BRI	0.83%	600						
B.2.19.6.2.1	P-9	UNE ISDN/>=10 circuits/Dispatch/TN (%)	ISDN - BRI	0.00%	1						
B.2.19.6.2.2	P-9	UNE ISDN/>=10 circuits/Non-Dispatch/TN (%)	ISDN - BRI								

BellSouth Monthly Performance Summary

Tennessee, August 2002 - M ay 2002

			July								Jul			
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.2.18.10.1.2	P-3	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/TN (%)	8.45%	17,863							6.67%	16,359		
B.2.18.10.2.1	P-3	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/TN (%)	9.88%	81							4.39%	114		
B.2.18.10.2.2	P-3	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/TN (%)	9.88%	81							4.39%	114		
B.2.18.11.1.1	P-3	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/TN (%)	8.52%	17,528							6.79%	16,027		
B.2.18.11.1.4	P-3	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/TN (%)	0.08%	141,246							0.04%	128,242		
B.2.18.11.2.1	P-3	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/TN (%)	10.96%	73							2.86%	70		
B.2.18.11.2.4	P-3	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/TN (%)												
B.2.18.12.1.1	P-12	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/TN (%)	8.45%	17,863	0.00%	87		0.02969	2.8264	YES	6.67%	16,359	0.00%	104
B.2.18.12.1.2	P-12	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/TN (%)	8.45%	17,863							6.67%	16,359		
B.2.18.12.2.1	P-12	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/TN (%)	9.88%	81							4.39%	114		
B.2.18.12.2.2	P-12	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/TN (%)	9.88%	81							4.39%	114		
B.2.18.13.1.1	P-12	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/TN (%)	8.52%	17,528							6.79%	16,027		
B.2.18.13.1.4	P-12	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/TN (%)	0.08%	141,246	0.00%	1		0.02738	0.0274	YES	0.04%	128,242		
B.2.18.13.2.1	P-12	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/TN (%)	10.96%	73							2.86%	70		
B.2.18.13.2.4	P-12	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/TN (%)												
B.2.18.14.1.1	P-3	Other Design/<10 circuits/Dispatch/TN (%)	4.64%	1,618							4.88%	1,476		
B.2.18.14.1.2	P-3	Other Design/<10 circuits/Non-Dispatch/TN (%)	2.29%	218							1.04%	383		
B.2.18.14.2.1	P-3	Other Design/>=10 circuits/Dispatch/TN (%)	0.00%	3							33.33%	3		
B.2.18.14.2.2	P-3	Other Design/>=10 circuits/Non-Dispatch/TN (%)												
B.2.18.15.1.1	P-3	Other Non-Design/<10 circuits/Dispatch/TN (%)	8.45%	17,863							6.67%	16,359		
B.2.18.15.1.2	P-3	Other Non-Design/<10 circuits/Non-Dispatch/TN (%)	0.04%	276,311							0.02%	252,009		
B.2.18.15.2.1	P-3	Other Non-Design/>=10 circuits/Dispatch/TN (%)	9.88%	81							4.39%	114		
B.2.18.15.2.2	P-3	Other Non-Design/>=10 circuits/Non-Dispatch/TN (%)	0.00%	47							0.00%	35		
B.2.18.16.1.1	P-3	INP (Standalone)/<10 circuits/Dispatch/TN (%)	8.52%	17,528							6.79%	16,027		
B.2.18.16.1.2	P-3	INP (Standalone)/<10 circuits/Non-Dispatch/TN (%)	0.04%	274,116							0.02%	250,870		
B.2.18.16.2.1	P-3	INP (Standalone)/>=10 circuits/Dispatch/TN (%)	10.96%	73							2.86%	70		
B.2.18.16.2.2	P-3	INP (Standalone)/>=10 circuits/Non-Dispatch/TN (%)	0.00%	2										
B.2.18.17.1.1	P-12	LNP (Standalone)/<10 circuits/Dispatch/TN (%)	8.52%	17,528							6.79%	16,027		
B.2.18.17.1.2	P-12	LNP (Standalone)/<10 circuits/Non-Dispatch/TN (%)	0.04%	274,116	0.43%	468		0.00091	-4.2731	NO	0.02%	250,870	0.00%	456
B.2.18.17.2.1	P-12	LNP (Standalone)/>=10 circuits/Dispatch/TN (%)	10.96%	73							2.86%	70		
B.2.18.17.2.2	P-12	LNP (Standalone)/>=10 circuits/Non-Dispatch/TN (%)	0.00%	2	0.00%	4		0.00000		YES			0.00%	6
B.2.18.18.1.1	P-3	Digital Loop < DS1/<10 circuits/Dispatch/TN (%)	6.74%	2,062	2.21%	136		0.02220	2.0431	YES	6.01%	2,095	1.30%	154
B.2.18.18.1.2	P-3	Digital Loop < DS1/<10 circuits/Non-Dispatch/TN (%)	0.17%	2,881							0.28%	2,847		
B.2.18.18.2.1	P-3	Digital Loop < DS1/>=10 circuits/Dispatch/TN (%)	0.00%	4							0.00%	2		
B.2.18.18.2.2	P-3	Digital Loop < DS1/>=10 circuits/Non-Dispatch/TN (%)	0.00%	2							0.00%	2		
B.2.18.19.1.1	P-3	Digital Loop >= DS1/<10 circuits/Dispatch/TN (%)	4.77%	398	7.03%	185		0.01897	-1.1876	YES	4.22%	332	9.17%	229
B.2.18.19.1.2	P-3	Digital Loop >= DS1/<10 circuits/Non-Dispatch/TN (%)	1.34%	224							0.00%	261		
B.2.18.19.2.1	P-3	Digital Loop >= DS1/>=10 circuits/Dispatch/TN (%)	0.00%	1										
B.2.18.19.2.2	P-3	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/TN (%)	0.00%	15							0.00%	7		
		% Provisioning Troubles within 30 Days												
B.2.19.1.1.1	P-9	Switch Ports/<10 circuits/Dispatch/TN (%)	11.95%	16,027							11.38%	20,002		
B.2.19.1.1.2	P-9	Switch Ports/<10 circuits/Non-Dispatch/TN (%)	3.51%	250,870							3.18%	272,690		
B.2.19.1.2.1	P-9	Switch Ports/>=10 circuits/Dispatch/TN (%)	17.14%	70							20.59%	102		
B.2.19.1.2.2	P-9	Switch Ports/>=10 circuits/Non-Dispatch/TN (%)									0.00%	10		
B.2.19.2.1.1	P-9	Local Interoffice Transport/<10 circuits/Dispatch/TN (%)	8.32%	793							8.00%	888	0.00%	11
B.2.19.2.1.2	P-9	Local Interoffice Transport/<10 circuits/Non-Dispatch/TN (%)												
B.2.19.2.2.1	P-9	Local Interoffice Transport/>=10 circuits/Dispatch/TN (%)									0.00%	1		
B.2.19.2.2.2	P-9	Local Interoffice Transport/>=10 circuits/Non-Dispatch/TN (%)												
B.2.19.3.1.1	P-9	Loop + Port Combinations/<10 circuits/Dispatch/TN (%)	11.94%	16,359	12.16%	329		0.01806	-0.1183	YES	11.42%	20,295	12.03%	349
B.2.19.3.1.2	P-9	Loop + Port Combinations/<10 circuits/Non-Dispatch/TN (%)	3.51%	252,009	3.88%	5,882		0.00243	-1.5182	YES	3.18%	273,948	3.69%	7,476
B.2.19.3.1.3	P-9	Loop + Port Combinations/<10 circuits/Switch Based Orders/TN (%)	3.22%	123,120	3.58%	2,094		0.00389	-0.9174	YES	3.01%	132,674	3.86%	2,490
B.2.19.3.1.4	P-9	Loop + Port Combinations/<10 circuits/Dispatch In/TN (%)	3.78%	128,889	4.04%	3,788		0.00314	-0.8292	YES	3.33%	141,274	3.61%	4,986
B.2.19.3.2.1	P-9	Loop + Port Combinations/>=10 circuits/Dispatch/TN (%)	19.30%	114	0.00%	8		0.14434	1.3370	YES	22.12%	113	14.29%	7
B.2.19.3.2.2	P-9	Loop + Port Combinations/>=10 circuits/Non-Dispatch/TN (%)	5.71%	35							7.69%	65	0.00%	3
B.2.19.3.2.3	P-9	Loop + Port Combinations/>=10 circuits/Switch Based Orders/TN (%)	0.00%	13							8.33%	12		
B.2.19.3.2.4	P-9	Loop + Port Combinations/>=10 circuits/Dispatch In/TN (%)	9.09%	22							7.55%	53	0.00%	3
B.2.19.4.1.1	P-9	Combo Other/<10 circuits/Dispatch/TN (%)	11.46%	17,835	22.22%	36		0.05313	-2.0265	NO	11.08%	21,694	14.63%	41
B.2.19.4.1.4	P-9	Combo Other/<10 circuits/Dispatch In/TN (%)	11.46%	17,835							11.08%	21,694		
B.2.19.4.2.1	P-9	Combo Other/>=10 circuits/Dispatch/TN (%)	18.80%	117							21.93%	114		
B.2.19.4.2.4	P-9	Combo Other/>=10 circuits/Dispatch In/TN (%)	18.80%	117							21.93%	114		
B.2.19.5.1.1	P-9	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/TN (%)	4.14%	1,620	2.65%	113		0.01937	0.7644	YES	4.05%	1,555	4.49%	89
B.2.19.5.1.2	P-9	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/TN (%)	3.18%	2,107							3.27%	1,955		
B.2.19.5.2.1	P-9	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/TN (%)	0.00%	2							0.00%	4		
B.2.19.5.2.2	P-9	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/TN (%)	50.00%	2							0.00%	1		
B.2.19.6.1.1	P-9	UNE ISDN/<10 circuits/Dispatch/TN (%)	7.46%	268	16.36%	55		0.03890	-2.2881	NO	7.53%	332	11.29%	62
B.2.19.6.1.2	P-9	UNE ISDN/<10 circuits/Non-Dispatch/TN (%)	0.74%	540							0.59%	506		
B.2.19.6.2.1	P-9	UNE ISDN/>=10 circuits/Dispatch/TN (%)												
B.2.19.6.2.2	P-9	UNE ISDN/>=10 circuits/Non-Dispatch/TN (%)												

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Tennessee, August 2002 - May 2002

			ne				May							
			Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.18.10.1.2	P-3	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/TN (%)					5.88%	20,295						
B.2.18.10.2.1	P-3	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/TN (%)					8.85%	113						
B.2.18.10.2.2	P-3	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/TN (%)					8.85%	113						
B.2.18.11.1.1	P-3	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/TN (%)					5.93%	20,002						
B.2.18.11.1.4	P-3	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/TN (%)					0.05%	140,516						
B.2.18.11.2.1	P-3	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/TN (%)					9.80%	102						
B.2.18.11.2.4	P-3	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/TN (%)					0.00%	6						
B.2.18.12.1.1	P-12	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/TN (%)		0.02454	2.7175	YES	5.88%	20,295	0.00%	97		0.02395	2.4565	YES
B.2.18.12.1.2	P-12	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/TN (%)					5.88%	20,295						
B.2.18.12.2.1	P-12	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/TN (%)					8.85%	113						
B.2.18.12.2.2	P-12	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/TN (%)					8.85%	113						
B.2.18.13.1.1	P-12	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/TN (%)					5.93%	20,002						
B.2.18.13.1.4	P-12	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/TN (%)					0.05%	140,516						
B.2.18.13.2.1	P-12	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/TN (%)					9.80%	102						
B.2.18.13.2.4	P-12	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/TN (%)					0.00%	6						
B.2.18.14.1.1	P-3	Other Design/<10 circuits/Dispatch/TN (%)					4.22%	1,399						
B.2.18.14.1.2	P-3	Other Design/<10 circuits/Non-Dispatch/TN (%)					1.17%	256						
B.2.18.14.2.1	P-3	Other Design/>=10 circuits/Dispatch/TN (%)					0.00%	1						
B.2.18.14.2.2	P-3	Other Design/>=10 circuits/Non-Dispatch/TN (%)					0.00%	1						
B.2.18.15.1.1	P-3	Other Non-Design/<10 circuits/Dispatch/TN (%)					5.88%	20,295	0.00%	1		0.23532	0.2500	YES
B.2.18.15.1.2	P-3	Other Non-Design/<10 circuits/Non-Dispatch/TN (%)					0.03%	273,948						
B.2.18.15.2.1	P-3	Other Non-Design/>=10 circuits/Dispatch/TN (%)					8.85%	113						
B.2.18.15.2.2	P-3	Other Non-Design/>=10 circuits/Non-Dispatch/TN (%)					0.00%	65						
B.2.18.16.1.1	P-3	INP (Standalone)/<10 circuits/Dispatch/TN (%)					5.93%	20,002						
B.2.18.16.1.2	P-3	INP (Standalone)/<10 circuits/Non-Dispatch/TN (%)					0.03%	272,690						
B.2.18.16.2.1	P-3	INP (Standalone)/>=10 circuits/Dispatch/TN (%)					9.80%	102						
B.2.18.16.2.2	P-3	INP (Standalone)/>=10 circuits/Non-Dispatch/TN (%)					0.00%	10						
B.2.18.17.1.1	P-12	LNP (Standalone)/<10 circuits/Dispatch/TN (%)					5.93%	20,002						
B.2.18.17.1.2	P-12	LNP (Standalone)/<10 circuits/Non-Dispatch/TN (%)		0.00069	0.3159	YES	0.03%	272,690	0.57%	348		0.00086	-6.3894	NO
B.2.18.17.2.1	P-12	LNP (Standalone)/>=10 circuits/Dispatch/TN (%)					9.80%	102						
B.2.18.17.2.2	P-12	LNP (Standalone)/>=10 circuits/Non-Dispatch/TN (%)					0.00%	10	0.00%	9		0.00000		YES
B.2.18.18.1.1	P-3	Digital Loop < DS1/<10 circuits/Dispatch/TN (%)		0.01985	2.3756	YES	4.78%	2,052	2.74%	146		0.01827	1.1147	YES
B.2.18.18.1.2	P-3	Digital Loop < DS1/<10 circuits/Non-Dispatch/TN (%)					0.19%	2,635						
B.2.18.18.2.1	P-3	Digital Loop < DS1/>=10 circuits/Dispatch/TN (%)					0.00%	4						
B.2.18.18.2.2	P-3	Digital Loop < DS1/>=10 circuits/Non-Dispatch/TN (%)					0.00%	2						
B.2.18.19.1.1	P-3	Digital Loop >= DS1/<10 circuits/Dispatch/TN (%)		0.01726	-2.8693	NO	2.93%	205	6.77%	192		0.01693	-2.2707	NO
B.2.18.19.1.2	P-3	Digital Loop >= DS1/<10 circuits/Non-Dispatch/TN (%)					0.00%	132						
B.2.18.19.2.1	P-3	Digital Loop >= DS1/>=10 circuits/Dispatch/TN (%)												
B.2.18.19.2.2	P-3	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/TN (%)					0.00%	16						
		% Provisioning Troubles within 30 Days												
B.2.19.1.1.1	P-9	Switch Ports/<10 circuits/Dispatch/TN (%)					10.86%	22,726						
B.2.19.1.1.2	P-9	Switch Ports/<10 circuits/Non-Dispatch/TN (%)					3.28%	266,126						
B.2.19.1.2.1	P-9	Switch Ports/>=10 circuits/Dispatch/TN (%)					14.43%	97						
B.2.19.1.2.2	P-9	Switch Ports/>=10 circuits/Non-Dispatch/TN (%)												
B.2.19.2.1.1	P-9	Local Interoffice Transport/<10 circuits/Dispatch/TN (%)		0.08228	0.9717	YES	8.67%	692	0.00%	12		0.08194	1.0582	YES
B.2.19.2.1.2	P-9	Local Interoffice Transport/<10 circuits/Non-Dispatch/TN (%)												
B.2.19.2.2.1	P-9	Local Interoffice Transport/>=10 circuits/Dispatch/TN (%)												
B.2.19.2.2.2	P-9	Local Interoffice Transport/>=10 circuits/Non-Dispatch/TN (%)												
B.2.19.3.1.1	P-9	Loop + Port Combinations/<10 circuits/Dispatch/TN (%)		0.01717	-0.3598	YES	10.79%	23,045	14.46%	325		0.01733	-2.1172	NO
B.2.19.3.1.2	P-9	Loop + Port Combinations/<10 circuits/Non-Dispatch/TN (%)		0.00206	-2.5123	NO	3.27%	267,561	4.27%	5,147		0.00250	-4.0014	NO
B.2.19.3.1.3	P-9	Loop + Port Combinations/<10 circuits/Switch Based Orders/TN (%)		0.00346	-2.4350	NO	3.20%	131,058	4.03%	2,683		0.00343	-2.3992	NO
B.2.19.3.1.4	P-9	Loop + Port Combinations/<10 circuits/Dispatch In/TN (%)		0.00258	-1.0932	YES	3.34%	136,503	4.55%	2,464		0.00365	-3.2987	NO
B.2.19.3.2.1	P-9	Loop + Port Combinations/>=10 circuits/Dispatch/TN (%)		0.16167	0.4848	YES	16.54%	127	28.57%	7		0.14423	-0.8345	YES
B.2.19.3.2.2	P-9	Loop + Port Combinations/>=10 circuits/Non-Dispatch/TN (%)		0.15736	0.4888	YES	15.56%	45	0.00%	5		0.17085	0.9105	YES
B.2.19.3.2.3	P-9	Loop + Port Combinations/>=10 circuits/Switch Based Orders/TN (%)					11.11%	9	0.00%	1		0.33127	0.3354	YES
B.2.19.3.2.4	P-9	Loop + Port Combinations/>=10 circuits/Dispatch In/TN (%)		0.15676	0.4814	YES	16.67%	36	0.00%	4		0.19642	0.8485	YES
B.2.19.4.1.1	P-9	Combo Other/<10 circuits/Dispatch/TN (%)		0.04907	-0.7240	YES	10.54%	24,781	8.57%	35		0.05194	0.3791	YES
B.2.19.4.1.4	P-9	Combo Other/<10 circuits/Dispatch In/TN (%)					10.54%	24,781						
B.2.19.4.2.1	P-9	Combo Other/>=10 circuits/Dispatch/TN (%)					15.67%	134						
B.2.19.4.2.4	P-9	Combo Other/>=10 circuits/Dispatch In/TN (%)					15.67%	134						
B.2.19.5.1.1	P-9	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/TN (%)		0.02149	-0.2061	YES	3.69%	1,787	4.88%	82		0.02130	-0.5562	YES
B.2.19.5.1.2	P-9	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/TN (%)					1.70%	1,883						
B.2.19.5.2.1	P-9	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/TN (%)					0.00%	7						
B.2.19.5.2.2	P-9	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/TN (%)					0.00%	1						
B.2.19.6.1.1	P-9	UNE ISDN/<10 circuits/Dispatch/TN (%)		0.03651	-1.0300	YES	8.49%	365	15.94%	69		0.03660	-2.0354	NO
B.2.19.6.1.2	P-9	UNE ISDN/<10 circuits/Non-Dispatch/TN (%)					0.91%	661						
B.2.19.6.2.1	P-9	UNE ISDN/>=10 circuits/Dispatch/TN (%)												
B.2.19.6.2.2	P-9	UNE ISDN/>=10 circuits/Non-Dispatch/TN (%)												

BellSouth Monthly Performance Summary

Tennessee, August 2002 - M ay 2002

			Benchmark/ Analog	August BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.19.7.1.1	P-9	Line Sharing/<10 circuits/Dispatch/TN (%)	ADSL to Retail	4.65%	1,655	14.29%	7		0.07978	-1.2075	YES
B.2.19.7.1.2	P-9	Line Sharing/<10 circuits/Non-Dispatch/TN (%)	ADSL to Retail	2.24%	2,187	22.92%	48		0.02159	-9.5747	NO
B.2.19.7.2.1	P-9	Line Sharing/>=10 circuits/Dispatch/TN (%)	ADSL to Retail	0.00%	3						
B.2.19.7.2.2	P-9	Line Sharing/>=10 circuits/Non-Dispatch/TN (%)	ADSL to Retail	50.00%	2						
B.2.19.8.1.1	P-9	2W Analog Loop Design/<10 circuits/Dispatch/TN (%)	R&B - Disp	11.35%	17,863	3.85%	130		0.02792	2.6868	YES
B.2.19.8.1.2	P-9	2W Analog Loop Design/<10 circuits/Non-Dispatch/TN (%)	R&B - Disp	11.35%	17,863						
B.2.19.8.2.1	P-9	2W Analog Loop Design/>=10 circuits/Dispatch/TN (%)	R&B - Disp	14.81%	81						
B.2.19.8.2.2	P-9	2W Analog Loop Design/>=10 circuits/Non-Dispatch/TN (%)	R&B - Disp	14.81%	81						
B.2.19.9.1.1	P-9	2W Analog Loop Non-Design/<10 circuits/Dispatch/TN (%)	R&B (POTS) excl SB Or	11.37%	17,528	6.12%	49		0.04541	1.1556	YES
B.2.19.9.1.4	P-9	2W Analog Loop Non-Design/<10 circuits/Dispatch In/TN (%)	R&B (POTS) excl SB Or	3.97%	141,246						
B.2.19.9.2.1	P-9	2W Analog Loop Non-Design/>=10 circuits/Dispatch/TN (%)	R&B (POTS) excl SB Or	12.33%	73						
B.2.19.9.2.4	P-9	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/TN (%)	R&B (POTS) excl SB Or								
B.2.19.10.1.1	P-9	2W Analog Loop w/INP Design/<10 circuits/Dispatch/TN (%)	R&B - Disp	11.35%	17,863						
B.2.19.10.1.2	P-9	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/TN (%)	R&B - Disp	11.35%	17,863						
B.2.19.10.2.1	P-9	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/TN (%)	R&B - Disp	14.81%	81						
B.2.19.10.2.2	P-9	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/TN (%)	R&B - Disp	14.81%	81						
B.2.19.11.1.1	P-9	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/TN (%)	R&B (POTS) excl SB Or	11.37%	17,528						
B.2.19.11.1.4	P-9	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/TN (%)	R&B (POTS) excl SB Or	3.97%	141,246						
B.2.19.11.2.1	P-9	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/TN (%)	R&B (POTS) excl SB Or	12.33%	73						
B.2.19.11.2.4	P-9	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/TN (%)	R&B (POTS) excl SB Or								
B.2.19.12.1.1	P-9	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/TN (%)	R&B - Disp	11.35%	17,863	6.90%	87		0.03409	1.3057	YES
B.2.19.12.1.2	P-9	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/TN (%)	R&B - Disp	11.35%	17,863						
B.2.19.12.2.1	P-9	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/TN (%)	R&B - Disp	14.81%	81						
B.2.19.12.2.2	P-9	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/TN (%)	R&B - Disp	14.81%	81						
B.2.19.13.1.1	P-9	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/TN (%)	R&B (POTS) excl SB Or	11.37%	17,528						
B.2.19.13.1.4	P-9	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/TN (%)	R&B (POTS) excl SB Or	3.97%	141,246	0.00%	1		0.19531	0.2034	YES
B.2.19.13.2.1	P-9	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/TN (%)	R&B (POTS) excl SB Or	12.33%	73						
B.2.19.13.2.4	P-9	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/TN (%)	R&B (POTS) excl SB Or								
B.2.19.14.1.1	P-9	Other Design/<10 circuits/Dispatch/TN (%)	Design	5.13%	1,618						
B.2.19.14.1.2	P-9	Other Design/<10 circuits/Non-Dispatch/TN (%)	Design	2.29%	218						
B.2.19.14.2.1	P-9	Other Design/>=10 circuits/Dispatch/TN (%)	Design	0.00%	3						
B.2.19.14.2.2	P-9	Other Design/>=10 circuits/Non-Dispatch/TN (%)	Design								
B.2.19.15.1.1	P-9	Other Non-Design/<10 circuits/Dispatch/TN (%)	R&B	11.35%	17,863						
B.2.19.15.1.2	P-9	Other Non-Design/<10 circuits/Non-Dispatch/TN (%)	R&B	3.68%	276,311						
B.2.19.15.2.1	P-9	Other Non-Design/>=10 circuits/Dispatch/TN (%)	R&B	14.81%	81						
B.2.19.15.2.2	P-9	Other Non-Design/>=10 circuits/Non-Dispatch/TN (%)	R&B	10.64%	47						
B.2.19.16.1.1	P-9	INP (Standalone)/<10 circuits/Dispatch/TN (%)	R&B (POTS)	11.37%	17,528						
B.2.19.16.1.2	P-9	INP (Standalone)/<10 circuits/Non-Dispatch/TN (%)	R&B (POTS)	3.68%	274,116						
B.2.19.16.2.1	P-9	INP (Standalone)/>=10 circuits/Dispatch/TN (%)	R&B (POTS)	12.33%	73						
B.2.19.16.2.2	P-9	INP (Standalone)/>=10 circuits/Non-Dispatch/TN (%)	R&B (POTS)	0.00%	2						
B.2.19.17.1.1	P-9	LNP (Standalone)/<10 circuits/Dispatch/TN (%)	R&B (POTS)	11.37%	17,528	0.00%	1		0.31746	0.3582	YES
B.2.19.17.1.2	P-9	LNP (Standalone)/<10 circuits/Non-Dispatch/TN (%)	R&B (POTS)	3.68%	274,116	0.00%	470		0.00869	4.2336	YES
B.2.19.17.2.1	P-9	LNP (Standalone)/>=10 circuits/Dispatch/TN (%)	R&B (POTS)	12.33%	73						
B.2.19.17.2.2	P-9	LNP (Standalone)/>=10 circuits/Non-Dispatch/TN (%)	R&B (POTS)	0.00%	2	0.00%	5		0.00000		YES
B.2.19.18.1.1	P-9	Digital Loop < DS1/<10 circuits/Dispatch/TN (%)	Digital Loop < DS1	5.09%	2,062	5.88%	136		0.01946	-0.4060	YES
B.2.19.18.1.2	P-9	Digital Loop < DS1/<10 circuits/Non-Dispatch/TN (%)	Digital Loop < DS1	1.91%	2,881						
B.2.19.18.2.1	P-9	Digital Loop < DS1/>=10 circuits/Dispatch/TN (%)	Digital Loop < DS1	0.00%	4						
B.2.19.18.2.2	P-9	Digital Loop < DS1/>=10 circuits/Non-Dispatch/TN (%)	Digital Loop < DS1	50.00%	2						
B.2.19.19.1.1	P-9	Digital Loop >= DS1/<10 circuits/Dispatch/TN (%)	Digital Loop >= DS1	3.52%	398	18.92%	185		0.01639	-9.3952	NO
B.2.19.19.1.2	P-9	Digital Loop >= DS1/<10 circuits/Non-Dispatch/TN (%)	Digital Loop >= DS1	0.00%	224						
B.2.19.19.2.1	P-9	Digital Loop >= DS1/>=10 circuits/Dispatch/TN (%)	Digital Loop >= DS1	0.00%	1						
B.2.19.19.2.2	P-9	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/TN (%)	Digital Loop >= DS1	6.67%	15						
		Average Completion Notice Interval - Mechanized									
B.2.21.1.1.1	P-5	Switch Ports/<10 circuits/Dispatch/TN (hours)	R&B (POTS)	2.20	19,209			15.312			
B.2.21.1.1.2	P-5	Switch Ports/<10 circuits/Non-Dispatch/TN (hours)	R&B (POTS)	1.20	290,992			9.276			
B.2.21.1.2.1	P-5	Switch Ports/>=10 circuits/Dispatch/TN (hours)	R&B (POTS)	2.27	88			12.767			
B.2.21.1.2.2	P-5	Switch Ports/>=10 circuits/Non-Dispatch/TN (hours)	R&B (POTS)	23.62	3			39.271			
B.2.21.2.1.1	P-5	Local Interoffice Transport/<10 circuits/Dispatch/TN (hours)	DS1/ DS3 - Interoffice	93.56	871			222.368			
B.2.21.2.1.2	P-5	Local Interoffice Transport/<10 circuits/Non-Dispatch/TN (hours)	DS1/ DS3 - Interoffice								
B.2.21.2.2.1	P-5	Local Interoffice Transport/>=10 circuits/Dispatch/TN (hours)	DS1/ DS3 - Interoffice								
B.2.21.2.2.2	P-5	Local Interoffice Transport/>=10 circuits/Non-Dispatch/TN (hours)	DS1/ DS3 - Interoffice								
B.2.21.3.1.1	P-5	Loop + Port Combinations/<10 circuits/Dispatch/TN (hours)	R&B	2.25	19,725	0.24	292	15.580	0.91849	2.1955	YES
B.2.21.3.1.2	P-5	Loop + Port Combinations/<10 circuits/Non-Dispatch/TN (hours)	R&B	1.20	292,219	0.83	20,676	9.338	0.06720	5.6217	YES
B.2.21.3.1.3	P-5	Loop + Port Combinations/<10 circuits/Switch Based Orders/TN (hours)	R&B	1.29	140,761	0.76	2,561	10.201	0.20339	2.6238	YES
B.2.21.3.1.4	P-5	Loop + Port Combinations/<10 circuits/Dispatch in/TN (hours)	R&B	1.12	151,458	0.84	18,115	8.457	0.06649	4.2978	YES
B.2.21.3.2.1	P-5	Loop + Port Combinations/>=10 circuits/Dispatch/TN (hours)	R&B	2.44	104	0.02	1	12.336	12.39522	0.1957	YES
B.2.21.3.2.2	P-5	Loop + Port Combinations/>=10 circuits/Non-Dispatch/TN (hours)	R&B	1.80	65			8.467			
B.2.21.3.2.3	P-5	Loop + Port Combinations/>=10 circuits/Switch Based Orders/TN (hours)	R&B	5.51	14			18.268			

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			July								Jur			
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.2.19.7.1.1	P-9	Line Sharing/<10 circuits/Dispatch/TN (%)	4.14%	1,620	40.00%	5		0.08919	-4.0213	NO	4.05%	1,555	0.00%	5
B.2.19.7.1.2	P-9	Line Sharing/<10 circuits/Non-Dispatch/TN (%)	3.18%	2,107	13.16%	38		0.02872	-3.4743	NO	3.27%	1,955	6.67%	45
B.2.19.7.2.1	P-9	Line Sharing/>=10 circuits/Dispatch/TN (%)	0.00%	2							0.00%	4		
B.2.19.7.2.2	P-9	Line Sharing/>=10 circuits/Non-Dispatch/TN (%)	50.00%	2							0.00%	1		
B.2.19.8.1.1	P-9	2W Analog Loop Design/<10 circuits/Dispatch/TN (%)	11.94%	16,359	10.99%	182		0.02417	0.3953	YES	11.42%	20,295	8.38%	167
B.2.19.8.1.2	P-9	2W Analog Loop Design/<10 circuits/Non-Dispatch/TN (%)	11.94%	16,359							11.42%	20,295		
B.2.19.8.2.1	P-9	2W Analog Loop Design/>=10 circuits/Dispatch/TN (%)	19.30%	114							22.12%	113		
B.2.19.8.2.2	P-9	2W Analog Loop Design/>=10 circuits/Non-Dispatch/TN (%)	19.30%	114							22.12%	113		
B.2.19.9.1.1	P-9	2W Analog Loop Non-Design/<10 circuits/Dispatch/TN (%)	11.95%	16,027	5.56%	36		0.05413	1.1821	YES	11.38%	20,002	0.00%	28
B.2.19.9.1.4	P-9	2W Analog Loop Non-Design/<10 circuits/Dispatch In/TN (%)	3.78%	128,242							3.33%	140,516		
B.2.19.9.2.1	P-9	2W Analog Loop Non-Design/>=10 circuits/Dispatch/TN (%)	17.14%	70							20.59%	102		
B.2.19.9.2.4	P-9	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/TN (%)									0.00%	6		
B.2.19.10.1.1	P-9	2W Analog Loop w/INP Design/<10 circuits/Dispatch/TN (%)	11.94%	16,359							11.42%	20,295		
B.2.19.10.1.2	P-9	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/TN (%)	11.94%	16,359							11.42%	20,295		
B.2.19.10.2.1	P-9	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/TN (%)	19.30%	114							22.12%	113		
B.2.19.10.2.2	P-9	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/TN (%)	19.30%	114							22.12%	113		
B.2.19.11.1.1	P-9	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/TN (%)	11.95%	16,027							11.38%	20,002		
B.2.19.11.1.4	P-9	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/TN (%)	3.78%	128,242							3.33%	140,516		
B.2.19.11.2.1	P-9	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/TN (%)	17.14%	70							20.59%	102		
B.2.19.11.2.4	P-9	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/TN (%)									0.00%	6		
B.2.19.12.1.1	P-9	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/TN (%)	11.94%	16,359	11.54%	104		0.03190	0.1273	YES	11.42%	20,295	10.31%	97
B.2.19.12.1.2	P-9	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/TN (%)	11.94%	16,359							11.42%	20,295		
B.2.19.12.2.1	P-9	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/TN (%)	19.30%	114							22.12%	113		
B.2.19.12.2.2	P-9	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/TN (%)	19.30%	114							22.12%	113		
B.2.19.13.1.1	P-9	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/TN (%)	11.95%	16,027							11.38%	20,002		
B.2.19.13.1.4	P-9	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/TN (%)	3.78%	128,242							3.33%	140,516		
B.2.19.13.2.1	P-9	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/TN (%)	17.14%	70							20.59%	102		
B.2.19.13.2.4	P-9	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/TN (%)									0.00%	6		
B.2.19.14.1.1	P-9	Other Design/<10 circuits/Dispatch/TN (%)	6.03%	1,476							6.22%	1,399		
B.2.19.14.1.2	P-9	Other Design/<10 circuits/Non-Dispatch/TN (%)	4.18%	383							4.69%	256		
B.2.19.14.2.1	P-9	Other Design/>=10 circuits/Dispatch/TN (%)	0.00%	3							0.00%	1		
B.2.19.14.2.2	P-9	Other Design/>=10 circuits/Non-Dispatch/TN (%)									0.00%	1		
B.2.19.15.1.1	P-9	Other Non-Design/<10 circuits/Dispatch/TN (%)	11.94%	16,359							11.42%	20,295	0.00%	1
B.2.19.15.1.2	P-9	Other Non-Design/<10 circuits/Non-Dispatch/TN (%)	3.51%	252,009							3.18%	273,948		
B.2.19.15.2.1	P-9	Other Non-Design/>=10 circuits/Dispatch/TN (%)	19.30%	114							22.12%	113		
B.2.19.15.2.2	P-9	Other Non-Design/>=10 circuits/Non-Dispatch/TN (%)	5.71%	35							7.69%	65		
B.2.19.16.1.1	P-9	INP (Standalone)/<10 circuits/Dispatch/TN (%)	11.95%	16,027							11.38%	20,002		
B.2.19.16.1.2	P-9	INP (Standalone)/<10 circuits/Non-Dispatch/TN (%)	3.51%	250,870							3.18%	272,690		
B.2.19.16.2.1	P-9	INP (Standalone)/>=10 circuits/Dispatch/TN (%)	17.14%	70							20.59%	102		
B.2.19.16.2.2	P-9	INP (Standalone)/>=10 circuits/Non-Dispatch/TN (%)									0.00%	10		
B.2.19.17.1.1	P-9	LNP (Standalone)/<10 circuits/Dispatch/TN (%)	11.95%	16,027							11.38%	20,002		
B.2.19.17.1.2	P-9	LNP (Standalone)/<10 circuits/Non-Dispatch/TN (%)	3.51%	250,870	0.00%	457		0.00862	4.0732	YES	3.18%	272,690	0.00%	350
B.2.19.17.2.1	P-9	LNP (Standalone)/>=10 circuits/Dispatch/TN (%)	17.14%	70							20.59%	102		
B.2.19.17.2.2	P-9	LNP (Standalone)/>=10 circuits/Non-Dispatch/TN (%)			0.00%	7					0.00%	10	0.00%	9
B.2.19.18.1.1	P-9	Digital Loop < DS1/<10 circuits/Dispatch/TN (%)	4.87%	2,095	7.14%	154		0.01797	-1.2656	YES	4.39%	2,052	7.53%	146
B.2.19.18.1.2	P-9	Digital Loop < DS1/<10 circuits/Non-Dispatch/TN (%)	2.70%	2,847							2.85%	2,635		
B.2.19.18.2.1	P-9	Digital Loop < DS1/>=10 circuits/Dispatch/TN (%)	0.00%	2							0.00%	4		
B.2.19.18.2.2	P-9	Digital Loop < DS1/>=10 circuits/Non-Dispatch/TN (%)	50.00%	2							0.00%	2		
B.2.19.19.1.1	P-9	Digital Loop >= DS1/<10 circuits/Dispatch/TN (%)	6.63%	332	14.41%	229		0.02137	-3.6429	NO	10.24%	205	14.58%	192
B.2.19.19.1.2	P-9	Digital Loop >= DS1/<10 circuits/Non-Dispatch/TN (%)	3.45%	261							3.03%	132		
B.2.19.19.2.1	P-9	Digital Loop >= DS1/>=10 circuits/Dispatch/TN (%)												
B.2.19.19.2.2	P-9	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/TN (%)	0.00%	7							0.00%	16		
		Average Completion Notice Interval - Mechanized												
B.2.21.1.1.1	P-5	Switch Ports/<10 circuits/Dispatch/TN (hours)	2.69	17,420			19.209				1.22	15,962		
B.2.21.1.1.2	P-5	Switch Ports/<10 circuits/Non-Dispatch/TN (hours)	1.18	273,262			8.520				0.96	250,415		
B.2.21.1.2.1	P-5	Switch Ports/>=10 circuits/Dispatch/TN (hours)	3.50	71			17.656				0.35	70		
B.2.21.1.2.2	P-5	Switch Ports/>=10 circuits/Non-Dispatch/TN (hours)	0.83	2			0.012							
B.2.21.2.1.1	P-5	Local Interoffice Transport/<10 circuits/Dispatch/TN (hours)	112.59	982			295.432				77.65	780		
B.2.21.2.1.2	P-5	Local Interoffice Transport/<10 circuits/Non-Dispatch/TN (hours)												
B.2.21.2.2.1	P-5	Local Interoffice Transport/>=10 circuits/Dispatch/TN (hours)												
B.2.21.2.2.2	P-5	Local Interoffice Transport/>=10 circuits/Non-Dispatch/TN (hours)												
B.2.21.3.1.1	P-5	Loop + Port Combinations/<10 circuits/Dispatch/TN (hours)	2.72	17,753	0.13	290	19.200	1.13661	2.2801	YES	1.49	16,291	0.02	272
B.2.21.3.1.2	P-5	Loop + Port Combinations/<10 circuits/Non-Dispatch/TN (hours)	1.18	275,448	1.06	9,671	8.506	0.08800	1.3976	YES	0.97	251,549	0.93	5,603
B.2.21.3.1.3	P-5	Loop + Port Combinations/<10 circuits/Switch Based Orders/TN (hours)	1.25	132,713	0.94	2,668	9.081	0.17756	1.7328	YES	0.99	122,832	0.84	1,891
B.2.21.3.1.4	P-5	Loop + Port Combinations/<10 circuits/Dispatch In/TN (hours)	1.12	142,735	1.10	7,003	7.934	0.09711	0.1636	YES	0.95	128,717	0.98	3,712
B.2.21.3.2.1	P-5	Loop + Port Combinations/>=10 circuits/Dispatch/TN (hours)	3.16	79	0.02	3	16.757	9.85673	0.3191	YES	0.26	114	0.02	6
B.2.21.3.2.2	P-5	Loop + Port Combinations/>=10 circuits/Non-Dispatch/TN (hours)	0.81	47			0.291				15.56	35		
B.2.21.3.2.3	P-5	Loop + Port Combinations/>=10 circuits/Switch Based Orders/TN (hours)	0.92	10			0.195				0.81	13		

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			19				May							
			Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.19.7.1.1	P-9	Line Sharing/<10 circuits/Dispatch/TN (%)		0.08832	0.4587	YES	3.69%	1,787	20.00%	10		0.05981	-2.7266	NO
B.2.19.7.1.2	P-9	Line Sharing/<10 circuits/Non-Dispatch/TN (%)		0.02683	-1.2646	YES	1.70%	1,883	4.35%	46		0.01929	-1.3731	YES
B.2.19.7.2.1	P-9	Line Sharing/>=10 circuits/Dispatch/TN (%)					0.00%	7						
B.2.19.7.2.2	P-9	Line Sharing/>=10 circuits/Non-Dispatch/TN (%)					0.00%	1						
B.2.19.8.1.1	P-9	2W Analog Loop Design/<10 circuits/Dispatch/TN (%)		0.02471	1.2276	YES	10.79%	23,045	6.57%	213		0.02136	1.9755	YES
B.2.19.8.1.2	P-9	2W Analog Loop Design/<10 circuits/Non-Dispatch/TN (%)					10.79%	23,045						
B.2.19.8.2.1	P-9	2W Analog Loop Design/>=10 circuits/Dispatch/TN (%)					16.54%	127						
B.2.19.8.2.2	P-9	2W Analog Loop Design/>=10 circuits/Non-Dispatch/TN (%)					16.54%	127						
B.2.19.9.1.1	P-9	2W Analog Loop Non-Design/<10 circuits/Dispatch/TN (%)		0.06007	1.8952	YES	10.86%	22,726	0.00%	26		0.06104	1.7783	YES
B.2.19.9.1.4	P-9	2W Analog Loop Non-Design/<10 circuits/Dispatch In/TN (%)					3.34%	135,735						
B.2.19.9.2.1	P-9	2W Analog Loop Non-Design/>=10 circuits/Dispatch/TN (%)					14.43%	97						
B.2.19.9.2.4	P-9	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/TN (%)												
B.2.19.10.1.1	P-9	2W Analog Loop w/INP Design/<10 circuits/Dispatch/TN (%)					10.79%	23,045						
B.2.19.10.1.2	P-9	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/TN (%)					10.79%	23,045						
B.2.19.10.2.1	P-9	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/TN (%)					16.54%	127						
B.2.19.10.2.2	P-9	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/TN (%)					16.54%	127						
B.2.19.11.1.1	P-9	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/TN (%)					10.86%	22,726						
B.2.19.11.1.4	P-9	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/TN (%)					3.34%	135,735						
B.2.19.11.2.1	P-9	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/TN (%)					14.43%	97						
B.2.19.11.2.4	P-9	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/TN (%)												
B.2.19.12.1.1	P-9	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/TN (%)		0.03237	0.3421	YES	10.79%	23,045	8.99%	89		0.03295	0.5472	YES
B.2.19.12.1.2	P-9	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/TN (%)					10.79%	23,045						
B.2.19.12.2.1	P-9	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/TN (%)					16.54%	127						
B.2.19.12.2.2	P-9	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/TN (%)					16.54%	127						
B.2.19.13.1.1	P-9	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/TN (%)					10.86%	22,726						
B.2.19.13.1.4	P-9	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/TN (%)					3.34%	135,735						
B.2.19.13.2.1	P-9	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/TN (%)					14.43%	97						
B.2.19.13.2.4	P-9	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/TN (%)												
B.2.19.14.1.1	P-9	Other Design/<10 circuits/Dispatch/TN (%)					7.20%	1,736						
B.2.19.14.1.2	P-9	Other Design/<10 circuits/Non-Dispatch/TN (%)					7.10%	155						
B.2.19.14.2.1	P-9	Other Design/>=10 circuits/Dispatch/TN (%)					0.00%	7						
B.2.19.14.2.2	P-9	Other Design/>=10 circuits/Non-Dispatch/TN (%)					0.00%	1						
B.2.19.15.1.1	P-9	Other Non-Design/<10 circuits/Dispatch/TN (%)		0.31802	0.3590	YES	10.79%	23,045	100.00%	1		0.31029	-2.8750	NO
B.2.19.15.1.2	P-9	Other Non-Design/<10 circuits/Non-Dispatch/TN (%)					3.27%	267,561						
B.2.19.15.2.1	P-9	Other Non-Design/>=10 circuits/Dispatch/TN (%)					16.54%	127						
B.2.19.15.2.2	P-9	Other Non-Design/>=10 circuits/Non-Dispatch/TN (%)					15.56%	45						
B.2.19.16.1.1	P-9	INP (Standalone)/<10 circuits/Dispatch/TN (%)					10.86%	22,726						
B.2.19.16.1.2	P-9	INP (Standalone)/<10 circuits/Non-Dispatch/TN (%)					3.28%	266,126	0.00%	1		0.17806	0.1841	YES
B.2.19.16.2.1	P-9	INP (Standalone)/>=10 circuits/Dispatch/TN (%)					14.43%	97						
B.2.19.16.2.2	P-9	INP (Standalone)/>=10 circuits/Non-Dispatch/TN (%)												
B.2.19.17.1.1	P-9	LNP (Standalone)/<10 circuits/Dispatch/TN (%)					10.86%	22,726	0.00%	2		0.21998	0.4935	YES
B.2.19.17.1.2	P-9	LNP (Standalone)/<10 circuits/Non-Dispatch/TN (%)		0.00939	3.3888	YES	3.28%	266,126	0.00%	378		0.00917	3.5768	YES
B.2.19.17.2.1	P-9	LNP (Standalone)/>=10 circuits/Dispatch/TN (%)					14.43%	97						
B.2.19.17.2.2	P-9	LNP (Standalone)/>=10 circuits/Non-Dispatch/TN (%)		0.00000		YES			0.00%	7				
B.2.19.18.1.1	P-9	Digital Loop < DS1/<10 circuits/Dispatch/TN (%)		0.01754	-1.7949	NO	4.34%	2,302	9.72%	144		0.01751	-3.0714	NO
B.2.19.18.1.2	P-9	Digital Loop < DS1/<10 circuits/Non-Dispatch/TN (%)					1.55%	2,637						
B.2.19.18.2.1	P-9	Digital Loop < DS1/>=10 circuits/Dispatch/TN (%)					0.00%	7						
B.2.19.18.2.2	P-9	Digital Loop < DS1/>=10 circuits/Non-Dispatch/TN (%)					0.00%	1						
B.2.19.19.1.1	P-9	Digital Loop >= DS1/<10 circuits/Dispatch/TN (%)		0.03045	-1.4250	YES	5.51%	272	19.23%	182		0.02186	-6.2745	NO
B.2.19.19.1.2	P-9	Digital Loop >= DS1/<10 circuits/Non-Dispatch/TN (%)					2.44%	164						
B.2.19.19.2.1	P-9	Digital Loop >= DS1/>=10 circuits/Dispatch/TN (%)					0.00%	1						
B.2.19.19.2.2	P-9	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/TN (%)					0.00%	16						
		Average Completion Notice Interval - Mechanized												
B.2.21.1.1.1	P-5	Switch Ports/<10 circuits/Dispatch/TN (hours)	16.021				1.03	19,946			10.223			
B.2.21.1.1.2	P-5	Switch Ports/<10 circuits/Non-Dispatch/TN (hours)	15.687				0.92	271,843			5.251			
B.2.21.1.2.1	P-5	Switch Ports/>=10 circuits/Dispatch/TN (hours)	2.162				1.82	101			9.957			
B.2.21.1.2.2	P-5	Switch Ports/>=10 circuits/Non-Dispatch/TN (hours)					0.55	10			0.234			
B.2.21.2.1.1	P-5	Local Interoffice Transport/<10 circuits/Dispatch/TN (hours)	158.969				33.85	873			74.226			
B.2.21.2.1.2	P-5	Local Interoffice Transport/<10 circuits/Non-Dispatch/TN (hours)												
B.2.21.2.2.1	P-5	Local Interoffice Transport/>=10 circuits/Dispatch/TN (hours)					0.02	1			0.000			
B.2.21.2.2.2	P-5	Local Interoffice Transport/>=10 circuits/Non-Dispatch/TN (hours)												
B.2.21.3.1.1	P-5	Loop + Port Combinations/<10 circuits/Dispatch/TN (hours)	17.986	1.09965	1.3371	YES	1.09	20,239	0.09	305	11.055	0.63776	1.5649	YES
B.2.21.3.1.2	P-5	Loop + Port Combinations/<10 circuits/Non-Dispatch/TN (hours)	15.669	0.21164	0.1665	YES	0.92	273,098	0.96	7,183	5.252	0.06278	-0.5561	YES
B.2.21.3.1.3	P-5	Loop + Port Combinations/<10 circuits/Switch Based Orders/TN (hours)	5.105	0.11830	1.2826	YES	1.01	131,986	0.83	2,262	6.278	0.13313	1.3275	YES
B.2.21.3.1.4	P-5	Loop + Port Combinations/<10 circuits/Dispatch In/TN (hours)	21.329	0.35509	-0.0955	YES	0.84	141,112	1.01	4,921	4.062	0.05891	-2.9584	NO
B.2.21.3.2.1	P-5	Loop + Port Combinations/>=10 circuits/Dispatch/TN (hours)	1.701	0.71262	0.3367	YES	8.73	112	0.02	7	75.352	29.35702	0.2967	YES
B.2.21.3.2.2	P-5	Loop + Port Combinations/>=10 circuits/Non-Dispatch/TN (hours)	87.547				0.76	65	0.90	1	0.286	0.28775	-0.4963	YES
B.2.21.3.2.3	P-5	Loop + Port Combinations/>=10 circuits/Switch Based Orders/TN (hours)	0.210				0.71	12			0.236			

BellSouth Monthly Performance Summary

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			Benchmark/ Analog	August BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.21.3.2.4	P-5	Loop + Port Combinations/>=10 circuits/Dispatch In/TN (hours)	R&B	0.78	51			0.273			
B.2.21.4.1.1	P-5	Combo Other/<10 circuits/Dispatch/TN (hours)	R&B&D - Disp	15.65	21,159			114.732			
B.2.21.4.1.4	P-5	Combo Other/<10 circuits/Dispatch In/TN (hours)	R&B&D - Disp	15.65	21,159			114.732			
B.2.21.4.2.1	P-5	Combo Other/>=10 circuits/Dispatch/TN (hours)	R&B&D - Disp	97.33	227			252.733			
B.2.21.4.2.4	P-5	Combo Other/>=10 circuits/Dispatch In/TN (hours)	R&B&D - Disp	97.33	227			252.733			
B.2.21.5.1.1	P-5	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/TN (hours)	ADSL to Retail	5.20	1,659	11.15	63	63.709	8.17756	-0.7268	YES
B.2.21.5.1.2	P-5	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/TN (hours)	ADSL to Retail	0.96	2,460			4.452			
B.2.21.5.2.1	P-5	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/TN (hours)	ADSL to Retail	11.22	4			22.253			
B.2.21.5.2.2	P-5	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/TN (hours)	ADSL to Retail	1.22	1			0.000			
B.2.21.6.1.1	P-5	UNE ISDN/<10 circuits/Dispatch/TN (hours)	ISDN - BRI	18.85	487	26.09	30	82.222	15.46712	-0.4683	YES
B.2.21.6.1.2	P-5	UNE ISDN/<10 circuits/Non-Dispatch/TN (hours)	ISDN - BRI	2.52	528			20.846			
B.2.21.6.2.1	P-5	UNE ISDN/>=10 circuits/Dispatch/TN (hours)	ISDN - BRI								
B.2.21.6.2.2	P-5	UNE ISDN/>=10 circuits/Non-Dispatch/TN (hours)	ISDN - BRI								
B.2.21.7.1.1	P-5	Line Sharing/<10 circuits/Dispatch/TN (hours)	ADSL to Retail	5.20	1,659			18.200			
B.2.21.7.1.2	P-5	Line Sharing/<10 circuits/Non-Dispatch/TN (hours)	ADSL to Retail	0.96	2,460	1.07	1	4.452	4.45261	-0.0244	YES
B.2.21.7.2.1	P-5	Line Sharing/>=10 circuits/Dispatch/TN (hours)	ADSL to Retail	11.22	4			22.253			
B.2.21.7.2.2	P-5	Line Sharing/>=10 circuits/Non-Dispatch/TN (hours)	ADSL to Retail	1.22	1			0.000			
B.2.21.8.1.1	P-5	2W Analog Loop Design/<10 circuits/Dispatch/TN (hours)	R&B - Disp	2.25	19,725	26.78	105	44.299	4.33464	-5.6580	NO
B.2.21.8.1.2	P-5	2W Analog Loop Design/<10 circuits/Non-Dispatch/TN (hours)	R&B - Disp	2.25	19,725			15.580			
B.2.21.8.2.1	P-5	2W Analog Loop Design/>=10 circuits/Dispatch/TN (hours)	R&B - Disp	2.44	104			12.336			
B.2.21.8.2.2	P-5	2W Analog Loop Design/>=10 circuits/Non-Dispatch/TN (hours)	R&B - Disp	2.44	104			12.336			
B.2.21.9.1.1	P-5	2W Analog Loop Non-Design/<10 circuits/Dispatch/TN (hours)	R&B (POTS) excl SB Or	2.20	19,209	0.24	48	15.312	2.21280	0.8832	YES
B.2.21.9.1.4	P-5	2W Analog Loop Non-Design/<10 circuits/Dispatch In/TN (hours)	R&B (POTS) excl SB Or	1.12	150,703	0.02	1	8.410	8.40996	0.1309	YES
B.2.21.9.2.1	P-5	2W Analog Loop Non-Design/>=10 circuits/Dispatch/TN (hours)	R&B (POTS) excl SB Or	2.27	88			12.767			
B.2.21.9.2.4	P-5	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/TN (hours)	R&B (POTS) excl SB Or	0.63	1			0.000			
B.2.21.10.1.1	P-5	2W Analog Loop w/INP Design/<10 circuits/Dispatch/TN (hours)	R&B - Disp	2.25	19,725			15.580			
B.2.21.10.1.2	P-5	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/TN (hours)	R&B - Disp	2.25	19,725			15.580			
B.2.21.10.2.1	P-5	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/TN (hours)	R&B - Disp	2.44	104			12.336			
B.2.21.10.2.2	P-5	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/TN (hours)	R&B - Disp	2.44	104			12.336			
B.2.21.11.1.1	P-5	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/TN (hours)	R&B (POTS) excl SB Or	2.20	19,209			15.312			
B.2.21.11.1.4	P-5	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/TN (hours)	R&B (POTS) excl SB Or	1.12	150,703			8.410			
B.2.21.11.2.1	P-5	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/TN (hours)	R&B (POTS) excl SB Or	2.27	88			12.767			
B.2.21.11.2.4	P-5	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/TN (hours)	R&B (POTS) excl SB Or	0.63	1			0.000			
B.2.21.12.1.1	P-5	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/TN (hours)	R&B - Disp	2.25	19,725	12.75	83	43.658	4.80220	-2.1867	NO
B.2.21.12.1.2	P-5	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/TN (hours)	R&B - Disp	2.25	19,725			15.580			
B.2.21.12.2.1	P-5	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/TN (hours)	R&B - Disp	2.44	104			12.336			
B.2.21.12.2.2	P-5	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/TN (hours)	R&B - Disp	2.44	104			12.336			
B.2.21.13.1.1	P-5	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/TN (hours)	R&B (POTS) excl SB Or	2.20	19,209			15.312			
B.2.21.13.1.4	P-5	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/TN (hours)	R&B (POTS) excl SB Or	1.12	150,703			8.410			
B.2.21.13.2.1	P-5	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/TN (hours)	R&B (POTS) excl SB Or	2.27	88			12.767			
B.2.21.13.2.4	P-5	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/TN (hours)	R&B (POTS) excl SB Or	0.63	1			0.000			
B.2.21.14.1.1	P-5	Other Design/<10 circuits/Dispatch/TN (hours)	Design	199.87	1,434			393.173			
B.2.21.14.1.2	P-5	Other Design/<10 circuits/Non-Dispatch/TN (hours)	Design	21.99	225			58.750			
B.2.21.14.2.1	P-5	Other Design/>=10 circuits/Dispatch/TN (hours)	Design	177.56	123			322.537			
B.2.21.14.2.2	P-5	Other Design/>=10 circuits/Non-Dispatch/TN (hours)	Design	31.49	12			50.534			
B.2.21.15.1.1	P-5	Other Non-Design/<10 circuits/Dispatch/TN (hours)	R&B	2.25	19,725			15.580			
B.2.21.15.1.2	P-5	Other Non-Design/<10 circuits/Non-Dispatch/TN (hours)	R&B	1.20	292,219			9.338			
B.2.21.15.2.1	P-5	Other Non-Design/>=10 circuits/Dispatch/TN (hours)	R&B	2.44	104			12.336			
B.2.21.15.2.2	P-5	Other Non-Design/>=10 circuits/Non-Dispatch/TN (hours)	R&B	1.80	65			8.467			
B.2.21.16.1.1	P-5	INP (Standalone)/<10 circuits/Dispatch/TN (hours)	R&B (POTS)	2.20	19,209			15.312			
B.2.21.16.1.2	P-5	INP (Standalone)/<10 circuits/Non-Dispatch/TN (hours)	R&B (POTS)	1.20	290,992			9.276			
B.2.21.16.2.1	P-5	INP (Standalone)/>=10 circuits/Dispatch/TN (hours)	R&B (POTS)	2.27	88			12.767			
B.2.21.16.2.2	P-5	INP (Standalone)/>=10 circuits/Non-Dispatch/TN (hours)	R&B (POTS)	23.62	3			39.271			
B.2.21.17.1.1	P-5	LNP (Standalone)/<10 circuits/Dispatch/TN (hours)	R&B (POTS)	2.20	19,209	0.16	2	15.312	10.82752	0.1881	YES
B.2.21.17.1.2	P-5	LNP (Standalone)/<10 circuits/Non-Dispatch/TN (hours)	R&B (POTS)	1.20	290,992	0.71	248	9.276	0.58926	0.8263	YES
B.2.21.17.2.1	P-5	LNP (Standalone)/>=10 circuits/Dispatch/TN (hours)	R&B (POTS)	2.27	88			12.767			
B.2.21.17.2.2	P-5	LNP (Standalone)/>=10 circuits/Non-Dispatch/TN (hours)	R&B (POTS)	23.62	3	0.75	1	39.271	45.34592	0.5044	YES
B.2.21.18.1.1	P-5	Digital Loop < DS1/<10 circuits/Dispatch/TN (hours)	Digital Loop < DS1	16.28	2,282	16.68	89	83.410	9.01225	-0.0447	YES
B.2.21.18.1.2	P-5	Digital Loop < DS1/<10 circuits/Non-Dispatch/TN (hours)	Digital Loop < DS1	1.71	3,105			13.750			
B.2.21.18.2.1	P-5	Digital Loop < DS1/>=10 circuits/Dispatch/TN (hours)	Digital Loop < DS1	11.22	4			22.253			
B.2.21.18.2.2	P-5	Digital Loop < DS1/>=10 circuits/Non-Dispatch/TN (hours)	Digital Loop < DS1	1.22	1			0.000			
B.2.21.19.1.1	P-5	Digital Loop >= DS1/<10 circuits/Dispatch/TN (hours)	Digital Loop >= DS1	128.70	254	38.86	163	267.760	26.87217	3.3431	YES
B.2.21.19.1.2	P-5	Digital Loop >= DS1/<10 circuits/Non-Dispatch/TN (hours)	Digital Loop >= DS1	10.17	137			27.864			
B.2.21.19.2.1	P-5	Digital Loop >= DS1/>=10 circuits/Dispatch/TN (hours)	Digital Loop >= DS1	338.19	45			446.059			
B.2.21.19.2.2	P-5	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/TN (hours)	Digital Loop >= DS1	11.50	30			34.074			
		Average Completion Notice Interval - Non-Mechanized									
B.2.22.1.1.1	P-5	Switch Ports/<10 circuits/Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.1.1.2	P-5	Switch Ports/<10 circuits/Non-Dispatch/TN (hours)	Diagnostic								Diagnostic

BellSouth Monthly Performance Summary
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			July								Jur			
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.2.21.3.2.4	P-5	Loop + Port Combinations/>=10 circuits/Dispatch In/TN (hours)	0.78	37			0.306				24.29	22		
B.2.21.4.1.1	P-5	Combo Other/<10 circuits/Dispatch/TN (hours)	16.32	19,301	18.59	3	117.257	67.70386	-0.0336	YES	14.18	17,722	32.31	3
B.2.21.4.1.4	P-5	Combo Other/<10 circuits/Dispatch In/TN (hours)	16.32	19,301			117.257				14.18	17,722		
B.2.21.4.2.1	P-5	Combo Other/>=10 circuits/Dispatch/TN (hours)	13.52	82			92.154				0.56	117		
B.2.21.4.2.4	P-5	Combo Other/>=10 circuits/Dispatch In/TN (hours)	13.52	82			92.154				0.56	117		
B.2.21.5.1.1	P-5	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/TN (hours)	5.56	1,652	14.14	43	29.328	4.53026	-1.8948	NO	5.11	1,619	7.35	66
B.2.21.5.1.2	P-5	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/TN (hours)	1.46	2,187			13.765				1.23	2,107		
B.2.21.5.2.1	P-5	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/TN (hours)	5.41	3			9.329				0.02	2		
B.2.21.5.2.2	P-5	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/TN (hours)	1.05	2			0.165				1.03	2		
B.2.21.6.1.1	P-5	UNE ISDN/<10 circuits/Dispatch/TN (hours)	32.08	275	15.36	39	55.837	9.55401	1.7506	YES	50.04	253	10.34	44
B.2.21.6.1.2	P-5	UNE ISDN/<10 circuits/Non-Dispatch/TN (hours)	3.00	598			19.318				2.84	537		
B.2.21.6.2.1	P-5	UNE ISDN/>=10 circuits/Dispatch/TN (hours)	25.57	1			0.000							
B.2.21.6.2.2	P-5	UNE ISDN/>=10 circuits/Non-Dispatch/TN (hours)												
B.2.21.7.1.1	P-5	Line Sharing/<10 circuits/Dispatch/TN (hours)	5.56	1,652			25.251				5.11	1,619		
B.2.21.7.1.2	P-5	Line Sharing/<10 circuits/Non-Dispatch/TN (hours)	1.46	2,187			13.765				1.23	2,107		
B.2.21.7.2.1	P-5	Line Sharing/>=10 circuits/Dispatch/TN (hours)	5.41	3			9.329				0.02	2		
B.2.21.7.2.2	P-5	Line Sharing/>=10 circuits/Non-Dispatch/TN (hours)	1.05	2			0.165				1.03	2		
B.2.21.8.1.1	P-5	2W Analog Loop Design/<10 circuits/Dispatch/TN (hours)	2.72	17,753	20.87	111	34.326	3.26828	-5.5542	NO	1.49	16,291	28.89	153
B.2.21.8.1.2	P-5	2W Analog Loop Design/<10 circuits/Non-Dispatch/TN (hours)	2.72	17,753			19.200				1.49	16,291		
B.2.21.8.2.1	P-5	2W Analog Loop Design/>=10 circuits/Dispatch/TN (hours)	3.16	79			16.757				0.26	114		
B.2.21.8.2.2	P-5	2W Analog Loop Design/>=10 circuits/Non-Dispatch/TN (hours)	3.16	79			16.757				0.26	114		
B.2.21.9.1.1	P-5	2W Analog Loop Non-Design/<10 circuits/Dispatch/TN (hours)	2.69	17,420	0.06	48	19.209	2.77646	0.9461	YES	1.22	15,962	0.02	36
B.2.21.9.1.4	P-5	2W Analog Loop Non-Design/<10 circuits/Dispatch In/TN (hours)	1.12	141,014			7.956				0.94	128,074		
B.2.21.9.2.1	P-5	2W Analog Loop Non-Design/>=10 circuits/Dispatch/TN (hours)	3.50	71			17.656				0.35	70		
B.2.21.9.2.4	P-5	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/TN (hours)												
B.2.21.10.1.1	P-5	2W Analog Loop w/INP Design/<10 circuits/Dispatch/TN (hours)	2.72	17,753			19.200				1.49	16,291		
B.2.21.10.1.2	P-5	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/TN (hours)	2.72	17,753			19.200				1.49	16,291		
B.2.21.10.2.1	P-5	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/TN (hours)	3.16	79			16.757				0.26	114		
B.2.21.10.2.2	P-5	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/TN (hours)	3.16	79			16.757				0.26	114		
B.2.21.11.1.1	P-5	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/TN (hours)	2.69	17,420			19.209				1.22	15,962		
B.2.21.11.1.4	P-5	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/TN (hours)	1.12	141,014			7.956				0.94	128,074		
B.2.21.11.2.1	P-5	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/TN (hours)	3.50	71			17.656				0.35	70		
B.2.21.11.2.4	P-5	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/TN (hours)												
B.2.21.12.1.1	P-5	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/TN (hours)	2.72	17,753	20.43	80	90.055	10.09115	-1.7553	NO	1.49	16,291	6.92	77
B.2.21.12.1.2	P-5	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/TN (hours)	2.72	17,753			19.200				1.49	16,291		
B.2.21.12.2.1	P-5	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/TN (hours)	3.16	79			16.757				0.26	114		
B.2.21.12.2.2	P-5	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/TN (hours)	3.16	79			16.757				0.26	114		
B.2.21.13.1.1	P-5	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/TN (hours)	2.69	17,420			19.209				1.22	15,962		
B.2.21.13.1.4	P-5	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/TN (hours)	1.12	141,014			7.956				0.94	128,074		
B.2.21.13.2.1	P-5	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/TN (hours)	3.50	71			17.656				0.35	70		
B.2.21.13.2.4	P-5	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/TN (hours)												
B.2.21.14.1.1	P-5	Other Design/<10 circuits/Dispatch/TN (hours)	172.27	1,548			375.289				158.72	1,431		
B.2.21.14.1.2	P-5	Other Design/<10 circuits/Non-Dispatch/TN (hours)	33.67	217			122.798				12.56	382		
B.2.21.14.2.1	P-5	Other Design/>=10 circuits/Dispatch/TN (hours)	286.32	3			465.966				12.24	3		
B.2.21.14.2.2	P-5	Other Design/>=10 circuits/Non-Dispatch/TN (hours)												
B.2.21.15.1.1	P-5	Other Non-Design/<10 circuits/Dispatch/TN (hours)	2.72	17,753			19.200				1.49	16,291		
B.2.21.15.1.2	P-5	Other Non-Design/<10 circuits/Non-Dispatch/TN (hours)	1.18	275,448			8.506				0.97	251,549		
B.2.21.15.2.1	P-5	Other Non-Design/>=10 circuits/Dispatch/TN (hours)	3.16	79			16.757				0.26	114		
B.2.21.15.2.2	P-5	Other Non-Design/>=10 circuits/Non-Dispatch/TN (hours)	0.81	47			0.291				15.56	35		
B.2.21.16.1.1	P-5	INP (Standalone)/<10 circuits/Dispatch/TN (hours)	2.69	17,420			19.209				1.22	15,962		
B.2.21.16.1.2	P-5	INP (Standalone)/<10 circuits/Non-Dispatch/TN (hours)	1.18	273,262			8.520				0.96	250,415		
B.2.21.16.2.1	P-5	INP (Standalone)/>=10 circuits/Dispatch/TN (hours)	3.50	71			17.656				0.35	70		
B.2.21.16.2.2	P-5	INP (Standalone)/>=10 circuits/Non-Dispatch/TN (hours)	0.83	2			0.012							
B.2.21.17.1.1	P-5	LNP (Standalone)/<10 circuits/Dispatch/TN (hours)	2.69	17,420			19.209				1.22	15,962		
B.2.21.17.1.2	P-5	LNP (Standalone)/<10 circuits/Non-Dispatch/TN (hours)	1.18	273,262	0.71	245	8.520	0.54455	0.8719	YES	0.96	250,415	0.67	206
B.2.21.17.2.1	P-5	LNP (Standalone)/>=10 circuits/Dispatch/TN (hours)	3.50	71			17.656				0.35	70		
B.2.21.17.2.2	P-5	LNP (Standalone)/>=10 circuits/Non-Dispatch/TN (hours)	0.83	2	0.82	1	0.012	0.01443	0.5774	YES			0.43	1
B.2.21.18.1.1	P-5	Digital Loop < DS1/<10 circuits/Dispatch/TN (hours)	15.83	2,046	14.05	76	67.474	7.88224	0.2262	YES	18.69	2,076	8.83	104
B.2.21.18.1.2	P-5	Digital Loop < DS1/<10 circuits/Non-Dispatch/TN (hours)	1.84	2,879			15.354				1.89	2,844		
B.2.21.18.2.1	P-5	Digital Loop < DS1/>=10 circuits/Dispatch/TN (hours)	10.45	4			12.633				0.02	2		
B.2.21.18.2.2	P-5	Digital Loop < DS1/>=10 circuits/Non-Dispatch/TN (hours)	1.05	2			0.165				1.03	2		
B.2.21.19.1.1	P-5	Digital Loop >= DS1/<10 circuits/Dispatch/TN (hours)	119.68	391	43.98	138	255.370	25.28536	2.9935	YES	134.39	318	34.84	194
B.2.21.19.1.2	P-5	Digital Loop >= DS1/<10 circuits/Non-Dispatch/TN (hours)	22.77	222			116.592				7.95	260		
B.2.21.19.2.1	P-5	Digital Loop >= DS1/>=10 circuits/Dispatch/TN (hours)	824.28	1			0.000							
B.2.21.19.2.2	P-5	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/TN (hours)	0.72	15			0.269				74.77	7		
		Average Completion Notice Interval - Non-Mechanized												
B.2.22.1.1.1	P-5	Switch Ports/<10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.1.1.2	P-5	Switch Ports/<10 circuits/Non-Dispatch/TN (hours)								Diagnostic				

BellSouth Monthly Performance Summary

Tennessee, August 2002 - M ay 2002

			ne				May							
			Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.21.3.2.4	P-5	Loop + Port Combinations/>=10 circuits/Dispatch In/TN (hours)	110.429				0.77	53	0.90	1	0.297	0.29950	-0.4441	YES
B.2.21.4.1.1	P-5	Combo Other/<10 circuits/Dispatch/TN (hours)	122.518	70.74192	-0.2562	YES	9.87	21,619			132.826			
B.2.21.4.1.4	P-5	Combo Other/<10 circuits/Dispatch In/TN (hours)	122.518											
B.2.21.4.2.1	P-5	Combo Other/>=10 circuits/Dispatch/TN (hours)	3.764				10.52	113			77.396			
B.2.21.4.2.4	P-5	Combo Other/>=10 circuits/Dispatch In/TN (hours)	3.764											
B.2.21.5.1.1	P-5	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/TN (hours)	30.978	3.89006	-0.5752	YES	8.33	1,554			26.912			
B.2.21.5.1.2	P-5	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/TN (hours)	11.676				1.00	1,955			10.287			
B.2.21.5.2.1	P-5	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/TN (hours)	0.000				28.39	4			56.708			
B.2.21.5.2.2	P-5	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/TN (hours)	0.024				406.93	1			0.000			
B.2.21.6.1.1	P-5	UNE ISDN/<10 circuits/Dispatch/TN (hours)	358.083	58.48916	0.6788	YES	34.80	326	25.62	48	106.904	16.52725	0.5552	YES
B.2.21.6.1.2	P-5	UNE ISDN/<10 circuits/Non-Dispatch/TN (hours)	16.548				2.68	503			14.912			
B.2.21.6.2.1	P-5	UNE ISDN/>=10 circuits/Dispatch/TN (hours)												
B.2.21.6.2.2	P-5	UNE ISDN/>=10 circuits/Non-Dispatch/TN (hours)												
B.2.21.7.1.1	P-5	Line Sharing/<10 circuits/Dispatch/TN (hours)	17.384				8.33	1,554	55.33	1	26.912	26.92037	-1.7460	NO
B.2.21.7.1.2	P-5	Line Sharing/<10 circuits/Non-Dispatch/TN (hours)	11.676				1.00	1,955			10.287			
B.2.21.7.2.1	P-5	Line Sharing/>=10 circuits/Dispatch/TN (hours)	0.000				28.39	4			56.708			
B.2.21.7.2.2	P-5	Line Sharing/>=10 circuits/Non-Dispatch/TN (hours)	0.024				406.93	1			0.000			
B.2.21.8.1.1	P-5	2W Analog Loop Design/<10 circuits/Dispatch/TN (hours)	46.862	3.80634	-7.1976	NO	1.09	20,239	18.02	152	29.315	2.38664	-7.0938	NO
B.2.21.8.1.2	P-5	2W Analog Loop Design/<10 circuits/Non-Dispatch/TN (hours)	17.986				1.09	20,239			11.055			
B.2.21.8.2.1	P-5	2W Analog Loop Design/>=10 circuits/Dispatch/TN (hours)	1.701				8.73	112			75.352			
B.2.21.8.2.2	P-5	2W Analog Loop Design/>=10 circuits/Non-Dispatch/TN (hours)	1.701				8.73	112			75.352			
B.2.21.9.1.1	P-5	2W Analog Loop Non-Design/<10 circuits/Dispatch/TN (hours)	16.021	2.67315	0.4512	YES	1.03	19,946	0.02	28	10.223	1.93335	0.5251	YES
B.2.21.9.1.4	P-5	2W Analog Loop Non-Design/<10 circuits/Dispatch In/TN (hours)	21.367				0.84	140,355			4.071			
B.2.21.9.2.1	P-5	2W Analog Loop Non-Design/>=10 circuits/Dispatch/TN (hours)	2.162				1.82	101			9.957			
B.2.21.9.2.4	P-5	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/TN (hours)					0.48	6			0.207			
B.2.21.10.1.1	P-5	2W Analog Loop w/INP Design/<10 circuits/Dispatch/TN (hours)	17.986				1.09	20,239			11.055			
B.2.21.10.1.2	P-5	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/TN (hours)	17.986				1.09	20,239			11.055			
B.2.21.10.2.1	P-5	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/TN (hours)	1.701				8.73	112			75.352			
B.2.21.10.2.2	P-5	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/TN (hours)	1.701				8.73	112			75.352			
B.2.21.11.1.1	P-5	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/TN (hours)	16.021				1.03	19,946			10.223			
B.2.21.11.1.4	P-5	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/TN (hours)	21.367				0.84	140,355			4.071			
B.2.21.11.2.1	P-5	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/TN (hours)	2.162				1.82	101			9.957			
B.2.21.11.2.4	P-5	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/TN (hours)					0.48	6			0.207			
B.2.21.12.1.1	P-5	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/TN (hours)	39.573	4.52043	-1.2013	YES	1.09	20,239	14.98	88	38.954	4.16156	-3.3369	NO
B.2.21.12.1.2	P-5	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/TN (hours)	17.986				1.09	20,239			11.055			
B.2.21.12.2.1	P-5	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/TN (hours)	1.701				8.73	112			75.352			
B.2.21.12.2.2	P-5	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/TN (hours)	1.701				8.73	112			75.352			
B.2.21.13.1.1	P-5	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/TN (hours)	16.021				1.03	19,946			10.223			
B.2.21.13.1.4	P-5	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/TN (hours)	21.367				0.84	140,355			4.071			
B.2.21.13.2.1	P-5	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/TN (hours)	2.162				1.82	101			9.957			
B.2.21.13.2.4	P-5	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/TN (hours)					0.48	6			0.207			
B.2.21.14.1.1	P-5	Other Design/<10 circuits/Dispatch/TN (hours)	399.489				138.55	1,380			507.034			
B.2.21.14.1.2	P-5	Other Design/<10 circuits/Non-Dispatch/TN (hours)	47.422				25.96	255			74.404			
B.2.21.14.2.1	P-5	Other Design/>=10 circuits/Dispatch/TN (hours)	21.170				211.23	1			0.000			
B.2.21.14.2.2	P-5	Other Design/>=10 circuits/Non-Dispatch/TN (hours)					0.82	1			0.000			
B.2.21.15.1.1	P-5	Other Non-Design/<10 circuits/Dispatch/TN (hours)	17.986				1.09	20,239			11.055			
B.2.21.15.1.2	P-5	Other Non-Design/<10 circuits/Non-Dispatch/TN (hours)	15.669				0.92	273,098			5.252			
B.2.21.15.2.1	P-5	Other Non-Design/>=10 circuits/Dispatch/TN (hours)	1.701				8.73	112			75.352			
B.2.21.15.2.2	P-5	Other Non-Design/>=10 circuits/Non-Dispatch/TN (hours)	87.547				0.76	65			0.286			
B.2.21.16.1.1	P-5	INP (Standalone)/<10 circuits/Dispatch/TN (hours)	16.021				1.03	19,946			10.223			
B.2.21.16.1.2	P-5	INP (Standalone)/<10 circuits/Non-Dispatch/TN (hours)	15.687				0.92	271,843			5.251			
B.2.21.16.2.1	P-5	INP (Standalone)/>=10 circuits/Dispatch/TN (hours)	2.162				1.82	101			9.957			
B.2.21.16.2.2	P-5	INP (Standalone)/>=10 circuits/Non-Dispatch/TN (hours)					0.55	10			0.234			
B.2.21.17.1.1	P-5	LNP (Standalone)/<10 circuits/Dispatch/TN (hours)	16.021				1.03	19,946			10.223			
B.2.21.17.1.2	P-5	LNP (Standalone)/<10 circuits/Non-Dispatch/TN (hours)	15.687	1.09338	0.2624	YES	0.92	271,843	0.66	147	5.251	0.43325	0.6060	YES
B.2.21.17.2.1	P-5	LNP (Standalone)/>=10 circuits/Dispatch/TN (hours)	2.162				1.82	101			9.957			
B.2.21.17.2.2	P-5	LNP (Standalone)/>=10 circuits/Non-Dispatch/TN (hours)	0.000				0.55	10			0.234			
B.2.21.18.1.1	P-5	Digital Loop < DS1/<10 circuits/Dispatch/TN (hours)	138.584	13.92549	0.7081	YES	16.86	2,042	25.62	48	106.904	15.61057	-0.5616	YES
B.2.21.18.1.2	P-5	Digital Loop < DS1/<10 circuits/Non-Dispatch/TN (hours)	15.486				2.44	2,632			19.032			
B.2.21.18.2.1	P-5	Digital Loop < DS1/>=10 circuits/Dispatch/TN (hours)	0.000				28.39	4			56.708			
B.2.21.18.2.2	P-5	Digital Loop < DS1/>=10 circuits/Non-Dispatch/TN (hours)	0.024				203.88	2			287.168			
B.2.21.19.1.1	P-5	Digital Loop >= DS1/<10 circuits/Dispatch/TN (hours)	239.589	21.82671	4.5607	YES	82.12	205	32.58	148	188.933	20.37924	2.4310	YES
B.2.21.19.1.2	P-5	Digital Loop >= DS1/<10 circuits/Non-Dispatch/TN (hours)	41.640				16.18	132			73.533			
B.2.21.19.2.1	P-5	Digital Loop >= DS1/>=10 circuits/Dispatch/TN (hours)												
B.2.21.19.2.2	P-5	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/TN (hours)	195.754				0.71	16			0.274			
		Average Completion Notice Interval - Non-Mechanized												
B.2.22.1.1.1	P-5	Switch Ports/<10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.1.1.2	P-5	Switch Ports/<10 circuits/Non-Dispatch/TN (hours)				Diagnostic								Diagnostic

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			Benchmark/ Analog	August BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.22.1.2.1	P-5	Switch Ports/>=10 circuits/Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.1.2.2	P-5	Switch Ports/>=10 circuits/Non-Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.2.1.1	P-5	Local Interoffice Transport/<10 circuits/Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.2.1.2	P-5	Local Interoffice Transport/<10 circuits/Non-Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.2.2.1	P-5	Local Interoffice Transport/>=10 circuits/Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.2.2.2	P-5	Local Interoffice Transport/>=10 circuits/Non-Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.3.1.1	P-5	Loop + Port Combinations/<10 circuits/Dispatch/TN (hours)	Diagnostic			20.09	82				Diagnostic
B.2.22.3.1.2	P-5	Loop + Port Combinations/<10 circuits/Non-Dispatch/TN (hours)	Diagnostic			6.78	240				Diagnostic
B.2.22.3.1.3	P-5	Loop + Port Combinations/<10 circuits/Switch Based Orders/TN (hours)	Diagnostic			4.97	186				Diagnostic
B.2.22.3.1.4	P-5	Loop + Port Combinations/<10 circuits/Dispatch In/TN (hours)	Diagnostic			12.98	54				Diagnostic
B.2.22.3.2.1	P-5	Loop + Port Combinations/>=10 circuits/Dispatch/TN (hours)	Diagnostic			40.73	1				Diagnostic
B.2.22.3.2.2	P-5	Loop + Port Combinations/>=10 circuits/Non-Dispatch/TN (hours)	Diagnostic			27.10	1				Diagnostic
B.2.22.3.2.3	P-5	Loop + Port Combinations/>=10 circuits/Switch Based Orders/TN (hours)	Diagnostic								Diagnostic
B.2.22.3.2.4	P-5	Loop + Port Combinations/>=10 circuits/Dispatch In/TN (hours)	Diagnostic			27.10	1				Diagnostic
B.2.22.4.1.1	P-5	Combo Other/<10 circuits/Dispatch/TN (hours)	Diagnostic			48.50	30				Diagnostic
B.2.22.4.1.4	P-5	Combo Other/<10 circuits/Dispatch In/TN (hours)	Diagnostic								Diagnostic
B.2.22.4.2.1	P-5	Combo Other/>=10 circuits/Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.4.2.4	P-5	Combo Other/>=10 circuits/Dispatch In/TN (hours)	Diagnostic								Diagnostic
B.2.22.5.1.1	P-5	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/TN (hours)	Diagnostic			19.99	11				Diagnostic
B.2.22.5.1.2	P-5	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.5.2.1	P-5	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.5.2.2	P-5	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.6.1.1	P-5	UNE ISDN/<10 circuits/Dispatch/TN (hours)	Diagnostic			33.79	5				Diagnostic
B.2.22.6.1.2	P-5	UNE ISDN/<10 circuits/Non-Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.6.2.1	P-5	UNE ISDN/>=10 circuits/Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.6.2.2	P-5	UNE ISDN/>=10 circuits/Non-Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.7.1.1	P-5	Line Sharing/<10 circuits/Dispatch/TN (hours)	Diagnostic			0.02	3				Diagnostic
B.2.22.7.1.2	P-5	Line Sharing/<10 circuits/Non-Dispatch/TN (hours)	Diagnostic			3.77	50				Diagnostic
B.2.22.7.2.1	P-5	Line Sharing/>=10 circuits/Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.7.2.2	P-5	Line Sharing/>=10 circuits/Non-Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.8.1.1	P-5	2W Analog Loop Design/<10 circuits/Dispatch/TN (hours)	Diagnostic			90.47	11				Diagnostic
B.2.22.8.1.2	P-5	2W Analog Loop Design/<10 circuits/Non-Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.8.2.1	P-5	2W Analog Loop Design/>=10 circuits/Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.8.2.2	P-5	2W Analog Loop Design/>=10 circuits/Non-Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.9.1.1	P-5	2W Analog Loop Non-Design/<10 circuits/Dispatch/TN (hours)	Diagnostic			17.82	2				Diagnostic
B.2.22.9.1.4	P-5	2W Analog Loop Non-Design/<10 circuits/Dispatch In/TN (hours)	Diagnostic								Diagnostic
B.2.22.9.2.1	P-5	2W Analog Loop Non-Design/>=10 circuits/Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.9.2.4	P-5	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/TN (hours)	Diagnostic								Diagnostic
B.2.22.10.1.1	P-5	2W Analog Loop w/INP Design/<10 circuits/Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.10.1.2	P-5	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.10.2.1	P-5	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.10.2.2	P-5	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.11.1.1	P-5	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.11.1.4	P-5	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/TN (hours)	Diagnostic			13.80	1				Diagnostic
B.2.22.11.2.1	P-5	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.11.2.4	P-5	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/TN (hours)	Diagnostic								Diagnostic
B.2.22.12.1.1	P-5	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/TN (hours)	Diagnostic			17.55	5				Diagnostic
B.2.22.12.1.2	P-5	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.12.2.1	P-5	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.12.2.2	P-5	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.13.1.1	P-5	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/TN (hours)	Diagnostic			0.02	1				Diagnostic
B.2.22.13.1.4	P-5	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/TN (hours)	Diagnostic								Diagnostic
B.2.22.13.2.1	P-5	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.13.2.4	P-5	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/TN (hours)	Diagnostic								Diagnostic
B.2.22.14.1.1	P-5	Other Design/<10 circuits/Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.14.1.2	P-5	Other Design/<10 circuits/Non-Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.14.2.1	P-5	Other Design/>=10 circuits/Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.14.2.2	P-5	Other Design/>=10 circuits/Non-Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.15.1.1	P-5	Other Non-Design/<10 circuits/Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.15.1.2	P-5	Other Non-Design/<10 circuits/Non-Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.15.2.1	P-5	Other Non-Design/>=10 circuits/Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.15.2.2	P-5	Other Non-Design/>=10 circuits/Non-Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.16.1.1	P-5	INP (Standalone)/<10 circuits/Dispatch/TN (hours)	Diagnostic			21.89	3				Diagnostic
B.2.22.16.1.2	P-5	INP (Standalone)/<10 circuits/Non-Dispatch/TN (hours)	Diagnostic			55.68	4				Diagnostic
B.2.22.16.2.1	P-5	INP (Standalone)/>=10 circuits/Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.16.2.2	P-5	INP (Standalone)/>=10 circuits/Non-Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.17.1.1	P-5	LNP (Standalone)/<10 circuits/Dispatch/TN (hours)	Diagnostic			0.02	1				Diagnostic
B.2.22.17.1.2	P-5	LNP (Standalone)/<10 circuits/Non-Dispatch/TN (hours)	Diagnostic			3.48	249				Diagnostic

BellSouth Monthly Performance Summary

Tennessee, August 2002 - M ay 2002

			July								Jul			
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.2.22.1.2.1	P-5	Switch Ports/>=10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.1.2.2	P-5	Switch Ports/>=10 circuits/Non-Dispatch/TN (hours)								Diagnostic				
B.2.22.2.1.1	P-5	Local Interoffice Transport/<10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.2.1.2	P-5	Local Interoffice Transport/<10 circuits/Non-Dispatch/TN (hours)								Diagnostic				
B.2.22.2.2.1	P-5	Local Interoffice Transport/>=10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.2.2.2	P-5	Local Interoffice Transport/>=10 circuits/Non-Dispatch/TN (hours)								Diagnostic				
B.2.22.3.1.1	P-5	Loop + Port Combinations/<10 circuits/Dispatch/TN (hours)			22.49	52				Diagnostic			16.49	54
B.2.22.3.1.2	P-5	Loop + Port Combinations/<10 circuits/Non-Dispatch/TN (hours)			8.10	182				Diagnostic			9.31	275
B.2.22.3.1.3	P-5	Loop + Port Combinations/<10 circuits/Switch Based Orders/TN (hours)			4.38	129				Diagnostic			7.56	201
B.2.22.3.1.4	P-5	Loop + Port Combinations/<10 circuits/Dispatch In/TN (hours)			17.18	53				Diagnostic			14.06	74
B.2.22.3.2.1	P-5	Loop + Port Combinations/>=10 circuits/Dispatch/TN (hours)								Diagnostic			17.24	2
B.2.22.3.2.2	P-5	Loop + Port Combinations/>=10 circuits/Non-Dispatch/TN (hours)								Diagnostic				
B.2.22.3.2.3	P-5	Loop + Port Combinations/>=10 circuits/Switch Based Orders/TN (hours)								Diagnostic				
B.2.22.3.2.4	P-5	Loop + Port Combinations/>=10 circuits/Dispatch In/TN (hours)								Diagnostic				
B.2.22.4.1.1	P-5	Combo Other/<10 circuits/Dispatch/TN (hours)			37.58	35				Diagnostic			59.51	33
B.2.22.4.1.4	P-5	Combo Other/<10 circuits/Dispatch In/TN (hours)								Diagnostic				
B.2.22.4.2.1	P-5	Combo Other/>=10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.4.2.4	P-5	Combo Other/>=10 circuits/Dispatch In/TN (hours)								Diagnostic				
B.2.22.5.1.1	P-5	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/TN (hours)			11.46	39				Diagnostic			16.37	47
B.2.22.5.1.2	P-5	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/TN (hours)								Diagnostic				
B.2.22.5.2.1	P-5	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.5.2.2	P-5	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/TN (hours)								Diagnostic				
B.2.22.6.1.1	P-5	UNE ISDN/<10 circuits/Dispatch/TN (hours)			7.64	23				Diagnostic			92.87	10
B.2.22.6.1.2	P-5	UNE ISDN/<10 circuits/Non-Dispatch/TN (hours)								Diagnostic				
B.2.22.6.2.1	P-5	UNE ISDN/>=10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.6.2.2	P-5	UNE ISDN/>=10 circuits/Non-Dispatch/TN (hours)								Diagnostic				
B.2.22.7.1.1	P-5	Line Sharing/<10 circuits/Dispatch/TN (hours)			0.02	7				Diagnostic			0.02	5
B.2.22.7.1.2	P-5	Line Sharing/<10 circuits/Non-Dispatch/TN (hours)			0.77	48				Diagnostic			0.78	38
B.2.22.7.2.1	P-5	Line Sharing/>=10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.7.2.2	P-5	Line Sharing/>=10 circuits/Non-Dispatch/TN (hours)								Diagnostic				
B.2.22.8.1.1	P-5	2W Analog Loop Design/<10 circuits/Dispatch/TN (hours)			54.78	15				Diagnostic			52.85	25
B.2.22.8.1.2	P-5	2W Analog Loop Design/<10 circuits/Non-Dispatch/TN (hours)								Diagnostic				
B.2.22.8.2.1	P-5	2W Analog Loop Design/>=10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.8.2.2	P-5	2W Analog Loop Design/>=10 circuits/Non-Dispatch/TN (hours)								Diagnostic				
B.2.22.9.1.1	P-5	2W Analog Loop Non-Design/<10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.9.1.4	P-5	2W Analog Loop Non-Design/<10 circuits/Dispatch In/TN (hours)								Diagnostic				
B.2.22.9.2.1	P-5	2W Analog Loop Non-Design/>=10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.9.2.4	P-5	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/TN (hours)								Diagnostic				
B.2.22.10.1.1	P-5	2W Analog Loop w/INP Design/<10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.10.1.2	P-5	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/TN (hours)								Diagnostic				
B.2.22.10.2.1	P-5	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.10.2.2	P-5	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/TN (hours)								Diagnostic				
B.2.22.11.1.1	P-5	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.11.1.4	P-5	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/TN (hours)								Diagnostic				
B.2.22.11.2.1	P-5	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.11.2.4	P-5	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/TN (hours)								Diagnostic				
B.2.22.12.1.1	P-5	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/TN (hours)			167.62	7				Diagnostic			21.19	23
B.2.22.12.1.2	P-5	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/TN (hours)								Diagnostic				
B.2.22.12.2.1	P-5	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.12.2.2	P-5	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/TN (hours)								Diagnostic				
B.2.22.13.1.1	P-5	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.13.1.4	P-5	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/TN (hours)			13.77	1				Diagnostic				
B.2.22.13.2.1	P-5	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.13.2.4	P-5	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/TN (hours)								Diagnostic				
B.2.22.14.1.1	P-5	Other Design/<10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.14.1.2	P-5	Other Design/<10 circuits/Non-Dispatch/TN (hours)								Diagnostic				
B.2.22.14.2.1	P-5	Other Design/>=10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.14.2.2	P-5	Other Design/>=10 circuits/Non-Dispatch/TN (hours)								Diagnostic				
B.2.22.15.1.1	P-5	Other Non-Design/<10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.15.1.2	P-5	Other Non-Design/<10 circuits/Non-Dispatch/TN (hours)								Diagnostic				
B.2.22.15.2.1	P-5	Other Non-Design/>=10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.15.2.2	P-5	Other Non-Design/>=10 circuits/Non-Dispatch/TN (hours)								Diagnostic				
B.2.22.16.1.1	P-5	INP (Standalone)/<10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.16.1.2	P-5	INP (Standalone)/<10 circuits/Non-Dispatch/TN (hours)								Diagnostic				
B.2.22.16.2.1	P-5	INP (Standalone)/>=10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.16.2.2	P-5	INP (Standalone)/>=10 circuits/Non-Dispatch/TN (hours)								Diagnostic				
B.2.22.17.1.1	P-5	LNP (Standalone)/<10 circuits/Dispatch/TN (hours)			0.02	1				Diagnostic				
B.2.22.17.1.2	P-5	LNP (Standalone)/<10 circuits/Non-Dispatch/TN (hours)			3.57	223				Diagnostic			2.69	249

BellSouth Monthly Performance Summary

Tennessee, August 2002 - May 2002

			ne					May						
			Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.22.1.2.1	P-5	Switch Ports/>=10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.1.2.2	P-5	Switch Ports/>=10 circuits/Non-Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.2.1.1	P-5	Local Interoffice Transport/<10 circuits/Dispatch/TN (hours)				Diagnostic			16.46	11				Diagnostic
B.2.22.2.1.2	P-5	Local Interoffice Transport/<10 circuits/Non-Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.2.2.1	P-5	Local Interoffice Transport/>=10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.2.2.2	P-5	Local Interoffice Transport/>=10 circuits/Non-Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.3.1.1	P-5	Loop + Port Combinations/<10 circuits/Dispatch/TN (hours)				Diagnostic			14.64	42				Diagnostic
B.2.22.3.1.2	P-5	Loop + Port Combinations/<10 circuits/Non-Dispatch/TN (hours)				Diagnostic			4.18	289				Diagnostic
B.2.22.3.1.3	P-5	Loop + Port Combinations/<10 circuits/Switch Based Orders/TN (hours)				Diagnostic			2.04	225				Diagnostic
B.2.22.3.1.4	P-5	Loop + Port Combinations/<10 circuits/Dispatch In/TN (hours)				Diagnostic			11.69	64				Diagnostic
B.2.22.3.2.1	P-5	Loop + Port Combinations/>=10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.3.2.2	P-5	Loop + Port Combinations/>=10 circuits/Non-Dispatch/TN (hours)				Diagnostic			14.00	2				Diagnostic
B.2.22.3.2.3	P-5	Loop + Port Combinations/>=10 circuits/Switch Based Orders/TN (hours)				Diagnostic								Diagnostic
B.2.22.3.2.4	P-5	Loop + Port Combinations/>=10 circuits/Dispatch In/TN (hours)				Diagnostic			14.00	2				Diagnostic
B.2.22.4.1.1	P-5	Combo Other/<10 circuits/Dispatch/TN (hours)				Diagnostic			70.76	41				Diagnostic
B.2.22.4.1.4	P-5	Combo Other/<10 circuits/Dispatch In/TN (hours)				Diagnostic								Diagnostic
B.2.22.4.2.1	P-5	Combo Other/>=10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.4.2.4	P-5	Combo Other/>=10 circuits/Dispatch In/TN (hours)				Diagnostic								Diagnostic
B.2.22.5.1.1	P-5	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/TN (hours)				Diagnostic			11.93	89				Diagnostic
B.2.22.5.1.2	P-5	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.5.2.1	P-5	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.5.2.2	P-5	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.6.1.1	P-5	UNE ISDN/<10 circuits/Dispatch/TN (hours)				Diagnostic			31.47	14				Diagnostic
B.2.22.6.1.2	P-5	UNE ISDN/<10 circuits/Non-Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.6.2.1	P-5	UNE ISDN/>=10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.6.2.2	P-5	UNE ISDN/>=10 circuits/Non-Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.7.1.1	P-5	Line Sharing/<10 circuits/Dispatch/TN (hours)				Diagnostic			0.34	4				Diagnostic
B.2.22.7.1.2	P-5	Line Sharing/<10 circuits/Non-Dispatch/TN (hours)				Diagnostic			0.68	45				Diagnostic
B.2.22.7.2.1	P-5	Line Sharing/>=10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.7.2.2	P-5	Line Sharing/>=10 circuits/Non-Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.8.1.1	P-5	2W Analog Loop Design/<10 circuits/Dispatch/TN (hours)				Diagnostic			46.98	15				Diagnostic
B.2.22.8.1.2	P-5	2W Analog Loop Design/<10 circuits/Non-Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.8.2.1	P-5	2W Analog Loop Design/>=10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.8.2.2	P-5	2W Analog Loop Design/>=10 circuits/Non-Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.9.1.1	P-5	2W Analog Loop Non-Design/<10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.9.1.4	P-5	2W Analog Loop Non-Design/<10 circuits/Dispatch In/TN (hours)				Diagnostic								Diagnostic
B.2.22.9.2.1	P-5	2W Analog Loop Non-Design/>=10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.9.2.4	P-5	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/TN (hours)				Diagnostic								Diagnostic
B.2.22.10.1.1	P-5	2W Analog Loop w/INP Design/<10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.10.1.2	P-5	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.10.2.1	P-5	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.10.2.2	P-5	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.11.1.1	P-5	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.11.1.4	P-5	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/TN (hours)				Diagnostic								Diagnostic
B.2.22.11.2.1	P-5	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.11.2.4	P-5	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/TN (hours)				Diagnostic								Diagnostic
B.2.22.12.1.1	P-5	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/TN (hours)				Diagnostic			53.77	9				Diagnostic
B.2.22.12.1.2	P-5	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.12.2.1	P-5	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.12.2.2	P-5	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.13.1.1	P-5	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.13.1.4	P-5	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/TN (hours)				Diagnostic								Diagnostic
B.2.22.13.2.1	P-5	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.13.2.4	P-5	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/TN (hours)				Diagnostic								Diagnostic
B.2.22.14.1.1	P-5	Other Design/<10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.14.1.2	P-5	Other Design/<10 circuits/Non-Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.14.2.1	P-5	Other Design/>=10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.14.2.2	P-5	Other Design/>=10 circuits/Non-Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.15.1.1	P-5	Other Non-Design/<10 circuits/Dispatch/TN (hours)				Diagnostic			20.40	1				Diagnostic
B.2.22.15.1.2	P-5	Other Non-Design/<10 circuits/Non-Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.15.2.1	P-5	Other Non-Design/>=10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.15.2.2	P-5	Other Non-Design/>=10 circuits/Non-Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.16.1.1	P-5	INP (Standalone)/<10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.16.1.2	P-5	INP (Standalone)/<10 circuits/Non-Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.16.2.1	P-5	INP (Standalone)/>=10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.16.2.2	P-5	INP (Standalone)/>=10 circuits/Non-Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.17.1.1	P-5	LNP (Standalone)/<10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.17.1.2	P-5	LNP (Standalone)/<10 circuits/Non-Dispatch/TN (hours)				Diagnostic			3.17	203				Diagnostic

BellSouth Monthly Performance Summary

Tennessee, August 2002 - May 2002

			Benchmark/ Analog	August							
				BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.22.17.2.1	P-5	LNP (Standalone)/>=10 circuits/Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.17.2.2	P-5	LNP (Standalone)/>=10 circuits/Non-Dispatch/TN (hours)	Diagnostic			0.71	7				Diagnostic
B.2.22.18.1.1	P-5	Digital Loop < DS1/<10 circuits/Dispatch/TN (hours)	Diagnostic			24.33	15				Diagnostic
B.2.22.18.1.2	P-5	Digital Loop < DS1/<10 circuits/Non-Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.18.2.1	P-5	Digital Loop < DS1/>=10 circuits/Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.18.2.2	P-5	Digital Loop < DS1/>=10 circuits/Non-Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.19.1.1	P-5	Digital Loop >= DS1/<10 circuits/Dispatch/TN (hours)	Diagnostic			54.95	30				Diagnostic
B.2.22.19.1.2	P-5	Digital Loop >= DS1/<10 circuits/Non-Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.19.2.1	P-5	Digital Loop >= DS1/>=10 circuits/Dispatch/TN (hours)	Diagnostic								Diagnostic
B.2.22.19.2.2	P-5	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/TN (hours)	Diagnostic								Diagnostic
		Total Service Order Cycle Time - Mechanized									
B.2.24.1.1.1	P-10	Switch Ports/<10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.1.1.2	P-10	Switch Ports/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.1.2.1	P-10	Switch Ports/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.1.2.2	P-10	Switch Ports/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.2.1.1	P-10	Local Interoffice Transport/<10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.2.1.2	P-10	Local Interoffice Transport/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.2.2.1	P-10	Local Interoffice Transport/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.2.2.2	P-10	Local Interoffice Transport/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.3.1.1	P-10	Loop + Port Combinations/<10 circuits/Dispatch/TN (days)	Diagnostic			2.87	106				Diagnostic
B.2.24.3.1.2	P-10	Loop + Port Combinations/<10 circuits/Non-Dispatch/TN (days)	Diagnostic			1.10	3,987				Diagnostic
B.2.24.3.2.1	P-10	Loop + Port Combinations/>=10 circuits/Dispatch/TN (days)	Diagnostic			1.84	1				Diagnostic
B.2.24.3.2.2	P-10	Loop + Port Combinations/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.4.1.1	P-10	Combo Other/<10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.4.1.2	P-10	Combo Other/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.4.2.1	P-10	Combo Other/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.4.2.2	P-10	Combo Other/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.5.1.1	P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/TN (days)	Diagnostic			4.89	39				Diagnostic
B.2.24.5.1.2	P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.5.2.1	P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.5.2.2	P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.6.1.1	P-10	UNE ISDN/<10 circuits/Dispatch/TN (days)	Diagnostic			11.42	7				Diagnostic
B.2.24.6.1.2	P-10	UNE ISDN/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.6.2.1	P-10	UNE ISDN/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.6.2.2	P-10	UNE ISDN/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.7.1.1	P-10	Line Sharing/<10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.7.1.2	P-10	Line Sharing/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.7.2.1	P-10	Line Sharing/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.7.2.2	P-10	Line Sharing/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.8.1.1	P-10	2W Analog Loop Design/<10 circuits/Dispatch/TN (days)	Diagnostic			5.89	32				Diagnostic
B.2.24.8.1.2	P-10	2W Analog Loop Design/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.8.2.1	P-10	2W Analog Loop Design/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.8.2.2	P-10	2W Analog Loop Design/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.9.1.1	P-10	2W Analog Loop Non-Design/<10 circuits/Dispatch/TN (days)	Diagnostic			3.35	11				Diagnostic
B.2.24.9.1.2	P-10	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.9.2.1	P-10	2W Analog Loop Non-Design/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.9.2.2	P-10	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.10.1.1	P-10	2W Analog Loop w/INP Design/<10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.10.1.2	P-10	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.10.2.1	P-10	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.10.2.2	P-10	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.11.1.1	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.11.1.2	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.11.2.1	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.11.2.2	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.12.1.1	P-14	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/TN (days)	Diagnostic			5.05	1				Diagnostic
B.2.24.12.1.2	P-14	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.12.2.1	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.12.2.2	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.13.1.1	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.13.1.2	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.13.2.1	P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.13.2.2	P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.14.1.1	P-10	Other Design/<10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.14.1.2	P-10	Other Design/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.14.2.1	P-10	Other Design/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.14.2.2	P-10	Other Design/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.15.1.1	P-10	Other Non-Design/<10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic

BellSouth Monthly Performance Summary

Tennessee, August 2002 - M ay 2002

			July								Jul			
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.2.22.17.2.1	P-5	LNP (Standalone)/>=10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.17.2.2	P-5	LNP (Standalone)/>=10 circuits/Non-Dispatch/TN (hours)			3.99	4				Diagnostic			0.86	6
B.2.22.18.1.1	P-5	Digital Loop < DS1/<10 circuits/Dispatch/TN (hours)			9.77	60				Diagnostic			24.62	49
B.2.22.18.1.2	P-5	Digital Loop < DS1/<10 circuits/Non-Dispatch/TN (hours)								Diagnostic				
B.2.22.18.2.1	P-5	Digital Loop < DS1/>=10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.18.2.2	P-5	Digital Loop < DS1/>=10 circuits/Non-Dispatch/TN (hours)								Diagnostic				
B.2.22.19.1.1	P-5	Digital Loop >= DS1/<10 circuits/Dispatch/TN (hours)			48.27	39				Diagnostic			48.03	31
B.2.22.19.1.2	P-5	Digital Loop >= DS1/<10 circuits/Non-Dispatch/TN (hours)								Diagnostic				
B.2.22.19.2.1	P-5	Digital Loop >= DS1/>=10 circuits/Dispatch/TN (hours)								Diagnostic				
B.2.22.19.2.2	P-5	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/TN (hours)								Diagnostic				
		Total Service Order Cycle Time - Mechanized												
B.2.24.1.1.1	P-10	Switch Ports/<10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.1.1.2	P-10	Switch Ports/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.1.2.1	P-10	Switch Ports/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.1.2.2	P-10	Switch Ports/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.2.1.1	P-10	Local Interoffice Transport/<10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.2.1.2	P-10	Local Interoffice Transport/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.2.2.1	P-10	Local Interoffice Transport/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.2.2.2	P-10	Local Interoffice Transport/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.3.1.1	P-10	Loop + Port Combinations/<10 circuits/Dispatch/TN (days)			3.58	76				Diagnostic			2.86	105
B.2.24.3.1.2	P-10	Loop + Port Combinations/<10 circuits/Non-Dispatch/TN (days)			0.82	3,371				Diagnostic			0.79	2,181
B.2.24.3.2.1	P-10	Loop + Port Combinations/>=10 circuits/Dispatch/TN (days)			0.33	1				Diagnostic			5.56	2
B.2.24.3.2.2	P-10	Loop + Port Combinations/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.4.1.1	P-10	Combo Other/<10 circuits/Dispatch/TN (days)								Diagnostic			13.88	1
B.2.24.4.1.2	P-10	Combo Other/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.4.2.1	P-10	Combo Other/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.4.2.2	P-10	Combo Other/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.5.1.1	P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/TN (days)			4.86	15				Diagnostic			4.55	50
B.2.24.5.1.2	P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.5.2.1	P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.5.2.2	P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.6.1.1	P-10	UNE ISDN/<10 circuits/Dispatch/TN (days)			10.18	13				Diagnostic			10.83	16
B.2.24.6.1.2	P-10	UNE ISDN/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.6.2.1	P-10	UNE ISDN/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.6.2.2	P-10	UNE ISDN/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.7.1.1	P-10	Line Sharing/<10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.7.1.2	P-10	Line Sharing/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.7.2.1	P-10	Line Sharing/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.7.2.2	P-10	Line Sharing/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.8.1.1	P-10	2W Analog Loop Design/<10 circuits/Dispatch/TN (days)			5.36	33				Diagnostic			5.62	59
B.2.24.8.1.2	P-10	2W Analog Loop Design/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.8.2.1	P-10	2W Analog Loop Design/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.8.2.2	P-10	2W Analog Loop Design/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.9.1.1	P-10	2W Analog Loop Non-Design/<10 circuits/Dispatch/TN (days)			3.25	8				Diagnostic			4.76	5
B.2.24.9.1.2	P-10	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.9.2.1	P-10	2W Analog Loop Non-Design/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.9.2.2	P-10	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.10.1.1	P-10	2W Analog Loop w/INP Design/<10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.10.1.2	P-10	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.10.2.1	P-10	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.10.2.2	P-10	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.11.1.1	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.11.1.2	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.11.2.1	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.11.2.2	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.12.1.1	P-14	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/TN (days)			5.79	1				Diagnostic			6.12	2
B.2.24.12.1.2	P-14	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.12.2.1	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.12.2.2	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.13.1.1	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.13.1.2	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.13.2.1	P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.13.2.2	P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.14.1.1	P-10	Other Design/<10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.14.1.2	P-10	Other Design/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.14.2.1	P-10	Other Design/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.14.2.2	P-10	Other Design/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.15.1.1	P-10	Other Non-Design/<10 circuits/Dispatch/TN (days)								Diagnostic				

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Tennessee, August 2002 - May 2002

			ne						May					
			Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.22.17.2.1	P-5	LNP (Standalone)/>=10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.17.2.2	P-5	LNP (Standalone)/>=10 circuits/Non-Dispatch/TN (hours)				Diagnostic			0.74	9				Diagnostic
B.2.22.18.1.1	P-5	Digital Loop < DS1/<10 circuits/Dispatch/TN (hours)				Diagnostic			14.54	98				Diagnostic
B.2.22.18.1.2	P-5	Digital Loop < DS1/<10 circuits/Non-Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.18.2.1	P-5	Digital Loop < DS1/>=10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.18.2.2	P-5	Digital Loop < DS1/>=10 circuits/Non-Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.19.1.1	P-5	Digital Loop >= DS1/<10 circuits/Dispatch/TN (hours)				Diagnostic			49.90	44				Diagnostic
B.2.22.19.1.2	P-5	Digital Loop >= DS1/<10 circuits/Non-Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.19.2.1	P-5	Digital Loop >= DS1/>=10 circuits/Dispatch/TN (hours)				Diagnostic								Diagnostic
B.2.22.19.2.2	P-5	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/TN (hours)				Diagnostic								Diagnostic
		Total Service Order Cycle Time - Mechanized												
B.2.24.1.1.1	P-10	Switch Ports/<10 circuits/Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.1.1.2	P-10	Switch Ports/<10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.1.2.1	P-10	Switch Ports/>=10 circuits/Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.1.2.2	P-10	Switch Ports/>=10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.2.1.1	P-10	Local Interoffice Transport/<10 circuits/Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.2.1.2	P-10	Local Interoffice Transport/<10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.2.2.1	P-10	Local Interoffice Transport/>=10 circuits/Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.2.2.2	P-10	Local Interoffice Transport/>=10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.3.1.1	P-10	Loop + Port Combinations/<10 circuits/Dispatch/TN (days)				Diagnostic			2.80	83				Diagnostic
B.2.24.3.1.2	P-10	Loop + Port Combinations/<10 circuits/Non-Dispatch/TN (days)				Diagnostic			0.70	1,980				Diagnostic
B.2.24.3.2.1	P-10	Loop + Port Combinations/>=10 circuits/Dispatch/TN (days)				Diagnostic			4.28	3				Diagnostic
B.2.24.3.2.2	P-10	Loop + Port Combinations/>=10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.4.1.1	P-10	Combo Other/<10 circuits/Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.4.1.2	P-10	Combo Other/<10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.4.2.1	P-10	Combo Other/>=10 circuits/Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.4.2.2	P-10	Combo Other/>=10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.5.1.1	P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.5.1.2	P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.5.2.1	P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.5.2.2	P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.6.1.1	P-10	UNE ISDN/<10 circuits/Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.6.1.2	P-10	UNE ISDN/<10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.6.2.1	P-10	UNE ISDN/>=10 circuits/Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.6.2.2	P-10	UNE ISDN/>=10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.7.1.1	P-10	Line Sharing/<10 circuits/Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.7.1.2	P-10	Line Sharing/<10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.7.2.1	P-10	Line Sharing/>=10 circuits/Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.7.2.2	P-10	Line Sharing/>=10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.8.1.1	P-10	2W Analog Loop Design/<10 circuits/Dispatch/TN (days)				Diagnostic			4.94	53				Diagnostic
B.2.24.8.1.2	P-10	2W Analog Loop Design/<10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.8.2.1	P-10	2W Analog Loop Design/>=10 circuits/Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.8.2.2	P-10	2W Analog Loop Design/>=10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.9.1.1	P-10	2W Analog Loop Non-Design/<10 circuits/Dispatch/TN (days)				Diagnostic			4.47	2				Diagnostic
B.2.24.9.1.2	P-10	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.9.2.1	P-10	2W Analog Loop Non-Design/>=10 circuits/Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.9.2.2	P-10	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.10.1.1	P-10	2W Analog Loop w/INP Design/<10 circuits/Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.10.1.2	P-10	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.10.2.1	P-10	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.10.2.2	P-10	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.11.1.1	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.11.1.2	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.11.2.1	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.11.2.2	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.12.1.1	P-14	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.12.1.2	P-14	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.12.2.1	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.12.2.2	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.13.1.1	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.13.1.2	P-14	2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.13.2.1	P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.13.2.2	P-14	2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.14.1.1	P-10	Other Design/<10 circuits/Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.14.1.2	P-10	Other Design/<10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.14.2.1	P-10	Other Design/>=10 circuits/Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.14.2.2	P-10	Other Design/>=10 circuits/Non-Dispatch/TN (days)				Diagnostic								Diagnostic
B.2.24.15.1.1	P-10	Other Non-Design/<10 circuits/Dispatch/TN (days)				Diagnostic								Diagnostic

BellSouth Monthly Performance Summary

Tennessee, August 2002 - May 2002

			Benchmark/ Analog	August BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
B.2.24.15.1.2	P-10	Other Non-Design/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.15.2.1	P-10	Other Non-Design/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.15.2.2	P-10	Other Non-Design/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.16.1.1	P-10	INP (Standalone)/<10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.16.1.2	P-10	INP (Standalone)/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.16.2.1	P-10	INP (Standalone)/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.16.2.2	P-10	INP (Standalone)/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.17.1.1	P-14	LNP (Standalone)/<10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.17.1.2	P-14	LNP (Standalone)/<10 circuits/Non-Dispatch/TN (days)	Diagnostic			0.64	123				Diagnostic
B.2.24.17.2.1	P-14	LNP (Standalone)/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.17.2.2	P-14	LNP (Standalone)/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.18.1.1	P-10	Digital Loop < DS1/<10 circuits/Dispatch/TN (days)	Diagnostic			5.86	43				Diagnostic
B.2.24.18.1.2	P-10	Digital Loop < DS1/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.18.2.1	P-10	Digital Loop < DS1/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.18.2.2	P-10	Digital Loop < DS1/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.19.1.1	P-10	Digital Loop >= DS1/<10 circuits/Dispatch/TN (days)	Diagnostic			7.43	79				Diagnostic
B.2.24.19.1.2	P-10	Digital Loop >= DS1/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.19.2.1	P-10	Digital Loop >= DS1/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.24.19.2.2	P-10	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
		Total Service Order Cycle Time - Partially Mechanized									
B.2.25.1.1.1	P-10	Switch Ports/<10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.1.1.2	P-10	Switch Ports/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.1.2.1	P-10	Switch Ports/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.1.2.2	P-10	Switch Ports/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.2.1.1	P-10	Local Interoffice Transport/<10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.2.1.2	P-10	Local Interoffice Transport/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.2.2.1	P-10	Local Interoffice Transport/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.2.2.2	P-10	Local Interoffice Transport/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.3.1.1	P-10	Loop + Port Combinations/<10 circuits/Dispatch/TN (days)	Diagnostic			2.96	57				Diagnostic
B.2.25.3.1.2	P-10	Loop + Port Combinations/<10 circuits/Non-Dispatch/TN (days)	Diagnostic			1.65	1,222				Diagnostic
B.2.25.3.2.1	P-10	Loop + Port Combinations/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.3.2.2	P-10	Loop + Port Combinations/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.4.1.1	P-10	Combo Other/<10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.4.1.2	P-10	Combo Other/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.4.2.1	P-10	Combo Other/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.4.2.2	P-10	Combo Other/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.5.1.1	P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/TN (days)	Diagnostic			5.29	3				Diagnostic
B.2.25.5.1.2	P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.5.2.1	P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.5.2.2	P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.6.1.1	P-10	UNE ISDN/<10 circuits/Dispatch/TN (days)	Diagnostic			10.10	2				Diagnostic
B.2.25.6.1.2	P-10	UNE ISDN/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.6.2.1	P-10	UNE ISDN/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.6.2.2	P-10	UNE ISDN/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.7.1.1	P-10	Line Sharing/<10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.7.1.2	P-10	Line Sharing/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.7.2.1	P-10	Line Sharing/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.7.2.2	P-10	Line Sharing/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.8.1.1	P-10	2W Analog Loop Design/<10 circuits/Dispatch/TN (days)	Diagnostic			6.17	16				Diagnostic
B.2.25.8.1.2	P-10	2W Analog Loop Design/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.8.2.1	P-10	2W Analog Loop Design/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.8.2.2	P-10	2W Analog Loop Design/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.9.1.1	P-10	2W Analog Loop Non-Design/<10 circuits/Dispatch/TN (days)	Diagnostic			4.74	6				Diagnostic
B.2.25.9.1.2	P-10	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/TN (days)	Diagnostic			3.99	1				Diagnostic
B.2.25.9.2.1	P-10	2W Analog Loop Non-Design/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.9.2.2	P-10	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.10.1.1	P-10	2W Analog Loop w/INP Design/<10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.10.1.2	P-10	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.10.2.1	P-10	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.10.2.2	P-10	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.11.1.1	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.11.1.2	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.11.2.1	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.11.2.2	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.12.1.1	P-14	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/TN (days)	Diagnostic			5.66	21				Diagnostic
B.2.25.12.1.2	P-14	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.12.2.1	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/TN (days)	Diagnostic								Diagnostic
B.2.25.12.2.2	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/TN (days)	Diagnostic								Diagnostic

BellSouth Monthly Performance Summary
Tennessee, August 2002 - May 2002

			July								Jul			
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.2.24.15.1.2	P-10	Other Non-Design/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.15.2.1	P-10	Other Non-Design/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.15.2.2	P-10	Other Non-Design/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.16.1.1	P-10	INP (Standalone)/<10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.16.1.2	P-10	INP (Standalone)/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.16.2.1	P-10	INP (Standalone)/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.16.2.2	P-10	INP (Standalone)/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.17.1.1	P-14	LNP (Standalone)/<10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.17.1.2	P-14	LNP (Standalone)/<10 circuits/Non-Dispatch/TN (days)			0.63	139				Diagnostic			0.63	86
B.2.24.17.2.1	P-14	LNP (Standalone)/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.17.2.2	P-14	LNP (Standalone)/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.18.1.1	P-10	Digital Loop < DS1/<10 circuits/Dispatch/TN (days)			7.46	26				Diagnostic			6.10	62
B.2.24.18.1.2	P-10	Digital Loop < DS1/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.18.2.1	P-10	Digital Loop < DS1/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.18.2.2	P-10	Digital Loop < DS1/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.19.1.1	P-10	Digital Loop >= DS1/<10 circuits/Dispatch/TN (days)			6.73	69				Diagnostic			6.86	121
B.2.24.19.1.2	P-10	Digital Loop >= DS1/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.24.19.2.1	P-10	Digital Loop >= DS1/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.24.19.2.2	P-10	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
	Total Service Order Cycle Time - Partially Mechanized													
B.2.25.1.1.1	P-10	Switch Ports/<10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.25.1.1.2	P-10	Switch Ports/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.25.1.2.1	P-10	Switch Ports/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.25.1.2.2	P-10	Switch Ports/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.25.2.1.1	P-10	Local Interoffice Transport/<10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.25.2.1.2	P-10	Local Interoffice Transport/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.25.2.2.1	P-10	Local Interoffice Transport/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.25.2.2.2	P-10	Local Interoffice Transport/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.25.3.1.1	P-10	Loop + Port Combinations/<10 circuits/Dispatch/TN (days)			2.84	72				Diagnostic			3.16	44
B.2.25.3.1.2	P-10	Loop + Port Combinations/<10 circuits/Non-Dispatch/TN (days)			1.18	835				Diagnostic			1.25	760
B.2.25.3.2.1	P-10	Loop + Port Combinations/>=10 circuits/Dispatch/TN (days)								Diagnostic			3.75	1
B.2.25.3.2.2	P-10	Loop + Port Combinations/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.25.4.1.1	P-10	Combo Other/<10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.25.4.1.2	P-10	Combo Other/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.25.4.2.1	P-10	Combo Other/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.25.4.2.2	P-10	Combo Other/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.25.5.1.1	P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/TN (days)			4.45	4				Diagnostic				
B.2.25.5.1.2	P-10	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.25.5.2.1	P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.25.5.2.2	P-10	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.25.6.1.1	P-10	UNE ISDN/<10 circuits/Dispatch/TN (days)			12.75	1				Diagnostic			13.91	1
B.2.25.6.1.2	P-10	UNE ISDN/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.25.6.2.1	P-10	UNE ISDN/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.25.6.2.2	P-10	UNE ISDN/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.25.7.1.1	P-10	Line Sharing/<10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.25.7.1.2	P-10	Line Sharing/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.25.7.2.1	P-10	Line Sharing/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.25.7.2.2	P-10	Line Sharing/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.25.8.1.1	P-10	2W Analog Loop Design/<10 circuits/Dispatch/TN (days)			6.73	9				Diagnostic			5.77	9
B.2.25.8.1.2	P-10	2W Analog Loop Design/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.25.8.2.1	P-10	2W Analog Loop Design/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.25.8.2.2	P-10	2W Analog Loop Design/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.25.9.1.1	P-10	2W Analog Loop Non-Design/<10 circuits/Dispatch/TN (days)			4.92	4				Diagnostic			5.01	4
B.2.25.9.1.2	P-10	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.25.9.2.1	P-10	2W Analog Loop Non-Design/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.25.9.2.2	P-10	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.25.10.1.1	P-10	2W Analog Loop w/INP Design/<10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.25.10.1.2	P-10	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.25.10.2.1	P-10	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.25.10.2.2	P-10	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.25.11.1.1	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.25.11.1.2	P-10	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.25.11.2.1	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.25.11.2.2	P-10	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.25.12.1.1	P-14	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/TN (days)			5.67	23				Diagnostic			5.52	22
B.2.25.12.1.2	P-14	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/TN (days)								Diagnostic				
B.2.25.12.2.1	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/TN (days)								Diagnostic				
B.2.25.12.2.2	P-14	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/TN (days)								Diagnostic				