BellSouth Monthly Performance Summary

Florida Permanent, September 2002 - May 2002

			Standard Error	Zscore	Equity
A.3.3.2.2	M&R-3	Business/Non-Dispatch/FL (hours)	0.61062	3.4459	YES
A.3.3.3.1	M&R-3	Design (Specials)/Dispatch/FL (hours)	5.40378	0.4103	YES
A.3.3.3.2	M&R-3	Design (Specials)/Non-Dispatch/FL (hours)	2.16712	0.2000	YES
A.3.3.4.1	M&R-3	PBX/Dispatch/FL (hours)	4.76950	1.5823	YES
A.3.3.4.2	M&R-3	PBX/Non-Dispatch/FL (hours)	0.96138	1.4635	YES
A.3.3.5.1	M&R-3	Centrex/Dispatch/FL (hours)	8.20661	0.1170	YES
A.3.3.5.2	M&R-3	Centrex/Non-Dispatch/FL (hours)	8.75314	0.0270	YES
A.3.3.6.1	M&R-3	ISDN/Dispatch/FL (hours)	5.61100	0.6175	YES
A.3.3.6.2	M&R-3	ISDN/Non-Dispatch/FL (hours)	1.46580	0.3290	YES
		% Repeat Troubles within 30 Days			
A.3.4.1.1	M&R-4	Residence/Dispatch/FL (%)	0.00724	6.4466	YES
A.3.4.1.2	M&R-4	Residence/Non-Dispatch/FL (%)	0.01034	2.0541	YES
A.3.4.2.1	M&R-4	Business/Dispatch/FL (%)	0.01478	2.6459	YES
A.3.4.2.2	M&R-4	Business/Non-Dispatch/FL (%)	0.01805	-0.3555	YES
A.3.4.3.1	M&R-4	Design (Specials)/Dispatch/FL (%)	0.06800	0.1839	YES
A.3.4.3.2	M&R-4	Design (Specials)/Non-Dispatch/FL (%)	0.06319	0.4012	YES
A.3.4.4.1	M&R-4	PBX/Dispatch/FL (%)	0.10063	1.2208	YES
A.3.4.4.2	M&R-4	PBX/Non-Dispatch/FL (%)	0.09561	-0.3493	YES
A.3.4.5.1	M&R-4	Centrex/Dispatch/FL (%)	0.10241	1.1512	YES
A.3.4.5.2	M&R-4	Centrex/Non-Dispatch/FL (%)	0.17317	0.8017	YES
A.3.4.6.1	M&R-4	ISDN/Dispatch/FL (%)	0.18773	0.6329	YES
A.3.4.6.2	M&R-4	ISDN/Non-Dispatch/FL (%)	0.10149	-1.5690	YES
		Out of Service > 24 hours			
A.3.5.1.1	M&R-5	Residence/Dispatch/FL (%)	0.00737	3.8936	YES
A.3.5.1.2	M&R-5	Residence/Non-Dispatch/FL (%)	0.00852	1.4235	YES
A.3.5.2.1	M&R-5	Business/Dispatch/FL (%)	0.01371	0.8876	YES
A.3.5.2.2	M&R-5	Business/Non-Dispatch/FL (%)	0.01679	3.3674	YES
A.3.5.3.1	M&R-5	Design (Specials)/Dispatch/FL (%)	0.02249	0.7678	YES
A.3.5.3.2	M&R-5	Design (Specials)/Non-Dispatch/FL (%)	0.00998	0.3709	YES
A.3.5.4.1	M&R-5	PBX/Dispatch/FL (%)	0.11151	0.5920	YES
A.3.5.4.2	M&R-5	PBX/Non-Dispatch/FL (%)	0.03011	9.3774	YES
A.3.5.5.1	M&R-5	Centrex/Dispatch/FL (%)	0.21010	0.7441	YES
A.3.5.5.2	M&R-5	Centrex/Non-Dispatch/FL (%)	0.17818	0.1838	YES
A.3.5.6.1	M&R-5	ISDN/Dispatch/FL (%)	0.13352	0.4202	YES
A.3.5.6.2	M&R-5	ISDN/Non-Dispatch/FL (%)	0.03024	0.2391	YES
		Resale - Billing			
		Invoice Accuracy (Measured in dollars)			
A.4.1	B-1	FL (%)	0.00005	-491.9273	YES
		Invoice Accuracy (Measured in # of adjustments)			
A.4.2	B-1	FL (%)	0.01555	5.9407	NO
		Mean Time to Deliver Invoices - CRIS			
A.4.3	B-2	FL (business days)			YES
		% Billing Errors Corrected in X Days			
A.4.4	B-10	FL (days)			Diagnostic

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			Benchmark/ Analog	September BST Measure	September BST Volume	September CLEC Measure	September CLEC Volume	September Standard Deviation	September Standard Error	September Zscore	September Equity	August BST Measure	August BST Volume	August CLEC Measure	August CLEC Volume	August Standard Deviation	August Standard Error	August Zscore	August Equity
	Unbundled Network Elements - Ordering																		
	% Rejected Service Requests - Mechanized																		
B.1.1.1	O-7	Switch Ports/FL (%)	Diagnostic								Diagnostic								Diagnostic
B.1.1.2	O-7	Local Interoffice Transport/FL (%)	Diagnostic								Diagnostic								Diagnostic
B.1.1.3	O-7	Loop + Port Combinations/FL (%)	Diagnostic			16.80%	54,828				Diagnostic			16.23%	81,754				Diagnostic
B.1.1.4	O-7	Combo (other)/FL (%)	Diagnostic								Diagnostic								Diagnostic
B.1.1.5	O-7	xDSL (ADSL, HDSL and UCL)/FL (%)	Diagnostic			23.22%	926				Diagnostic			25.42%	708				Diagnostic
B.1.1.6	O-7	ISDN Loop (UDN, UDC)/FL (%)	Diagnostic			5.87%	392				Diagnostic			5.94%	219				Diagnostic
B.1.1.7	O-7	Line Sharing/FL (%)	Diagnostic			26.23%	507				Diagnostic			30.23%	258				Diagnostic
B.1.1.8	O-7	2W Analog Loop Design/FL (%)	Diagnostic			12.77%	744				Diagnostic			19.87%	1,037				Diagnostic
B.1.1.9	O-7	2W Analog Loop Non-Design/FL (%)	Diagnostic			8.85%	972				Diagnostic			14.19%	1,008				Diagnostic
B.1.1.10	O-7	2W Analog Loop w/INP Design/FL (%)	Diagnostic								Diagnostic								Diagnostic
B.1.1.11	O-7	2W Analog Loop w/INP Non-Design/FL (%)	Diagnostic								Diagnostic								Diagnostic
B.1.1.12	O-7	2W Analog Loop w/LNP Design/FL (%)	Diagnostic			26.32%	19				Diagnostic			62.86%	35				Diagnostic
B.1.1.13	O-7	2W Analog Loop w/LNP Non-Design/FL (%)	Diagnostic			81.58%	114				Diagnostic			71.94%	139				Diagnostic
B.1.1.14	O-7	Other Design/FL (%)	Diagnostic			100.00%	1				Diagnostic			85.71%	7				Diagnostic
B.1.1.15	O-7	Other Non-Design/FL (%)	Diagnostic			30.98%	12,890				Diagnostic			29.56%	15,960				Diagnostic
B.1.1.16	O-7	INP Standalone/FL (%)	Diagnostic								Diagnostic			100.00%	1				Diagnostic
B.1.1.17	O-7	LNP Standalone/FL (%)	Diagnostic			7.01%	3,554				Diagnostic			9.84%	3,818				Diagnostic
B.1.1.18	O-7	Digital Loop < DS1/FL (%)	Diagnostic			17.22%	1,057				Diagnostic			21.14%	884				Diagnostic
B.1.1.19	O-7	Digital Loop >= DS1/FL (%)	Diagnostic			25.42%	299				Diagnostic			25.71%	350				Diagnostic
B.1.1.20	O-7	EELs/FL (%)	Diagnostic			42.11%	19				Diagnostic			38.46%	13				Diagnostic
B.1.1.21	O-7	Line Splitting/FL (%)	Diagnostic								Diagnostic								Diagnostic
	% Rejected Service Requests - Partially Mechanized																		
B.1.2.1	O-7	Switch Ports/FL (%)	Diagnostic								Diagnostic								Diagnostic
B.1.2.2	O-7	Local Interoffice Transport/FL (%)	Diagnostic								Diagnostic								Diagnostic
B.1.2.3	O-7	Loop + Port Combinations/FL (%)	Diagnostic			43.30%	11,473				Diagnostic			31.03%	15,887				Diagnostic
B.1.2.4	O-7	Combo (other)/FL (%)	Diagnostic								Diagnostic								Diagnostic
B.1.2.5	O-7	xDSL (ADSL, HDSL and UCL)/FL (%)	Diagnostic			20.45%	88				Diagnostic			13.33%	30				Diagnostic
B.1.2.6	O-7	ISDN Loop (UDN, UDC)/FL (%)	Diagnostic			16.67%	18				Diagnostic			0.00%	23				Diagnostic
B.1.2.7	O-7	Line Sharing/FL (%)	Diagnostic			45.90%	122				Diagnostic			44.00%	75				Diagnostic
B.1.2.8	O-7	2W Analog Loop Design/FL (%)	Diagnostic			23.98%	288				Diagnostic			24.70%	238				Diagnostic
B.1.2.9	O-7	2W Analog Loop Non-Design/FL (%)	Diagnostic			11.16%	1,884				Diagnostic			11.10%	1,893				Diagnostic
B.1.2.10	O-7	2W Analog Loop w/INP Design/FL (%)	Diagnostic								Diagnostic								Diagnostic
B.1.2.11	O-7	2W Analog Loop w/INP Non-Design/FL (%)	Diagnostic								Diagnostic								Diagnostic
B.1.2.12	O-7	2W Analog Loop w/LNP Design/FL (%)	Diagnostic			50.70%	189				Diagnostic			31.03%	203				Diagnostic
B.1.2.13	O-7	2W Analog Loop w/LNP Non-Design/FL (%)	Diagnostic			24.50%	943				Diagnostic			21.44%	1,138				Diagnostic
B.1.2.14	O-7	Other Design/FL (%)	Diagnostic								Diagnostic			100.00%	1				Diagnostic
B.1.2.15	O-7	Other Non-Design/FL (%)	Diagnostic			35.41%	5,527				Diagnostic			37.51%	7,917				Diagnostic
B.1.2.16	O-7	INP Standalone/FL (%)	Diagnostic								Diagnostic								Diagnostic
B.1.2.17	O-7	LNP Standalone/FL (%)	Diagnostic			36.90%	1,309				Diagnostic			34.77%	1,504				Diagnostic
B.1.2.18	O-7	Digital Loop < DS1/FL (%)	Diagnostic			18.00%	50				Diagnostic			4.00%	50				Diagnostic
B.1.2.19	O-7	Digital Loop >= DS1/FL (%)	Diagnostic			24.46%	326				Diagnostic			25.94%	347				Diagnostic
B.1.2.20	O-7	EELs/FL (%)	Diagnostic			21.49%	121				Diagnostic			18.95%	58				Diagnostic
B.1.2.21	O-7	Line Splitting/FL (%)	Diagnostic								Diagnostic								Diagnostic
	% Rejected Service Requests - Non-Mechanized																		
B.1.3.1	O-7	Switch Ports/FL (%)	Diagnostic								Diagnostic								Diagnostic
B.1.3.2	O-7	Local Interoffice Transport/FL (%)	Diagnostic			53.85%	26				Diagnostic			61.54%	39				Diagnostic
B.1.3.3	O-7	Loop + Port Combinations/FL (%)	Diagnostic			44.73%	966				Diagnostic			38.75%	3,004				Diagnostic
B.1.3.4	O-7	Combo (other)/FL (%)	Diagnostic			60.00%	5				Diagnostic			55.00%	20				Diagnostic
B.1.3.5	O-7	xDSL (ADSL, HDSL and UCL)/FL (%)	Diagnostic			32.29%	96				Diagnostic			25.00%	88				Diagnostic
B.1.3.6	O-7	ISDN Loop (UDN, UDC)/FL (%)	Diagnostic			16.49%	97				Diagnostic			21.28%	296				Diagnostic
B.1.3.7	O-7	Line Sharing/FL (%)	Diagnostic			43.14%	51				Diagnostic			50.00%	28				Diagnostic
B.1.3.8	O-7	2W Analog Loop Design/FL (%)	Diagnostic			44.00%	175				Diagnostic			42.53%	174				Diagnostic
B.1.3.9	O-7	2W Analog Loop Non-Design/FL (%)	Diagnostic			36.46%	277				Diagnostic			37.50%	472				Diagnostic
B.1.3.10	O-7	2W Analog Loop w/INP Design/FL (%)	Diagnostic								Diagnostic			0.00%	5				Diagnostic
B.1.3.11	O-7	2W Analog Loop w/INP Non-Design/FL (%)	Diagnostic			33.33%	3				Diagnostic			0.00%	4				Diagnostic
B.1.3.12	O-7	2W Analog Loop w/LNP Design/FL (%)	Diagnostic			64.52%	31				Diagnostic			64.10%	39				Diagnostic
B.1.3.13	O-7	2W Analog Loop w/LNP Non-Design/FL (%)	Diagnostic			43.75%	32				Diagnostic			46.94%	49				Diagnostic
B.1.3.14	O-7	Other Design/FL (%)	Diagnostic								Diagnostic								Diagnostic
B.1.3.15	O-7	Other Non-Design/FL (%)	Diagnostic			33.53%	866				Diagnostic			30.34%	1,381				Diagnostic
B.1.3.16	O-7	INP Standalone/FL (%)	Diagnostic			30.77%	26				Diagnostic			36.36%	44				Diagnostic
B.1.3.17	O-7	LNP Standalone/FL (%)	Diagnostic			20.04%	489				Diagnostic			33.48%	914				Diagnostic
B.1.3.18	O-7	Digital Loop < DS1/FL (%)	Diagnostic			21.91%	178				Diagnostic			21.45%	373				Diagnostic
B.1.3.19	O-7	Digital Loop >= DS1/FL (%)	Diagnostic			43.46%	227				Diagnostic			45.52%	402				Diagnostic
B.1.3.20	O-7	EELs/FL (%)	Diagnostic			51.40%	107				Diagnostic			43.86%	171				Diagnostic
B.1.3.21	O-7	Line Splitting/FL (%)	Diagnostic								Diagnostic								Diagnostic
	Reject Interval - Mechanized																		
B.1.4.1	O-8	Switch Ports/FL (%)	>= 97% w in 1 hr																
B.1.4.2	O-8	Local Interoffice Transport/FL (%)	>= 97% w in 1 hr			94.76%	9,320				NO			96.39%	13,335				NO
B.1.4.3	O-8	Loop + Port Combinations/FL (%)	>= 97% w in 1 hr																
B.1.4.4	O-8	Combo (other)/FL (%)	>= 97% w in 1 hr			97.67%	215				YES			97.78%	180				YES
B.1.4.5	O-8	xDSL (ADSL, HDSL and UCL)/FL (%)	>= 97% w in 1 hr			95.65%	23				NO			76.92%	13				NO
B.1.4.6	O-8	ISDN Loop (UDN, UDC)/FL (%)	>= 97% w in 1 hr			75.94%	133				NO			56.96%	79				NO
B.1.4.7	O-8	Line Sharing/FL (%)	>= 97% w in 1 hr			81.25%	96				NO			74.88%	207				NO
B.1.4.8	O-8	2W Analog Loop Design/FL (%)	>= 97% w in 1 hr			61.80%	89				NO			64.14%	145				NO
B.1.4.9	O-8	2W Analog Loop Non-Design/FL (%)	>= 97% w in 1 hr																
B.1.4.10	O-8	2W Analog Loop w/INP Design/FL (%)	>= 97% w in 1 hr																
B.1.4.11	O-8	2W Analog Loop w/INP Non-Design/FL (%)	>= 97% w in 1 hr			100.00%	5				YES			90.91%	22				NO
B.1.4.12	O-8	2W Analog Loop w/LNP Design/FL (%)	>= 97% w in 1 hr			98.92%	93				YES			98.00%	100				YES
B.1.4.13	O-8	2W Analog Loop w/LNP Non-Design/FL (%)	>= 97% w in 1 hr			100.00%	1				YES			66.67%	6				NO
B.1.4.14	O-8	Other Design/FL (%)	>= 97% w in 1 hr			50.20%	4,177				NO			56.80%	5,049				NO
B.1.4.15	O-8	Other Non-Design/FL (%)	>= 97% w in 1 hr											0.00%	1				NO
B.1.4.16	O-8	INP Standalone/FL (%)	>= 97% w in 1 hr			98.80%	249				YES			96.32%	357				YES
B.1.4.17	O-8	LNP Standalone/FL (%)	>= 97% w in 1 hr			96.70%	182				NO			97.35%	189				YES
B.1.4.18	O-8	Digital Loop < DS1/FL (%)	>= 97% w in 1 hr			78.21%	78				NO			53.19%	94				NO
B.1.4.19	O-8	Digital Loop >= DS1/FL (%)	>= 97% w in 1 hr			100.00%	8				YES			80.00%	5				NO
B.1.4.20	O-8	EELs/FL (%)	>= 97% w in 1 hr																
B.1.4.21	O-8	Line Splitting/FL (%)	>= 97% w in 1 hr																
	Reject Interval - Partially Mechanized - 10 hours																		
B.1.7.1	O-8	Switch Ports/FL (%)	>= 95% w in 10 hrs																
B.1.7.2	O-8	Local Interoffice Transport/FL (%)	>= 95% w in 10 hrs			97.63%	5,016				YES			96.84%	5,033				YES
B.1.7.3	O-8	Loop + Port Combinations/FL (%)	>= 95% w in 10 hrs																
B.1.7.4	O-8	Combo (other)/FL (%)	>= 95% w in 10 hrs			100.00%	19				YES			50.00%	4				NO
B.1.7.5	O-8	xDSL (ADSL, HDSL and UCL)/FL (%)	>= 95% w in 10 hrs			66.67%	3				NO								
B.1.7.6	O-8	ISDN Loop (UDN, UDC)/FL (%)	>= 95% w in 10 hrs			92.86%	56				NO			88.89%	36				NO
B.1.7.7	O-8	Line Sharing/FL (%)	>= 95% w in 10 hrs			94.20%	69				NO			86.89%	63				NO
B.1.7.8	O-8	2W Analog Loop Design/FL (%)	>= 95% w in 10 hrs			79.06%	196				NO			78.81%	201				NO
B.1.7.9	O-8	2W Analog Loop Non-Design/FL (%)	>= 95% w in 10 hrs																
B.1.7.10	O-8	2W Analog Loop w/INP Design/FL (%)	>= 95% w in 10 hrs																
B.1.7.11	O-8	2W Analog Loop w/INP Non-Design/FL (%)	>= 95% w in 10 hrs																

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			July							June							May			
			BST	BST	CLEC	CLEC	Standard	Standard			BST	BST	CLEC	CLEC	Standard	Standard			BST	BST	CLEC	CLEC
			Measure	Volume	Measure	Volume	Deviation	Error	Zscore	Equity	Measure	Volume	Measure	Volume	Deviation	Error	Zscore	Equity	Measure	Volume	Measure	Volume
	[Unbundled Network Elements - Ordering]																					
	% Rejected Service Requests - Mechanized																					
B.1.1.1	O-7	Switch Ports/FL (%)								Diagnostic								Diagnostic				
B.1.1.2	O-7	Local Interoffice Transport/FL (%)								Diagnostic								Diagnostic			17.65%	44,624
B.1.1.3	O-7	Loop + Port Combinations/FL (%)			11.93%	76,239				Diagnostic			15.14%	56,934				Diagnostic				
B.1.1.4	O-7	Combo (other)/FL (%)								Diagnostic								Diagnostic			15.72%	598
B.1.1.5	O-7	xDSL (ADSL, HDSL and UCL)/FL (%)			20.63%	795				Diagnostic			14.51%	1,006				Diagnostic			13.33%	75
B.1.1.6	O-7	ISDN Loop (UDN, UDC)/FL (%)			9.74%	349				Diagnostic			9.86%	71				Diagnostic			34.69%	245
B.1.1.7	O-7	Line Sharing/FL (%)			23.88%	224				Diagnostic			22.76%	268				Diagnostic			22.90%	930
B.1.1.8	O-7	2W Analog Loop Design/FL (%)			15.87%	970				Diagnostic			22.65%	830				Diagnostic			17.82%	943
B.1.1.9	O-7	2W Analog Loop Non-Design/FL (%)			10.15%	1,399				Diagnostic			16.07%	952				Diagnostic				
B.1.1.10	O-7	2W Analog Loop w/INP Design/FL (%)								Diagnostic								Diagnostic			100.00%	3
B.1.1.11	O-7	2W Analog Loop w/INP Non-Design/FL (%)								Diagnostic								Diagnostic			55.56%	109
B.1.1.12	O-7	2W Analog Loop w/LNP Design/FL (%)			66.57%	23				Diagnostic			44.19%	43				Diagnostic			59.14%	257
B.1.1.13	O-7	2W Analog Loop w/LNP Non-Design/FL (%)			98.97%	97				Diagnostic			97.09%	103				Diagnostic			22.39%	335
B.1.1.14	O-7	Other Design/FL (%)			100.00%	2				Diagnostic			100.00%	2				Diagnostic			48.24%	14,806
B.1.1.15	O-7	Other Non-Design/FL (%)			42.77%	15,881				Diagnostic			48.18%	12,756				Diagnostic			100.00%	1
B.1.1.16	O-7	INP Standalone/FL (%)			50.00%	2				Diagnostic			100.00%	1				Diagnostic			6.75%	3,796
B.1.1.17	O-7	LNP Standalone/FL (%)			9.55%	3,309				Diagnostic			9.14%	3,029				Diagnostic			13.16%	76
B.1.1.18	O-7	Digital Loop < DS1/FL (%)			17.34%	1,090				Diagnostic			18.41%	668				Diagnostic			22.36%	331
B.1.1.19	O-7	Digital Loop >= DS1/FL (%)			25.94%	266				Diagnostic			16.67%	300				Diagnostic				
B.1.1.20	O-7	EELs/FL (%)			100.00%	2				Diagnostic			21.82%	55				Diagnostic				
B.1.1.21	O-7	Line Splitting/FL (%)								Diagnostic								Diagnostic				
	% Rejected Service Requests - Partially Mechanized																					
B.1.2.1	O-7	Switch Ports/FL (%)								Diagnostic								Diagnostic				
B.1.2.2	O-7	Local Interoffice Transport/FL (%)								Diagnostic								Diagnostic			26.09%	17,723
B.1.2.3	O-7	Loop + Port Combinations/FL (%)			28.06%	15,130				Diagnostic			21.52%	17,473				Diagnostic				
B.1.2.4	O-7	Combo (other)/FL (%)								Diagnostic								Diagnostic			6.98%	86
B.1.2.5	O-7	xDSL (ADSL, HDSL and UCL)/FL (%)			3.92%	51				Diagnostic			10.34%	29				Diagnostic			11.94%	268
B.1.2.6	O-7	ISDN Loop (UDN, UDC)/FL (%)			2.56%	39				Diagnostic			26.67%	30				Diagnostic			37.07%	232
B.1.2.7	O-7	Line Sharing/FL (%)			30.30%	66				Diagnostic			40.00%	75				Diagnostic			28.28%	290
B.1.2.8	O-7	2W Analog Loop Design/FL (%)			23.05%	347				Diagnostic			22.83%	433				Diagnostic			18.28%	1,167
B.1.2.9	O-7	2W Analog Loop Non-Design/FL (%)			13.02%	1,574				Diagnostic			16.80%	1,393				Diagnostic				
B.1.2.10	O-7	2W Analog Loop w/INP Design/FL (%)								Diagnostic								Diagnostic				
B.1.2.11	O-7	2W Analog Loop w/INP Non-Design/FL (%)								Diagnostic								Diagnostic			37.52%	757
B.1.2.12	O-7	2W Analog Loop w/LNP Design/FL (%)			35.28%	309				Diagnostic			32.98%	376				Diagnostic			24.97%	2,211
B.1.2.13	O-7	2W Analog Loop w/LNP Non-Design/FL (%)			22.45%	1,096				Diagnostic			23.26%	1,419				Diagnostic			25.71%	245
B.1.2.14	O-7	Other Design/FL (%)								Diagnostic			100.00%	1				Diagnostic			29.32%	7,661
B.1.2.15	O-7	Other Non-Design/FL (%)			37.28%	10,040				Diagnostic			41.21%	7,238				Diagnostic				
B.1.2.16	O-7	INP Standalone/FL (%)								Diagnostic								Diagnostic			31.30%	1,901
B.1.2.17	O-7	LNP Standalone/FL (%)			30.95%	1,596				Diagnostic			29.47%	1,707				Diagnostic			11.94%	268
B.1.2.18	O-7	Digital Loop < DS1/FL (%)			3.49%	86				Diagnostic			20.00%	50				Diagnostic			25.82%	244
B.1.2.19	O-7	Digital Loop >= DS1/FL (%)			24.60%	374				Diagnostic			16.49%	285				Diagnostic				
B.1.2.20	O-7	EELs/FL (%)			5.00%	40				Diagnostic			33.33%	6				Diagnostic				
B.1.2.21	O-7	Line Splitting/FL (%)								Diagnostic								Diagnostic				
	% Rejected Service Requests - Non-Mechanized																					
B.1.3.1	O-7	Switch Ports/FL (%)								Diagnostic			0.00%	1				Diagnostic			50.00%	4
B.1.3.2	O-7	Local Interoffice Transport/FL (%)			57.58%	33				Diagnostic			11.11%	9				Diagnostic			46.87%	45
B.1.3.3	O-7	Loop + Port Combinations/FL (%)			38.57%	3,023				Diagnostic			41.32%	2,287				Diagnostic			43.23%	2,084
B.1.3.4	O-7	Combo (other)/FL (%)			77.27%	22				Diagnostic			77.78%	9				Diagnostic			78.57%	14
B.1.3.5	O-7	xDSL (ADSL, HDSL and UCL)/FL (%)			20.00%	100				Diagnostic			25.91%	274				Diagnostic			26.86%	283
B.1.3.6	O-7	ISDN Loop (UDN, UDC)/FL (%)			20.96%	439				Diagnostic			19.19%	344				Diagnostic			16.21%	475
B.1.3.7	O-7	Line Sharing/FL (%)			25.00%	48				Diagnostic			35.85%	53				Diagnostic			35.09%	114
B.1.3.8	O-7	2W Analog Loop Design/FL (%)			37.82%	156				Diagnostic			27.38%	168				Diagnostic			33.84%	263
B.1.3.9	O-7	2W Analog Loop Non-Design/FL (%)			39.12%	340				Diagnostic			25.93%	671				Diagnostic			25.83%	906
B.1.3.10	O-7	2W Analog Loop w/INP Design/FL (%)			69.23%	13				Diagnostic								Diagnostic			33.33%	6
B.1.3.11	O-7	2W Analog Loop w/INP Non-Design/FL (%)			25.00%	12				Diagnostic			44.44%	9				Diagnostic			20.00%	5
B.1.3.12	O-7	2W Analog Loop w/LNP Design/FL (%)			52.94%	17				Diagnostic			62.16%	37				Diagnostic			51.32%	106
B.1.3.13	O-7	2W Analog Loop w/LNP Non-Design/FL (%)			49.09%	55				Diagnostic			43.18%	44				Diagnostic			51.76%	85
B.1.3.14	O-7	Other Design/FL (%)								Diagnostic			50.00%	2				Diagnostic			29.30%	587
B.1.3.15	O-7	Other Non-Design/FL (%)			26.52%	1,676				Diagnostic			31.56%	1,752				Diagnostic			38.26%	1,882
B.1.3.16	O-7	INP Standalone/FL (%)			26.67%	60				Diagnostic			35.62%	73				Diagnostic			40.91%	88
B.1.3.17	O-7	LNP Standalone/FL (%)			30.32%	818				Diagnostic			40.32%	930				Diagnostic			35.78%	911
B.1.3.18	O-7	Digital Loop < DS1/FL (%)			20.72%	526				Diagnostic			19.96%	561				Diagnostic			19.17%	751
B.1.3.19	O-7	Digital Loop >= DS1/FL (%)			38.55%	358				Diagnostic			36.27%	408				Diagnostic			29.66%	590
B.1.3.20	O-7	EELs/FL (%)			39.77%	88				Diagnostic			49.14%	116				Diagnostic			55.17%	29
B.1.3.21	O-7	Line Splitting/FL (%)								Diagnostic								Diagnostic				
	Reject Interval - Mechanized																					
B.1.4.1	O-8	Switch Ports/FL (%)																			93.80%	7,901
B.1.4.2	O-8	Local Interoffice Transport/FL (%)			93.13%	9,200				NO			95.49%	8,651				NO				
B.1.4.3	O-8	Loop + Port Combinations/FL (%)																			98.94%	94
B.1.4.4	O-8	Combo (other)/FL (%)			98.78%	164				YES			98.63%	146				YES			80.00%	10
B.1.4.5	O-8	xDSL (ADSL, HDSL and UCL)/FL (%)			85.71%	35				NO			55.56%	9				NO			83.91%	87
B.1.4.6	O-8	ISDN Loop (UDN, UDC)/FL (%)			79.25%	53				NO			69.35%	62				NO			79.26%	217
B.1.4.7	O-8	Line Sharing/FL (%)			72.55%	153				NO			77.60%	192				NO			61.27%	173
B.1.4.8	O-8	2W Analog Loop Design/FL (%)			52.03%	148				NO			64.29%	154								
B.1.4.9	O-8	2W Analog Loop Non-Design/FL (%)																			100.00%	3
B.1.4.10	O-8	2W Analog Loop w/INP Design/FL (%)																			96.36%	81
B.1.4.11	O-8	2W Analog Loop w/INP Non-Design/FL (%)			81.25%	16				NO			94.74%	19				NO			92.11%	152
B.1.4.12	O-8	2W Analog Loop w/LNP Design/FL (%)			86.14%	101				NO			98.00%	100				NO			58.97%	78
B.1.4.13	O-8	2W Analog Loop w/LNP Non-Design/FL (%)			50.00%	2				NO			0.00%	2				NO			77.92%	7,270
B.1.4.14	O-8	Other Design/FL (%)			86.61%	7,170				NO			73.90%	6,226				YES			0.00%	1
B.1.4.15	O-8	Other Non-Design/FL (%)			100.00%	1				YES			100.00%	1				NO			98.40%	333
B.1.4.16	O-8	INP Standalone/FL (%)			98.42%	317				YES			98.06%	279				NO			80.00%	10
B.1.4.17	O-8	LNP Standalone/FL (%)			96.32%	190				NO			98.00%	125				NO			98.44%	77
B.1.4.18	O-8	Digital Loop < DS1/FL (%)			55.71%	70				NO			84.00%	50				NO				
B.1.4.19	O-8	Digital Loop >= DS1/FL (%)			100.00%	2				YES			83.33%	12								
B.1.4.20	O-8	EELs/FL (%)																				
B.1.4.21	O-8	Line Splitting/FL (%)																				
	Reject Interval - Partially Mechanized - 10 hours																					
B.1.7.1	O-8	Switch Ports/FL (%)																YES			86.71%	5,058
B.1.7.2	O-8	Local Interoffice Transport/FL (%)			96.93%	4,339				YES			96.49%	3,983								
B.1.7.3	O-8	Loop + Port Combinations/FL (%)																YES			83.33%	6
B.1.7.4	O-8	Combo (other)/FL (%)			100.00%	2				YES			100.00%	3				NO			80.00%	35
B.1.7.5	O-8	xDSL (ADSL, HDSL and UCL)/FL (%)			100.00%	1				YES			46.67%	15				NO			75.28%	89
B.1.7.6	O-8	ISDN Loop (UDN, UDC)/FL (%)			95.00%	20				YES			84.85%	33				NO			84.52%	84
B.1.7.7	O-8	Line Sharing/FL (%)			90.24%	82				NO			87.96%	108				NO			84.71%	204
B.1.7.8	O-8	2W Analog Loop Design/FL (%)			76.78%	211				NO			78.24%	239								
B.1.7.9	O-8	2W Analog Loop Non-Design/FL (%)																				
B.1.7.10	O-8	2W Analog Loop w/INP Design/FL (%)																				
B.1.7.11	O-8	2W Analog Loop w/INP Non-Design/FL (%)																				

BellSouth Monthly Performance Summary

Florida Permanent, September 2002 - May 2002

			Standard Deviation	Standard Error	Zscore	Equity
	Unbundled Network Elements - Ordering					
	% Rejected Service Requests - Mechanized					
B.1.1.1	O-7	Switch Ports/FL (%)				Diagnostic
B.1.1.2	O-7	Local Interoffice Transport/FL (%)				Diagnostic
B.1.1.3	O-7	Loop + Port Combinations/FL (%)				Diagnostic
B.1.1.4	O-7	Combo (other)/FL (%)				Diagnostic
B.1.1.5	O-7	xDSL (ADSL, HDSL and UCL)/FL (%)				Diagnostic
B.1.1.6	O-7	ISDN Loop (UDN, UDC)/FL (%)				Diagnostic
B.1.1.7	O-7	Line Sharing/FL (%)				Diagnostic
B.1.1.8	O-7	2W Analog Loop Design/FL (%)				Diagnostic
B.1.1.9	O-7	2W Analog Loop Non-Design/FL (%)				Diagnostic
B.1.1.10	O-7	2W Analog Loop w/INP Design/FL (%)				Diagnostic
B.1.1.11	O-7	2W Analog Loop w/INP Non-Design/FL (%)				Diagnostic
B.1.1.12	O-7	2W Analog Loop w/LNP Design/FL (%)				Diagnostic
B.1.1.13	O-7	2W Analog Loop w/LNP Non-Design/FL (%)				Diagnostic
B.1.1.14	O-7	Other Design/FL (%)				Diagnostic
B.1.1.15	O-7	Other Non-Design/FL (%)				Diagnostic
B.1.1.16	O-7	INP Standalone/FL (%)				Diagnostic
B.1.1.17	O-7	LNP Standalone/FL (%)				Diagnostic
B.1.1.18	O-7	Digital Loop < DS1/FL (%)				Diagnostic
B.1.1.19	O-7	Digital Loop >= DS1/FL (%)				Diagnostic
B.1.1.20	O-7	EELs/FL (%)				Diagnostic
B.1.1.21	O-7	Line Splitting/FL (%)				Diagnostic
	% Rejected Service Requests - Partially Mechanized					
B.1.2.1	O-7	Switch Ports/FL (%)				Diagnostic
B.1.2.2	O-7	Local Interoffice Transport/FL (%)				Diagnostic
B.1.2.3	O-7	Loop + Port Combinations/FL (%)				Diagnostic
B.1.2.4	O-7	Combo (other)/FL (%)				Diagnostic
B.1.2.5	O-7	xDSL (ADSL, HDSL and UCL)/FL (%)				Diagnostic
B.1.2.6	O-7	ISDN Loop (UDN, UDC)/FL (%)				Diagnostic
B.1.2.7	O-7	Line Sharing/FL (%)				Diagnostic
B.1.2.8	O-7	2W Analog Loop Design/FL (%)				Diagnostic
B.1.2.9	O-7	2W Analog Loop Non-Design/FL (%)				Diagnostic
B.1.2.10	O-7	2W Analog Loop w/INP Design/FL (%)				Diagnostic
B.1.2.11	O-7	2W Analog Loop w/INP Non-Design/FL (%)				Diagnostic
B.1.2.12	O-7	2W Analog Loop w/LNP Design/FL (%)				Diagnostic
B.1.2.13	O-7	2W Analog Loop w/LNP Non-Design/FL (%)				Diagnostic
B.1.2.14	O-7	Other Design/FL (%)				Diagnostic
B.1.2.15	O-7	Other Non-Design/FL (%)				Diagnostic
B.1.2.16	O-7	INP Standalone/FL (%)				Diagnostic
B.1.2.17	O-7	LNP Standalone/FL (%)				Diagnostic
B.1.2.18	O-7	Digital Loop < DS1/FL (%)				Diagnostic
B.1.2.19	O-7	Digital Loop >= DS1/FL (%)				Diagnostic
B.1.2.20	O-7	EELs/FL (%)				Diagnostic
B.1.2.21	O-7	Line Splitting/FL (%)				Diagnostic
	% Rejected Service Requests - Non-Mechanized					
B.1.3.1	O-7	Switch Ports/FL (%)				Diagnostic
B.1.3.2	O-7	Local Interoffice Transport/FL (%)				Diagnostic
B.1.3.3	O-7	Loop + Port Combinations/FL (%)				Diagnostic
B.1.3.4	O-7	Combo (other)/FL (%)				Diagnostic
B.1.3.5	O-7	xDSL (ADSL, HDSL and UCL)/FL (%)				Diagnostic
B.1.3.6	O-7	ISDN Loop (UDN, UDC)/FL (%)				Diagnostic
B.1.3.7	O-7	Line Sharing/FL (%)				Diagnostic
B.1.3.8	O-7	2W Analog Loop Design/FL (%)				Diagnostic
B.1.3.9	O-7	2W Analog Loop Non-Design/FL (%)				Diagnostic
B.1.3.10	O-7	2W Analog Loop w/INP Design/FL (%)				Diagnostic
B.1.3.11	O-7	2W Analog Loop w/INP Non-Design/FL (%)				Diagnostic
B.1.3.12	O-7	2W Analog Loop w/LNP Design/FL (%)				Diagnostic
B.1.3.13	O-7	2W Analog Loop w/LNP Non-Design/FL (%)				Diagnostic
B.1.3.14	O-7	Other Design/FL (%)				Diagnostic
B.1.3.15	O-7	Other Non-Design/FL (%)				Diagnostic
B.1.3.16	O-7	INP Standalone/FL (%)				Diagnostic
B.1.3.17	O-7	LNP Standalone/FL (%)				Diagnostic
B.1.3.18	O-7	Digital Loop < DS1/FL (%)				Diagnostic
B.1.3.19	O-7	Digital Loop >= DS1/FL (%)				Diagnostic
B.1.3.20	O-7	EELs/FL (%)				Diagnostic
B.1.3.21	O-7	Line Splitting/FL (%)				Diagnostic
	Reject Interval - Mechanized					
B.1.4.1	O-8	Switch Ports/FL (%)				
B.1.4.2	O-8	Local Interoffice Transport/FL (%)				NO
B.1.4.3	O-8	Loop + Port Combinations/FL (%)				
B.1.4.4	O-8	Combo (other)/FL (%)				YES
B.1.4.5	O-8	xDSL (ADSL, HDSL and UCL)/FL (%)				NO
B.1.4.6	O-8	ISDN Loop (UDN, UDC)/FL (%)				NO
B.1.4.7	O-8	Line Sharing/FL (%)				NO
B.1.4.8	O-8	2W Analog Loop Design/FL (%)				NO
B.1.4.9	O-8	2W Analog Loop Non-Design/FL (%)				
B.1.4.10	O-8	2W Analog Loop w/INP Design/FL (%)				YES
B.1.4.11	O-8	2W Analog Loop w/INP Non-Design/FL (%)				YES
B.1.4.12	O-8	2W Analog Loop w/LNP Design/FL (%)				NO
B.1.4.13	O-8	2W Analog Loop w/LNP Non-Design/FL (%)				NO
B.1.4.14	O-8	Other Design/FL (%)				NO
B.1.4.15	O-8	Other Non-Design/FL (%)				NO
B.1.4.16	O-8	INP Standalone/FL (%)				NO
B.1.4.17	O-8	LNP Standalone/FL (%)				NO
B.1.4.18	O-8	Digital Loop < DS1/FL (%)				NO
B.1.4.19	O-8	Digital Loop >= DS1/FL (%)				
B.1.4.20	O-8	EELs/FL (%)				
B.1.4.21	O-8	Line Splitting/FL (%)				
	Reject Interval - Partially Mechanized - 10 hours					
B.1.7.1	O-8	Switch Ports/FL (%)				
B.1.7.2	O-8	Local Interoffice Transport/FL (%)				NO
B.1.7.3	O-8	Loop + Port Combinations/FL (%)				
B.1.7.4	O-8	Combo (other)/FL (%)				NO
B.1.7.5	O-8	xDSL (ADSL, HDSL and UCL)/FL (%)				NO
B.1.7.6	O-8	ISDN Loop (UDN, UDC)/FL (%)				NO
B.1.7.7	O-8	Line Sharing/FL (%)				NO
B.1.7.8	O-8	2W Analog Loop Design/FL (%)				NO
B.1.7.9	O-8	2W Analog Loop Non-Design/FL (%)				
B.1.7.10	O-8	2W Analog Loop w/INP Design/FL (%)				
B.1.7.11	O-8	2W Analog Loop w/INP Non-Design/FL (%)				

BellSouth Monthly Performance Summary

Florida Permanent, September 2002 - May 2002

			Benchmark/ Analog	September BST Measure	September BST Volume	September CLEC Measure	September CLEC Volume	September Standard Deviation	September Standard Error	September Zscore	September Equity	August BST Measure	August BST Volume	August CLEC Measure	August CLEC Volume	August Standard Deviation	August Standard Error	August Zscore	August Equity
B.1.7.12	O-8	2W Analog Loop w/LNP Design/FL (%)	>= 95% w in 10 hrs			94.95%	99				NO			92.31%	65				NO
B.1.7.13	O-8	2W Analog Loop w/LNP Non-Design/FL (%)	>= 95% w in 10 hrs			86.53%	245				NO			88.14%	253				NO
B.1.7.14	O-8	Other Design/FL (%)	>= 95% w in 10 hrs											100.00%	1				YES
B.1.7.15	O-8	Other Non-Design/FL (%)	>= 95% w in 10 hrs			97.24%	1,992				YES			98.84%	3,022				YES
B.1.7.16	O-8	INP Standalone/FL (%)	>= 95% w in 10 hrs																
B.1.7.17	O-8	LNP Standalone/FL (%)	>= 95% w in 10 hrs			93.75%	496				NO			96.62%	532				YES
B.1.7.18	O-8	Digital Loop < DS1/FL (%)	>= 95% w in 10 hrs			90.00%	10				NO			50.00%	2				NO
B.1.7.19	O-8	Digital Loop >= DS1/FL (%)	>= 95% w in 10 hrs			86.75%	83				NO			88.66%	97				NO
B.1.7.20	O-8	EELs/FL (%)	>= 95% w in 10 hrs			96.30%	27				YES			90.00%	10				NO
B.1.7.21	O-8	Line Splitting/FL (%)	>= 95% w in 10 hrs																
		Reject Interval - Non-Mechanized																	
B.1.8.1	O-8	Switch Ports/FL (%)	>= 95% w in 24 hrs																
B.1.8.2	O-8	Local Interoffice Transport/FL (%)	>= 95% w in 24 hrs			100.00%	14				YES			100.00%	25				YES
B.1.8.3	O-8	Loop + Port Combinations/FL (%)	>= 95% w in 24 hrs			96.03%	453				YES			97.57%	1,195				YES
B.1.8.4	O-8	Combo (other)/FL (%)	>= 95% w in 24 hrs			100.00%	3				YES			91.67%	12				NO
B.1.8.5	O-8	xDSL (ADSL, HDSL and UCL)/FL (%)	>= 95% w in 24 hrs			96.88%	32				YES			100.00%	22				YES
B.1.8.6	O-8	ISDN Loop (UDN, UDC)/FL (%)	>= 95% w in 24 hrs			100.00%	17				YES			100.00%	64				YES
B.1.8.7	O-8	Line Sharing/FL (%)	>= 95% w in 24 hrs			100.00%	22				YES			100.00%	14				YES
B.1.8.8	O-8	2W Analog Loop Design/FL (%)	>= 95% w in 24 hrs			98.72%	78				YES			98.88%	76				YES
B.1.8.9	O-8	2W Analog Loop Non-Design/FL (%)	>= 95% w in 24 hrs			100.00%	104				YES			100.00%	183				YES
B.1.8.10	O-8	2W Analog Loop w/INP Design/FL (%)	>= 95% w in 24 hrs																
B.1.8.11	O-8	2W Analog Loop w/INP Non-Design/FL (%)	>= 95% w in 24 hrs			100.00%	1				YES								
B.1.8.12	O-8	2W Analog Loop w/LNP Design/FL (%)	>= 95% w in 24 hrs			100.00%	20				YES			100.00%	25				YES
B.1.8.13	O-8	2W Analog Loop w/LNP Non-Design/FL (%)	>= 95% w in 24 hrs			100.00%	14				YES			100.00%	24				YES
B.1.8.14	O-8	Other Design/FL (%)	>= 95% w in 24 hrs																
B.1.8.15	O-8	Other Non-Design/FL (%)	>= 95% w in 24 hrs			99.32%	295				YES			98.42%	442				YES
B.1.8.16	O-8	INP Standalone/FL (%)	>= 95% w in 24 hrs			100.00%	8				YES			100.00%	16				YES
B.1.8.17	O-8	LNP Standalone/FL (%)	>= 95% w in 24 hrs			99.31%	144				YES			99.03%	310				YES
B.1.8.18	O-8	Digital Loop < DS1/FL (%)	>= 95% w in 24 hrs			100.00%	41				YES			100.00%	81				YES
B.1.8.19	O-8	Digital Loop >= DS1/FL (%)	>= 95% w in 24 hrs			97.17%	106				YES			97.87%	188				YES
B.1.8.20	O-8	EELs/FL (%)	>= 95% w in 24 hrs			100.00%	55				YES			96.00%	75				YES
B.1.8.21	O-8	Line Splitting/FL (%)	>= 95% w in 24 hrs																
		FOC Timeliness - Mechanized																	
B.1.9.1	O-9	Switch Ports/FL (%)	>= 95% w in 3 hrs																
B.1.9.2	O-9	Local Interoffice Transport/FL (%)	>= 95% w in 3 hrs																
B.1.9.3	O-9	Loop + Port Combinations/FL (%)	>= 95% w in 3 hrs			97.81%	45,642				YES			98.57%	68,436				YES
B.1.9.4	O-9	Combo (other)/FL (%)	>= 95% w in 3 hrs																
B.1.9.5	O-9	xDSL (ADSL, HDSL and UCL)/FL (%)	>= 95% w in 3 hrs			98.95%	667				YES			98.14%	485				YES
B.1.9.6	O-9	ISDN Loop (UDN, UDC)/FL (%)	>= 95% w in 3 hrs			95.42%	349				YES			76.56%	126				NO
B.1.9.7	O-9	Line Sharing/FL (%)	>= 95% w in 3 hrs			98.47%	393				YES			97.98%	196				YES
B.1.9.8	O-9	2W Analog Loop Design/FL (%)	>= 95% w in 3 hrs			95.63%	641				YES			93.38%	631				NO
B.1.9.9	O-9	2W Analog Loop Non-Design/FL (%)	>= 95% w in 3 hrs			99.32%	882				YES			99.03%	829				YES
B.1.9.10	O-9	2W Analog Loop w/INP Design/FL (%)	>= 95% w in 3 hrs																
B.1.9.11	O-9	2W Analog Loop w/INP Non-Design/FL (%)	>= 95% w in 3 hrs																
B.1.9.12	O-9	2W Analog Loop w/LNP Design/FL (%)	>= 95% w in 3 hrs			66.67%	6				NO			88.67%	9				NO
B.1.9.13	O-9	2W Analog Loop w/LNP Non-Design/FL (%)	>= 95% w in 3 hrs											96.55%	29				YES
B.1.9.14	O-9	Other Design/FL (%)	>= 95% w in 3 hrs											100.00%	1				YES
B.1.9.15	O-9	Other Non-Design/FL (%)	>= 95% w in 3 hrs			98.92%	9,819				YES			96.37%	12,289				YES
B.1.9.16	O-9	INP Standalone/FL (%)	>= 95% w in 3 hrs																
B.1.9.17	O-9	LNP Standalone/FL (%)	>= 95% w in 3 hrs			99.34%	3,309				YES			99.38%	3,240				YES
B.1.9.18	O-9	Digital Loop < DS1/FL (%)	>= 95% w in 3 hrs			97.76%	849				YES			93.49%	584				NO
B.1.9.19	O-9	Digital Loop >= DS1/FL (%)	>= 95% w in 3 hrs			97.69%	216				YES			96.95%	262				YES
B.1.9.20	O-9	EELs/FL (%)	>= 95% w in 3 hrs											66.67%	3				NO
B.1.9.21	O-9	Line Splitting/FL (%)	>= 95% w in 3 hrs																
		FOC Timeliness - Partially Mechanized - 10 hours																	
B.1.12.1	O-9	Switch Ports/FL (%)	>= 95% w in 10 hrs																
B.1.12.2	O-9	Local Interoffice Transport/FL (%)	>= 95% w in 10 hrs																
B.1.12.3	O-9	Loop + Port Combinations/FL (%)	>= 95% w in 10 hrs			91.20%	7,559				NO			91.83%	12,339				NO
B.1.12.4	O-9	Combo (other)/FL (%)	>= 95% w in 10 hrs																
B.1.12.5	O-9	xDSL (ADSL, HDSL and UCL)/FL (%)	>= 95% w in 10 hrs			89.06%	64				NO			81.82%	22				NO
B.1.12.6	O-9	ISDN Loop (UDN, UDC)/FL (%)	>= 95% w in 10 hrs			83.33%	18				NO			80.95%	21				NO
B.1.12.7	O-9	Line Sharing/FL (%)	>= 95% w in 10 hrs			94.44%	72				NO			96.36%	55				YES
B.1.12.8	O-9	2W Analog Loop Design/FL (%)	>= 95% w in 10 hrs			94.87%	234				NO			91.33%	196				NO
B.1.12.9	O-9	2W Analog Loop Non-Design/FL (%)	>= 95% w in 10 hrs			94.52%	1,552				NO			95.09%	1,609				YES
B.1.12.10	O-9	2W Analog Loop w/INP Design/FL (%)	>= 95% w in 10 hrs																
B.1.12.11	O-9	2W Analog Loop w/INP Non-Design/FL (%)	>= 95% w in 10 hrs																
B.1.12.12	O-9	2W Analog Loop w/LNP Design/FL (%)	>= 95% w in 10 hrs			72.22%	108				NO			59.80%	149				NO
B.1.12.13	O-9	2W Analog Loop w/LNP Non-Design/FL (%)	>= 95% w in 10 hrs			81.88%	778				NO			84.03%	952				NO
B.1.12.14	O-9	Other Design/FL (%)	>= 95% w in 10 hrs																
B.1.12.15	O-9	Other Non-Design/FL (%)	>= 95% w in 10 hrs			96.79%	3,611				YES			96.47%	5,206				YES
B.1.12.16	O-9	INP Standalone/FL (%)	>= 95% w in 10 hrs																
B.1.12.17	O-9	LNP Standalone/FL (%)	>= 95% w in 10 hrs			95.73%	843				YES			96.23%	1,027				YES
B.1.12.18	O-9	Digital Loop < DS1/FL (%)	>= 95% w in 10 hrs			87.50%	40				NO			81.40%	43				NO
B.1.12.19	O-9	Digital Loop >= DS1/FL (%)	>= 95% w in 10 hrs			90.46%	262				NO			91.07%	280				NO
B.1.12.20	O-9	EELs/FL (%)	>= 95% w in 10 hrs			82.72%	81				NO			71.43%	35				NO
B.1.12.21	O-9	Line Splitting/FL (%)	>= 95% w in 10 hrs																
		FOC Timeliness - Non-Mechanized																	
B.1.13.1	O-9	Switch Ports/FL (%)	>= 95% w in 24 hrs			100.00%	13				YES			88.24%	17				NO
B.1.13.2	O-9	Local Interoffice Transport/FL (%)	>= 95% w in 24 hrs			91.79%	597				NO			94.27%	1,779				NO
B.1.13.3	O-9	Loop + Port Combinations/FL (%)	>= 95% w in 24 hrs			100.00%	2				YES			100.00%	7				YES
B.1.13.4	O-9	Combo (other)/FL (%)	>= 95% w in 24 hrs			92.06%	63				NO			96.92%	65				YES
B.1.13.5	O-9	xDSL (ADSL, HDSL and UCL)/FL (%)	>= 95% w in 24 hrs			96.74%	92				YES			98.23%	229				YES
B.1.13.6	O-9	ISDN Loop (UDN, UDC)/FL (%)	>= 95% w in 24 hrs			100.00%	29				YES			100.00%	15				YES
B.1.13.7	O-9	Line Sharing/FL (%)	>= 95% w in 24 hrs			94.90%	98				NO			94.74%	95				NO
B.1.13.8	O-9	2W Analog Loop Design/FL (%)	>= 95% w in 24 hrs			98.32%	179				YES			97.29%	296				YES
B.1.13.9	O-9	2W Analog Loop Non-Design/FL (%)	>= 95% w in 24 hrs											100.00%	3				YES
B.1.13.10	O-9	2W Analog Loop w/INP Design/FL (%)	>= 95% w in 24 hrs			100.00%	2				YES			100.00%	4				YES
B.1.13.11	O-9	2W Analog Loop w/INP Non-Design/FL (%)	>= 95% w in 24 hrs			100.00%	10				YES			78.57%	14				NO
B.1.13.12	O-9	2W Analog Loop w/LNP Design/FL (%)	>= 95% w in 24 hrs			100.00%	17				YES			100.00%	26				YES
B.1.13.13	O-9	2W Analog Loop w/LNP Non-Design/FL (%)	>= 95% w in 24 hrs																
B.1.13.14	O-9	Other Design/FL (%)	>= 95% w in 24 hrs			98.98%	590				YES			99.59%	981				YES
B.1.13.15	O-9	Other Non-Design/FL (%)	>= 95% w in 24 hrs			100.00%	14				YES			100.00%	26				YES
B.1.13.16	O-9	INP Standalone/FL (%)	>= 95% w in 24 hrs			97.70%	348				YES			99.00%	600				YES
B.1.13.17	O-9	LNP Standalone/FL (%)	>= 95% w in 24 hrs			94.59%	148				NO			98.23%	285				YES
B.1.13.18	O-9	Digital Loop < DS1/FL (%)	>= 95% w in 24 hrs			93.80%	129				NO			99.14%	232				YES
B.1.13.19	O-9	Digital Loop >= DS1/FL (%)	>= 95% w in 24 hrs			94.55%	55				NO			97.98%	99				YES
B.1.13.20	O-9	EELs/FL (%)	>= 95% w in 24 hrs																
B.1.13.21	O-9	Line Splitting/FL (%)	>= 95% w in 24 hrs																
		FOC & Reject Response Completeness - Mechanized																	
B.1.14.1.1	O-11	Switch Ports/EDI/FL (%)	>= 95%																
B.1.14.1.2	O-11	Switch Ports/TAG/FL (%)	>= 95%																

BellSouth Monthly Performance Summary
Florida Permanent, September 2002 - May 2002

		July								June								May			
		BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.1.7.12	O-8	2W Analog Loop w/INP Design/FL (%)		72.80%	125				NO			75.18%	137				NO			74.23%	291
B.1.7.13	O-8	2W Analog Loop w/LNP Non-Design/FL (%)		72.96%	269				NO			85.30%	347				NO			84.13%	586
B.1.7.14	O-8	Other Design/FL (%)										100.00%	1				YES			86.36%	66
B.1.7.15	O-8	Other Non-Design/FL (%)		99.04%	3,866				YES			98.67%	3,004				YES			97.46%	2,324
B.1.7.16	O-8	INP Standalone/FL (%)																			
B.1.7.17	O-8	LNP Standalone/FL (%)		89.06%	530				NO			91.38%	522				NO			91.00%	611
B.1.7.18	O-8	Digital Loop < DS1/FL (%)		100.00%	3				YES			52.94%	17				NO			60.00%	35
B.1.7.19	O-8	Digital Loop >= DS1/FL (%)		89.25%	93				NO			87.76%	49				NO			88.36%	66
B.1.7.20	O-8	EELs/FL (%)		100.00%	2				YES			100.00%	2				YES				
B.1.7.21	O-8	Line Splitting/FL (%)																			
	Reject Interval - Non-Mechanized																				
B.1.8.1	O-8	Switch Ports/FL (%)																		100.00%	2
B.1.8.2	O-8	Local Interoffice Transport/FL (%)		94.74%	19				NO			100.00%	1				YES			100.00%	21
B.1.8.3	O-8	Loop + Port Combinations/FL (%)		98.22%	1,179				YES			98.50%	998				YES			98.38%	925
B.1.8.4	O-8	Combo (other)/FL (%)		100.00%	17				YES			100.00%	7				YES			100.00%	12
B.1.8.5	O-8	xDSL (ADSL, HDSL and UCL)/FL (%)		100.00%	23				YES			100.00%	74				YES			97.37%	76
B.1.8.6	O-8	ISDN Loop (UDN, UDC)/FL (%)		100.00%	84				YES			100.00%	72				YES			100.00%	81
B.1.8.7	O-8	Line Sharing/FL (%)		100.00%	12				YES			100.00%	19				YES			100.00%	43
B.1.8.8	O-8	2W Analog Loop Design/FL (%)		100.00%	60				YES			100.00%	46				YES			98.90%	91
B.1.8.9	O-8	2W Analog Loop Non-Design/FL (%)		100.00%	137				YES			100.00%	184				YES			100.00%	241
B.1.8.10	O-8	2W Analog Loop w/INP Design/FL (%)		100.00%	9				YES											100.00%	2
B.1.8.11	O-8	2W Analog Loop w/INP Non-Design/FL (%)		100.00%	3				YES			100.00%	4				YES			100.00%	1
B.1.8.12	O-8	2W Analog Loop w/LNP Design/FL (%)		90.00%	10				NO			95.83%	24				YES			97.14%	70
B.1.8.13	O-8	2W Analog Loop w/LNP Non-Design/FL (%)		100.00%	26				YES			100.00%	21				YES			97.83%	46
B.1.8.14	O-8	Other Design/FL (%)										100.00%	1				YES			99.43%	175
B.1.8.15	O-8	Other Non-Design/FL (%)		99.17%	484				YES			99.94%	557				YES			98.91%	737
B.1.8.16	O-8	INP Standalone/FL (%)		100.00%	16				YES			98.15%	26				YES			100.00%	36
B.1.8.17	O-8	LNP Standalone/FL (%)		99.60%	252				YES			98.44%	384				YES			98.82%	338
B.1.8.18	O-8	Digital Loop < DS1/FL (%)		100.00%	114				YES			100.00%	120				YES			97.97%	148
B.1.8.19	O-8	Digital Loop >= DS1/FL (%)		98.60%	143				YES			99.33%	150				YES			99.44%	178
B.1.8.20	O-8	EELs/FL (%)		100.00%	35				YES			98.31%	59				YES			100.00%	16
B.1.8.21	O-8	Line Splitting/FL (%)																			
	FOC Timeliness - Mechanized																				
B.1.9.1	O-9	Switch Ports/FL (%)																			
B.1.9.2	O-9	Local Interoffice Transport/FL (%)																			
B.1.9.3	O-9	Loop + Port Combinations/FL (%)		96.70%	67,227				YES			96.82%	48,246				YES			96.97%	35,367
B.1.9.4	O-9	Combo (other)/FL (%)																			
B.1.9.5	O-9	xDSL (ADSL, HDSL and UCL)/FL (%)		98.96%	579				YES			92.54%	657				NO			97.67%	472
B.1.9.6	O-9	ISDN Loop (UDN, UDC)/FL (%)		76.26%	155				NO			96.66%	54				YES			97.02%	48
B.1.9.7	O-9	Line Sharing/FL (%)		97.75%	178				YES			97.74%	221				YES			99.34%	152
B.1.9.8	O-9	2W Analog Loop Design/FL (%)		94.60%	815				NO			94.23%	641				NO			97.68%	708
B.1.9.9	O-9	2W Analog Loop Non-Design/FL (%)		98.42%	1,269				YES			99.00%	803				YES			99.21%	757
B.1.9.10	O-9	2W Analog Loop w/INP Design/FL (%)																			
B.1.9.11	O-9	2W Analog Loop w/INP Non-Design/FL (%)																			
B.1.9.12	O-9	2W Analog Loop w/LNP Design/FL (%)		80.00%	5				NO			91.30%	23				NO			100.00%	34
B.1.9.13	O-9	2W Analog Loop w/LNP Non-Design/FL (%)										0.00%	3				NO			100.00%	76
B.1.9.14	O-9	Other Design/FL (%)																		96.05%	253
B.1.9.15	O-9	Other Non-Design/FL (%)		96.30%	9,109				YES			94.85%	6,757				NO			93.86%	7,564
B.1.9.16	O-9	INP Standalone/FL (%)		100.00%	1				YES												
B.1.9.17	O-9	LNP Standalone/FL (%)		98.27%	3,002				YES			96.73%	2,815				YES			95.95%	3,434
B.1.9.18	O-9	Digital Loop < DS1/FL (%)		94.34%	689				NO			98.08%	520				YES			97.96%	49
B.1.9.19	O-9	Digital Loop >= DS1/FL (%)		84.10%	195				NO			63.81%	244				NO			96.00%	250
B.1.9.20	O-9	EELs/FL (%)										60.00%	15				NO				
B.1.9.21	O-9	Line Splitting/FL (%)																			
	FOC Timeliness - Partially Mechanized - 10 hours																				
B.1.12.1	O-9	Switch Ports/FL (%)																			
B.1.12.2	O-9	Local Interoffice Transport/FL (%)																			
B.1.12.3	O-9	Loop + Port Combinations/FL (%)		94.67%	11,745				NO			92.68%	14,712				NO			80.73%	13,549
B.1.12.4	O-9	Combo (other)/FL (%)																			
B.1.12.5	O-9	xDSL (ADSL, HDSL and UCL)/FL (%)		89.19%	37				NO			88.89%	18				NO			95.24%	63
B.1.12.6	O-9	ISDN Loop (UDN, UDC)/FL (%)		70.83%	24				NO			82.05%	39				NO			94.01%	267
B.1.12.7	O-9	Line Sharing/FL (%)		100.00%	42				YES			88.89%	54				NO			87.50%	168
B.1.12.8	O-9	2W Analog Loop Design/FL (%)		91.70%	289				NO			91.67%	348				NO			83.64%	214
B.1.12.9	O-9	2W Analog Loop Non-Design/FL (%)		94.11%	1,426				NO			91.53%	1,204				NO			93.01%	1,102
B.1.12.10	O-9	2W Analog Loop w/INP Design/FL (%)																			
B.1.12.11	O-9	2W Analog Loop w/INP Non-Design/FL (%)																			
B.1.12.12	O-9	2W Analog Loop w/LNP Design/FL (%)		74.04%	208				NO			66.57%	273				NO			77.96%	490
B.1.12.13	O-9	2W Analog Loop w/LNP Non-Design/FL (%)		85.43%	969				NO			90.56%	1,107				NO			92.92%	1,694
B.1.12.14	O-9	Other Design/FL (%)																		84.34%	198
B.1.12.15	O-9	Other Non-Design/FL (%)		97.44%	6,418				YES			96.82%	4,121				YES			95.70%	5,302
B.1.12.16	O-9	INP Standalone/FL (%)																			
B.1.12.17	O-9	LNP Standalone/FL (%)		92.63%	1,139				NO			94.08%	1,182				NO			93.08%	1,330
B.1.12.18	O-9	Digital Loop < DS1/FL (%)		84.48%	58				NO			84.31%	51				NO			94.01%	267
B.1.12.19	O-9	Digital Loop >= DS1/FL (%)		83.78%	296				NO			84.84%	244				NO			84.26%	197
B.1.12.20	O-9	EELs/FL (%)		58.82%	17				NO			0.00%	1				NO				
B.1.12.21	O-9	Line Splitting/FL (%)																			
	FOC Timeliness - Non-Mechanized																				
B.1.13.1	O-9	Switch Ports/FL (%)										100.00%	1				YES			100.00%	2
B.1.13.2	O-9	Local Interoffice Transport/FL (%)		96.67%	15				NO			87.50%	8				NO			96.67%	30
B.1.13.3	O-9	Loop + Port Combinations/FL (%)		95.88%	1,091				YES			95.67%	1,246				YES			95.01%	1,022
B.1.13.4	O-9	Combo (other)/FL (%)		100.00%	3				YES			100.00%	2				YES			75.00%	4
B.1.13.5	O-9	xDSL (ADSL, HDSL and UCL)/FL (%)		96.47%	85				YES			99.00%	200				YES			99.49%	196
B.1.13.6	O-9	ISDN Loop (UDN, UDC)/FL (%)		98.58%	348				YES			96.84%	285				YES			99.48%	382
B.1.13.7	O-9	Line Sharing/FL (%)		100.00%	35				YES			100.00%	35				YES			98.73%	79
B.1.13.8	O-9	2W Analog Loop Design/FL (%)		95.96%	99				YES			96.83%	126				YES			100.00%	170
B.1.13.9	O-9	2W Analog Loop Non-Design/FL (%)		97.16%	211				YES			99.21%	504				YES			99.04%	626
B.1.13.10	O-9	2W Analog Loop w/INP Design/FL (%)																		100.00%	2
B.1.13.11	O-9	2W Analog Loop w/INP Non-Design/FL (%)		100.00%	6				YES			100.00%	3				YES			100.00%	3
B.1.13.12	O-9	2W Analog Loop w/LNP Design/FL (%)		100.00%	7				YES			100.00%	14				YES			100.00%	39
B.1.13.13	O-9	2W Analog Loop w/LNP Non-Design/FL (%)		100.00%	25				YES			100.00%	23				YES			97.30%	37
B.1.13.14	O-9	Other Design/FL (%)										100.00%	1				YES			99.52%	414
B.1.13.15	O-9	Other Non-Design/FL (%)		98.82%	1,167				YES			98.36%	1,221				YES			99.31%	1,161
B.1.13.16	O-9	INP Standalone/FL (%)		100.00%	34				YES			97.44%	39				YES			100.00%	42
B.1.13.17	O-9	LNP Standalone/FL (%)		99.13%	573				YES			99.63%	540				YES			99.02%	512
B.1.13.18	O-9	Digital Loop < DS1/FL (%)		98.10%	421				YES			97.80%	455				YES			99.49%	583
B.1.13.19	O-9	Digital Loop >= DS1/FL (%)		99.08%	218				YES			98.44%	256				YES			99.52%	414
B.1.13.20	O-9	EELs/FL (%)		96.15%	54				YES			100.00%	63				YES			100.00%	13
B.1.13.21	O-9	Line Splitting/FL (%)																			
	FOC & Reject Response Completeness - Mechanized																				
B.1.14.1.1	O-11	Switch Ports/EDI/FL (%)																			
B.1.14.1.2	O-11	Switch Ports/TAG/FL (%)																			

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			Standard Deviation	Standard Error	Zscore	Equity
B.1.7.12	[illegible]	2W Analog Loop w/LNP Non-Design/FL (%)				NO
B.1.7.13	O-8	2W Analog Loop w/LNP Non-Design/FL (%)				NO
B.1.7.14	O-8	Other Design/FL (%)				NO
B.1.7.15	O-8	Other Non-Design/FL (%)				YES
B.1.7.16	O-8	INP Standalone/FL (%)				
B.1.7.17	O-8	LNP Standalone/FL (%)				NO
B.1.7.18	O-8	Digital Loop < DS1/FL (%)				NO
B.1.7.19	O-8	Digital Loop >= DS1/FL (%)				NO
B.1.7.20	O-8	EELs/FL (%)				
B.1.7.21	O-8	Line Splitting/FL (%)				
		Reject Interval - Non-Mechanized				
B.1.8.1	O-8	Switch Ports/FL (%)				YES
B.1.8.2	O-8	Local Interoffice Transport/FL (%)				YES
B.1.8.3	O-8	Loop + Port Combinations/FL (%)				YES
B.1.8.4	O-8	Combo (other)/FL (%)				YES
B.1.8.5	O-8	xDSL (ADSL, HDSL and UCL)/FL (%)				YES
B.1.8.6	O-8	ISDN Loop (UDN, UDC)/FL (%)				YES
B.1.8.7	O-8	Line Sharing/FL (%)				YES
B.1.8.8	O-8	2W Analog Loop Design/FL (%)				YES
B.1.8.9	O-8	2W Analog Loop Non-Design/FL (%)				YES
B.1.8.10	O-8	2W Analog Loop w/INP Design/FL (%)				YES
B.1.8.11	O-8	2W Analog Loop w/INP Non-Design/FL (%)				YES
B.1.8.12	O-8	2W Analog Loop w/LNP Design/FL (%)				YES
B.1.8.13	O-8	2W Analog Loop w/LNP Non-Design/FL (%)				YES
B.1.8.14	O-8	Other Design/FL (%)				YES
B.1.8.15	O-8	Other Non-Design/FL (%)				YES
B.1.8.16	O-8	INP Standalone/FL (%)				YES
B.1.8.17	O-8	LNP Standalone/FL (%)				YES
B.1.8.18	O-8	Digital Loop < DS1/FL (%)				YES
B.1.8.19	O-8	Digital Loop >= DS1/FL (%)				YES
B.1.8.20	O-8	EELs/FL (%)				YES
B.1.8.21	O-8	Line Splitting/FL (%)				
		FOC Timeliness - Mechanized				
B.1.9.1	O-9	Switch Ports/FL (%)				
B.1.9.2	O-9	Local Interoffice Transport/FL (%)				
B.1.9.3	O-9	Loop + Port Combinations/FL (%)				YES
B.1.9.4	O-9	Combo (other)/FL (%)				
B.1.9.5	O-9	xDSL (ADSL, HDSL and UCL)/FL (%)				YES
B.1.9.6	O-9	ISDN Loop (UDN, UDC)/FL (%)				YES
B.1.9.7	O-9	Line Sharing/FL (%)				YES
B.1.9.8	O-9	2W Analog Loop Design/FL (%)				YES
B.1.9.9	O-9	2W Analog Loop Non-Design/FL (%)				YES
B.1.9.10	O-9	2W Analog Loop w/INP Design/FL (%)				
B.1.9.11	O-9	2W Analog Loop w/INP Non-Design/FL (%)				
B.1.9.12	O-9	2W Analog Loop w/LNP Design/FL (%)				YES
B.1.9.13	O-9	2W Analog Loop w/LNP Non-Design/FL (%)				YES
B.1.9.14	O-9	Other Design/FL (%)				YES
B.1.9.15	O-9	Other Non-Design/FL (%)				NO
B.1.9.16	O-9	INP Standalone/FL (%)				
B.1.9.17	O-9	LNP Standalone/FL (%)				YES
B.1.9.18	O-9	Digital Loop < DS1/FL (%)				YES
B.1.9.19	O-9	Digital Loop >= DS1/FL (%)				YES
B.1.9.20	O-9	EELs/FL (%)				
B.1.9.21	O-9	Line Splitting/FL (%)				
		FOC Timeliness - Partially Mechanized - 10 hours				
B.1.12.1	O-9	Switch Ports/FL (%)				
B.1.12.2	O-9	Local Interoffice Transport/FL (%)				
B.1.12.3	O-9	Loop + Port Combinations/FL (%)				NO
B.1.12.4	O-9	Combo (other)/FL (%)				
B.1.12.5	O-9	xDSL (ADSL, HDSL and UCL)/FL (%)				YES
B.1.12.6	O-9	ISDN Loop (UDN, UDC)/FL (%)				NO
B.1.12.7	O-9	Line Sharing/FL (%)				NO
B.1.12.8	O-9	2W Analog Loop Design/FL (%)				NO
B.1.12.9	O-9	2W Analog Loop Non-Design/FL (%)				NO
B.1.12.10	O-9	2W Analog Loop w/INP Design/FL (%)				
B.1.12.11	O-9	2W Analog Loop w/INP Non-Design/FL (%)				
B.1.12.12	O-9	2W Analog Loop w/LNP Design/FL (%)				NO
B.1.12.13	O-9	2W Analog Loop w/LNP Non-Design/FL (%)				NO
B.1.12.14	O-9	Other Design/FL (%)				NO
B.1.12.15	O-9	Other Non-Design/FL (%)				YES
B.1.12.16	O-9	INP Standalone/FL (%)				
B.1.12.17	O-9	LNP Standalone/FL (%)				NO
B.1.12.18	O-9	Digital Loop < DS1/FL (%)				NO
B.1.12.19	O-9	Digital Loop >= DS1/FL (%)				NO
B.1.12.20	O-9	EELs/FL (%)				
B.1.12.21	O-9	Line Splitting/FL (%)				
		FOC Timeliness - Non-Mechanized				
B.1.13.1	O-9	Switch Ports/FL (%)				YES
B.1.13.2	O-9	Local Interoffice Transport/FL (%)				YES
B.1.13.3	O-9	Loop + Port Combinations/FL (%)				YES
B.1.13.4	O-9	Combo (other)/FL (%)				NO
B.1.13.5	O-9	xDSL (ADSL, HDSL and UCL)/FL (%)				YES
B.1.13.6	O-9	ISDN Loop (UDN, UDC)/FL (%)				YES
B.1.13.7	O-9	Line Sharing/FL (%)				YES
B.1.13.8	O-9	2W Analog Loop Design/FL (%)				YES
B.1.13.9	O-9	2W Analog Loop Non-Design/FL (%)				YES
B.1.13.10	O-9	2W Analog Loop w/INP Design/FL (%)				YES
B.1.13.11	O-9	2W Analog Loop w/INP Non-Design/FL (%)				YES
B.1.13.12	O-9	2W Analog Loop w/LNP Design/FL (%)				YES
B.1.13.13	O-9	2W Analog Loop w/LNP Non-Design/FL (%)				YES
B.1.13.14	O-9	Other Design/FL (%)				YES
B.1.13.15	O-9	Other Non-Design/FL (%)				YES
B.1.13.16	O-9	INP Standalone/FL (%)				YES
B.1.13.17	O-9	LNP Standalone/FL (%)				YES
B.1.13.18	O-9	Digital Loop < DS1/FL (%)				YES
B.1.13.19	O-9	Digital Loop >= DS1/FL (%)				YES
B.1.13.20	O-9	EELs/FL (%)				YES
B.1.13.21	O-9	Line Splitting/FL (%)				
		FOC & Reject Response Completeness - Mechanized				
B.1.14.1.1	O-11	Switch Ports/EDI/FL (%)				
B.1.14.1.2	O-11	Switch Ports/TAG/FL (%)				

BellSouth Monthly Performance Summary

Florida Permanent, September 2002 - May 2002

			Benchmark/ Analog	September BST Measure	September BST Volume	September CLEC Measure	September CLEC Volume	September Standard Deviation	September Standard Error	September Zscore	September Equity	August BST Measure	August BST Volume	August CLEC Measure	August CLEC Volume	August Standard Deviation	August Standard Error	August Zscore	August Equity
B.1.14.2.1	O-11	Local Interoffice Transport/EDI/FL (%)	>= 95%																
B.1.14.2.2	O-11	Local Interoffice Transport/TAG/FL (%)	>= 95%			100.00%	30,394				YES			99.42%	30,685				YES
B.1.14.3.1	O-11	Loop + Port Combinations/EDI/FL (%)	>= 95%			99.84%	24,432				YES			99.86%	51,079				YES
B.1.14.3.2	O-11	Loop + Port Combinations/TAG/FL (%)	>= 95%																
B.1.14.4.1	O-11	Combo (other)/EDI/FL (%)	>= 95%																
B.1.14.4.2	O-11	Combo (other)/TAG/FL (%)	>= 95%																
B.1.14.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/FL (%)	>= 95%			96.94%	359				YES			98.90%	363				YES
B.1.14.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/FL (%)	>= 95%			93.30%	587				NO			88.41%	345				NO
B.1.14.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/FL (%)	>= 95%			98.00%	50				YES			90.91%	11				NO
B.1.14.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/FL (%)	>= 95%			95.32%	342				YES			82.02%	208				NO
B.1.14.7.1	O-11	Line Sharing/EDI/FL (%)	>= 95%			100.00%	447				YES			99.06%	212				YES
B.1.14.7.2	O-11	Line Sharing/TAG/FL (%)	>= 95%			100.00%	60				YES			97.83%	46				YES
B.1.14.8.1	O-11	2W Analog Loop Design/EDI/FL (%)	>= 95%			95.53%	179				YES			92.66%	218				NO
B.1.14.8.2	O-11	2W Analog Loop Design/TAG/FL (%)	>= 95%			99.47%	565				YES			99.63%	819				YES
B.1.14.9.1	O-11	2W Analog Loop Non-Design/EDI/FL (%)	>= 95%			100.00%	11				YES			100.00%	4				YES
B.1.14.9.2	O-11	2W Analog Loop Non-Design/TAG/FL (%)	>= 95%			98.96%	961				YES			95.82%	1,004				YES
B.1.14.10.1	O-11	2W Analog Loop w/INP Design/EDI/FL (%)	>= 95%																
B.1.14.10.2	O-11	2W Analog Loop w/INP Design/TAG/FL (%)	>= 95%																
B.1.14.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/FL (%)	>= 95%																
B.1.14.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/FL (%)	>= 95%																
B.1.14.12.1	O-11	2W Analog Loop w/LNP Design/EDI/FL (%)	>= 95%			70.00%	10				NO			87.50%	32				NO
B.1.14.12.2	O-11	2W Analog Loop w/LNP Design/TAG/FL (%)	>= 95%			44.44%	9				NO			66.67%	3				NO
B.1.14.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/FL (%)	>= 95%			75.00%	4				NO			100.00%	6				YES
B.1.14.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/FL (%)	>= 95%			81.82%	110				NO			92.48%	133				NO
B.1.14.14.1	O-11	Other Design/EDI/FL (%)	>= 95%																
B.1.14.14.2	O-11	Other Design/TAG/FL (%)	>= 95%			100.00%	1				YES			100.00%	7				YES
B.1.14.15.1	O-11	Other Non-Design/EDI/FL (%)	>= 95%			99.95%	11,194				YES			99.24%	14,280				YES
B.1.14.15.2	O-11	Other Non-Design/TAG/FL (%)	>= 95%			99.47%	1,696				YES			93.81%	1,680				NO
B.1.14.16.1	O-11	INP Standalone/EDI/FL (%)	>= 95%											100.00%	1				YES
B.1.14.16.2	O-11	INP Standalone/TAG/FL (%)	>= 95%																
B.1.14.17.1	O-11	LNP Standalone/EDI/FL (%)	>= 95%			99.85%	3,400				YES			99.17%	3,384				YES
B.1.14.17.2	O-11	LNP Standalone/TAG/FL (%)	>= 95%			96.05%	154				YES			96.58%	234				YES
B.1.14.18.1	O-11	Digital Loop < DS1/EDI/FL (%)	>= 95%			96.18%	329				YES			98.92%	366				YES
B.1.14.18.2	O-11	Digital Loop < DS1/TAG/FL (%)	>= 95%			67.39%	726				YES			77.33%	525				NO
B.1.14.19.1	O-11	Digital Loop >= DS1/EDI/FL (%)	>= 95%			94.29%	105				NO			90.55%	127				NO
B.1.14.19.2	O-11	Digital Loop >= DS1/TAG/FL (%)	>= 95%			98.45%	194				YES			99.10%	223				YES
B.1.14.20.1	O-11	EELs/EDI/FL (%)	>= 95%			35.29%	17				NO			50.00%	10				NO
B.1.14.20.2	O-11	EELs/TAG/FL (%)	>= 95%			100.00%	2				YES			66.67%	3				NO
B.1.14.21.1	O-11	Line Splitting/EDI/FL (%)	>= 95%																
B.1.14.21.2	O-11	Line Splitting/TAG/FL (%)	>= 95%																
		FOC & Reject Response Completeness - Partially Mechanized																	
B.1.15.1.1	O-11	Switch Ports/EDI/FL (%)	>= 95%																
B.1.15.1.2	O-11	Switch Ports/TAG/FL (%)	>= 95%																
B.1.15.2.1	O-11	Local Interoffice Transport/EDI/FL (%)	>= 95%																
B.1.15.2.2	O-11	Local Interoffice Transport/TAG/FL (%)	>= 95%			99.97%	3,924				YES			99.75%	3,994				YES
B.1.15.3.1	O-11	Loop + Port Combinations/EDI/FL (%)	>= 95%			99.96%	7,551				YES			99.82%	11,893				YES
B.1.15.3.2	O-11	Loop + Port Combinations/TAG/FL (%)	>= 95%																
B.1.15.4.1	O-11	Combo (other)/EDI/FL (%)	>= 95%																
B.1.15.4.2	O-11	Combo (other)/TAG/FL (%)	>= 95%																
B.1.15.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/FL (%)	>= 95%			86.67%	30				NO			83.33%	15				NO
B.1.15.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/FL (%)	>= 95%			77.59%	58				NO			60.00%	15				NO
B.1.15.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/FL (%)	>= 95%			100.00%	2				YES			100.00%	1				YES
B.1.15.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/FL (%)	>= 95%			100.00%	16				YES			81.82%	22				NO
B.1.15.7.1	O-11	Line Sharing/EDI/FL (%)	>= 95%			100.00%	100				YES			100.00%	54				YES
B.1.15.7.2	O-11	Line Sharing/TAG/FL (%)	>= 95%			100.00%	22				YES			100.00%	21				YES
B.1.15.8.1	O-11	2W Analog Loop Design/EDI/FL (%)	>= 95%			100.00%	207				YES			99.32%	148				YES
B.1.15.8.2	O-11	2W Analog Loop Design/TAG/FL (%)	>= 95%			98.77%	81				YES			98.86%	90				YES
B.1.15.9.1	O-11	2W Analog Loop Non-Design/EDI/FL (%)	>= 95%			100.00%	48				YES			100.00%	10				YES
B.1.15.9.2	O-11	2W Analog Loop Non-Design/TAG/FL (%)	>= 95%			99.78%	1,636				YES			99.70%	1,683				YES
B.1.15.10.1	O-11	2W Analog Loop w/INP Design/EDI/FL (%)	>= 95%																
B.1.15.10.2	O-11	2W Analog Loop w/INP Design/TAG/FL (%)	>= 95%																
B.1.15.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/FL (%)	>= 95%																
B.1.15.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/FL (%)	>= 95%																
B.1.15.12.1	O-11	2W Analog Loop w/LNP Design/EDI/FL (%)	>= 95%			100.00%	130				YES			100.00%	162				YES
B.1.15.12.2	O-11	2W Analog Loop w/LNP Design/TAG/FL (%)	>= 95%			100.00%	50				YES			100.00%	41				YES
B.1.15.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/FL (%)	>= 95%			100.00%	48				YES			100.00%	13				YES
B.1.15.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/FL (%)	>= 95%			100.00%	895				YES			99.91%	1,125				YES
B.1.15.14.1	O-11	Other Design/EDI/FL (%)	>= 95%																
B.1.15.14.2	O-11	Other Design/TAG/FL (%)	>= 95%											100.00%	1				YES
B.1.15.15.1	O-11	Other Non-Design/EDI/FL (%)	>= 95%			99.86%	4,995				YES			99.82%	7,191				YES
B.1.15.15.2	O-11	Other Non-Design/TAG/FL (%)	>= 95%			99.81%	532				YES			98.21%	726				YES
B.1.15.16.1	O-11	INP Standalone/EDI/FL (%)	>= 95%																
B.1.15.16.2	O-11	INP Standalone/TAG/FL (%)	>= 95%																
B.1.15.17.1	O-11	LNP Standalone/EDI/FL (%)	>= 95%			100.00%	1,180				YES			99.84%	1,287				YES
B.1.15.17.2	O-11	LNP Standalone/TAG/FL (%)	>= 95%			100.00%	129				YES			99.54%	217				YES
B.1.15.18.1	O-11	Digital Loop < DS1/EDI/FL (%)	>= 95%			100.00%	9				YES			92.86%	14				NO
B.1.15.18.2	O-11	Digital Loop < DS1/TAG/FL (%)	>= 95%			75.61%	41				NO			75.00%	36				NO
B.1.15.19.1	O-11	Digital Loop >= DS1/EDI/FL (%)	>= 95%			99.61%	258				YES			99.61%	255				YES
B.1.15.19.2	O-11	Digital Loop >= DS1/TAG/FL (%)	>= 95%			100.00%	65				YES			98.91%	92				YES
B.1.15.20.1	O-11	EELs/EDI/FL (%)	>= 95%			96.34%	82				YES			83.78%	37				NO
B.1.15.20.2	O-11	EELs/TAG/FL (%)	>= 95%			81.54%	39				NO			59.09%	22				NO
B.1.15.21.1	O-11	Line Splitting/EDI/FL (%)	>= 95%																
B.1.15.21.2	O-11	Line Splitting/TAG/FL (%)	>= 95%																
		FOC & Reject Response Completeness - Non-Mechanized																	
B.1.16.1	O-11	Switch Ports/FL (%)	>= 95%																
B.1.16.2	O-11	Local Interoffice Transport/FL (%)	>= 95%			100.00%	26				YES			97.44%	39				YES
B.1.16.3	O-11	Loop + Port Combinations/FL (%)	>= 95%			96.49%	966				YES			93.26%	3,004				NO
B.1.16.4	O-11	Combo (other)/FL (%)	>= 95%			100.00%	5				YES			85.00%	20				NO
B.1.16.5	O-11	xDSL (ADSL, HDSL and UCL)/FL (%)	>= 95%			94.79%	96				NO			95.45%	88				YES
B.1.16.6	O-11	ISDN Loop (UDN, UDC)/FL (%)	>= 95%			95.88%	97				YES			93.58%	296				NO
B.1.16.7	O-11	Line Sharing/FL (%)	>= 95%			98.04%	51				YES			100.00%	28				YES
B.1.16.8	O-11	2W Analog Loop Design/FL (%)	>= 95%			96.00%	175				YES			95.40%	174				YES
B.1.16.9	O-11	2W Analog Loop Non-Design/FL (%)	>= 95%			97.11%	277				YES			95.13%	472				YES
B.1.16.10	O-11	2W Analog Loop w/INP Design/FL (%)	>= 95%											50.00%	6				NO
B.1.16.11	O-11	2W Analog Loop w/INP Non-Design/FL (%)	>= 95%			100.00%	3				YES			100.00%	4				YES
B.1.16.12	O-11	2W Analog Loop w/LNP Design/FL (%)	>= 95%			93.55%	31				NO			97.44%	39				YES
B.1.16.13	O-11	2W Analog Loop w/LNP Non-Design/FL (%)	>= 95%			96.88%	32				YES			97.96%	49				YES
B.1.16.14	O-11	Other Design/FL (%)	>= 95%																
B.1.16.15	O-11	Other Non-Design/FL (%)	>= 95%			98.73%	969				YES			98.04%	1,381				YES
B.1.16.16	O-11	INP Standalone/FL (%)	>= 95%			84.62%	26				NO			90.91%	44				NO
B.1.16.17	O-11	LNP Standalone/FL (%)	>= 95%			96.36%	489				YES			95.50%	914				YES
B.1.16.18	O-11	Digital Loop < DS1/FL (%)	>= 95%			96.07%	178				YES			94.10%	373				NO

BellSouth Monthly Performance Summary

Florida Permanent, September 2002 - May 2002

			July							June								May			
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zacors	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zacors	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.1.14.2.1	BST	FOC Interoffice Transport/EDI/FL (%)																				
B.1.14.2.2	O-11	Local Interoffice Transport/TAG/FL (%)								YES			99.53%	20,966				YES			96.35%	13,232
B.1.14.3.1	O-11	Loop + Port Combinations/EDI/FL (%)			99.88%	29,319				YES			99.97%	35,968				YES			96.45%	31,392
B.1.14.3.2	O-11	Loop + Port Combinations/TAG/FL (%)			99.97%	46,920																
B.1.14.4.1	O-11	Combo (other)/EDI/FL (%)																				
B.1.14.4.2	O-11	Combo (other)/TAG/FL (%)																				
B.1.14.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/FL (%)			99.07%	430				YES			98.00%	472				YES			97.00%	367
B.1.14.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/FL (%)			85.75%	365				NO			84.04%	534				NO			86.15%	231
B.1.14.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/FL (%)			90.91%	22				NO			100.00%	13				YES			80.00%	5
B.1.14.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/FL (%)			51.68%	327				NO			100.00%	58				YES			77.14%	70
B.1.14.7.1	O-11	Line Sharing/EDI/FL (%)			100.00%	182				YES			100.00%	182				YES			95.21%	167
B.1.14.7.2	O-11	Line Sharing/TAG/FL (%)			100.00%	42				YES			100.00%	86				YES			87.18%	78
B.1.14.8.1	O-11	2W Analog Loop Design/EDI/FL (%)			93.36%	226				NO			97.35%	340				YES			91.77%	328
B.1.14.8.2	O-11	2W Analog Loop Design/TAG/FL (%)			99.46%	744				YES			98.78%	490				YES			98.50%	802
B.1.14.9.1	O-11	2W Analog Loop Non-Design/EDI/FL (%)			100.00%	2				YES												
B.1.14.9.2	O-11	2W Analog Loop Non-Design/TAG/FL (%)			99.36%	1,397				YES			99.47%	952				YES			98.61%	943
B.1.14.10.1	O-11	2W Analog Loop w/INP Design/EDI/FL (%)																				
B.1.14.10.2	O-11	2W Analog Loop w/INP Design/TAG/FL (%)																				
B.1.14.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/FL (%)																				
B.1.14.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/FL (%)																			100.00%	3
B.1.14.12.1	O-11	2W Analog Loop w/LNP Design/EDI/FL (%)			95.24%	21				YES			96.97%	33				YES			86.46%	96
B.1.14.12.2	O-11	2W Analog Loop w/LNP Design/TAG/FL (%)			100.00%	2				YES			100.00%	10				YES			92.31%	13
B.1.14.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/FL (%)																				
B.1.14.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/FL (%)			98.97%	97				YES			100.00%	103				YES			88.72%	257
B.1.14.14.1	O-11	Other Design/EDI/FL (%)																			89.86%	138
B.1.14.14.2	O-11	Other Design/TAG/FL (%)			100.00%	2				YES			100.00%	2				YES			96.48%	197
B.1.14.15.1	O-11	Other Non-Design/EDI/FL (%)			98.01%	14,536				YES			98.19%	11,850				YES			97.02%	13,524
B.1.14.15.2	O-11	Other Non-Design/TAG/FL (%)			90.71%	1,345				NO			89.29%	906				NO			97.13%	1,282
B.1.14.16.1	O-11	INP Standalone/EDI/FL (%)																			100.00%	1
B.1.14.16.2	O-11	INP Standalone/TAG/FL (%)			100.00%	2				YES			100.00%	1				YES				
B.1.14.17.1	O-11	LNP Standalone/EDI/FL (%)			99.46%	3,123				YES			99.65%	2,861				YES			99.31%	3,486
B.1.14.17.2	O-11	LNP Standalone/TAG/FL (%)			99.46%	186				YES			97.62%	198				YES			97.10%	310
B.1.14.18.1	O-11	Digital Loop < DS1/EDI/FL (%)			98.86%	448				YES			98.33%	480				YES			80.00%	5
B.1.14.18.2	O-11	Digital Loop < DS1/TAG/FL (%)			67.90%	542				NO			89.89%	188				NO			77.46%	71
B.1.14.19.1	O-11	Digital Loop >= DS1/EDI/FL (%)			89.80%	98				NO			93.28%	119				NO			89.86%	138
B.1.14.19.2	O-11	Digital Loop >= DS1/TAG/FL (%)			99.40%	168				YES			98.90%	181				YES			98.45%	193
B.1.14.20.1	O-11	EELs/EDI/FL (%)			100.00%	2				YES			62.50%	8				NO				
B.1.14.20.2	O-11	EELs/TAG/FL (%)											46.81%	47				NO				
B.1.14.21.1	O-11	Line Splitting/EDI/FL (%)																				
B.1.14.21.2	O-11	Line Splitting/TAG/FL (%)																				
		FOC & Reject Response Completeness - Partially Mechanized																				
B.1.15.1.1	O-11	Switch Ports/EDI/FL (%)																				
B.1.15.1.2	O-11	Switch Ports/TAG/FL (%)																				
B.1.15.2.1	O-11	Local Interoffice Transport/EDI/FL (%)																				
B.1.15.2.2	O-11	Local Interoffice Transport/TAG/FL (%)																				
B.1.15.3.1	O-11	Loop + Port Combinations/EDI/FL (%)			99.94%	5,399				YES			99.44%	6,469				YES			95.98%	3,755
B.1.15.3.2	O-11	Loop + Port Combinations/TAG/FL (%)			99.77%	9,731				YES			99.74%	11,004				YES			99.83%	13,968
B.1.15.4.1	O-11	Combo (other)/EDI/FL (%)																				
B.1.15.4.2	O-11	Combo (other)/TAG/FL (%)																				
B.1.15.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/FL (%)			100.00%	25				YES			75.00%	12				NO			73.58%	53
B.1.15.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/FL (%)			46.15%	26				NO			54.71%	17				NO			78.79%	33
B.1.15.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/FL (%)											50.00%	2				NO			100.00%	18
B.1.15.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/FL (%)			66.67%	39				NO			100.00%	28				YES			99.60%	250
B.1.15.7.1	O-11	Line Sharing/EDI/FL (%)			100.00%	48				YES			100.00%	52				YES			97.58%	124
B.1.15.7.2	O-11	Line Sharing/TAG/FL (%)			100.00%	18				YES			95.65%	23				YES			98.15%	108
B.1.15.8.1	O-11	2W Analog Loop Design/EDI/FL (%)			99.54%	217				YES			99.72%	355				YES			95.41%	196
B.1.15.8.2	O-11	2W Analog Loop Design/TAG/FL (%)			100.00%	130				YES			98.72%	78				YES			100.00%	94
B.1.15.9.1	O-11	2W Analog Loop Non-Design/EDI/FL (%)			100.00%	6				YES												
B.1.15.9.2	O-11	2W Analog Loop Non-Design/TAG/FL (%)			99.74%	1,568				YES			99.71%	1,393				YES			99.91%	1,167
B.1.15.10.1	O-11	2W Analog Loop w/INP Design/EDI/FL (%)																				
B.1.15.10.2	O-11	2W Analog Loop w/INP Design/TAG/FL (%)																				
B.1.15.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/FL (%)																				
B.1.15.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/FL (%)																				
B.1.15.12.1	O-11	2W Analog Loop w/LNP Design/EDI/FL (%)			99.58%	237				YES			99.65%	282				YES			100.00%	805
B.1.15.12.2	O-11	2W Analog Loop w/LNP Design/TAG/FL (%)			100.00%	72				YES			100.00%	94				YES			100.00%	152
B.1.15.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/FL (%)			100.00%	2				YES												
B.1.15.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/FL (%)			99.97%	7,094				YES			100.00%	1,419				YES			100.00%	2,211
B.1.15.14.1	O-11	Other Design/EDI/FL (%)																			93.08%	159
B.1.15.14.2	O-11	Other Design/TAG/FL (%)											100.00%	1				YES			100.00%	86
B.1.15.15.1	O-11	Other Non-Design/EDI/FL (%)			99.83%	9,442				YES			99.87%	6,956				YES			94.81%	7,193
B.1.15.15.2	O-11	Other Non-Design/TAG/FL (%)			95.99%	598				YES			98.10%	282				YES			96.37%	468
B.1.15.16.1	O-11	INP Standalone/EDI/FL (%)																				
B.1.15.16.2	O-11	INP Standalone/TAG/FL (%)																				
B.1.15.17.1	O-11	LNP Standalone/EDI/FL (%)			100.00%	1,389				YES			99.52%	1,451				YES			99.87%	1,511
B.1.15.17.2	O-11	LNP Standalone/TAG/FL (%)			100.00%	207				YES			100.00%	256				YES			99.49%	390
B.1.15.18.1	O-11	Digital Loop < DS1/EDI/FL (%)			100.00%	25				YES			71.43%	14				NO			100.00%	18
B.1.15.18.2	O-11	Digital Loop < DS1/TAG/FL (%)			57.38%	61				NO			88.89%	36				NO			99.60%	250
B.1.15.19.1	O-11	Digital Loop >= DS1/EDI/FL (%)			99.28%	279				YES			99.12%	227				YES			93.08%	159
B.1.15.19.2	O-11	Digital Loop >= DS1/TAG/FL (%)			100.00%	95				YES			98.28%	58				YES			100.00%	85
B.1.15.20.1	O-11	EELs/EDI/FL (%)			57.89%	19				NO			100.00%	2				YES				
B.1.15.20.2	O-11	EELs/TAG/FL (%)			33.33%	21				NO			25.00%	4				NO				
B.1.15.21.1	O-11	Line Splitting/EDI/FL (%)																				
B.1.15.21.2	O-11	Line Splitting/TAG/FL (%)																				
		FOC & Reject Response Completeness - Non-Mechanized																				
B.1.16.1	O-11	Switch Ports/FL (%)											100.00%	1				YES			100.00%	4
B.1.16.2	O-11	Local Interoffice Transport/FL (%)			96.97%	33				YES			100.00%	9				YES			100.00%	45
B.1.16.3	O-11	Loop + Port Combinations/FL (%)			92.96%	3,023				NO			92.61%	2,287				NO			91.41%	2,064
B.1.16.4	O-11	Combo (other)/FL (%)			95.45%	22				YES			100.00%	9				YES			92.86%	14
B.1.16.5	O-11	xDSL (ADSL, HDSL and UCL)/FL (%)			96.00%	100				YES			97.08%	274				YES			94.70%	283
B.1.16.6	O-11	ISDN Loop (UDN, UDC)/FL (%)			96.81%	439				YES			97.67%	344				YES			93.47%	475
B.1.16.7	O-11	Line Sharing/FL (%)			97.92%	48				YES			100.00%	53				YES			99.12%	114
B.1.16.8	O-11	2W Analog Loop Design/FL (%)			96.51%	156				YES			98.21%	168				YES			95.06%	263
B.1.16.9	O-11	2W Analog Loop Non-Design/FL (%)			96.18%	340				YES			97.02%	671				YES			91.72%	906
B.1.16.10	O-11	2W Analog Loop w/INP Design/FL (%)			69.23%	13				NO											66.67%	6
B.1.16.11	O-11	2W Analog Loop w/INP Non-Design/FL (%)			75.00%	12				NO			77.78%	9				NO			80.00%	5
B.1.16.12	O-11	2W Analog Loop w/LNP Design/FL (%)			94.12%	17				NO			89.19%	37				NO			95.28%	106
B.1.16.13	O-11	2W Analog Loop w/LNP Non-Design/FL (%)			94.55%	55				NO			96.45%	44				YES			90.50%	86
B.1.16.14	O-11	Other Design/FL (%)											100.00%	2				YES			95.40%	587
B.1.16.15	O-11	Other Non-Design/FL (%)			98.21%	1,676				YES			98.29%	1,752				YES			98.71%	1,862
B.1.16.16	O-11	INP Standalone/FL (%)			83.33%	60				NO			86.30%	73				NO			96.26%	88
B.1.16.17	O-11	LNP Standalone/FL (%)			98.20%	818				YES			97.20%	930				YES			96.91%	911
B.1.16.18	O-11	Digital Loop < DS1/FL (%)			98.58%	526				YES			97.50%	561				YES			93.87%	751

BellSouth Monthly Performance Summary

Florida Permanent, September 2002 - May 2002

			Standard Deviation	Standard Error	Zscore	Equity
B.1.14.2.1	HSN-11	Local Interoffice Transport/DET/FL (%)				
B.1.14.2.2	O-11	Local Interoffice Transport/TAG/FL (%)				
B.1.14.3.1	O-11	Loop + Port Combinations/EDI/FL (%)				YES
B.1.14.3.2	O-11	Loop + Port Combinations/TAG/FL (%)				YES
B.1.14.4.1	O-11	Combo (other)/EDI/FL (%)				
B.1.14.4.2	O-11	Combo (other)/TAG/FL (%)				
B.1.14.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/FL (%)				YES
B.1.14.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/FL (%)				NO
B.1.14.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/FL (%)				NO
B.1.14.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/FL (%)				NO
B.1.14.7.1	O-11	Line Sharing/EDI/FL (%)				YES
B.1.14.7.2	O-11	Line Sharing/TAG/FL (%)				NO
B.1.14.8.1	O-11	2W Analog Loop Design/EDI/FL (%)				NO
B.1.14.8.2	O-11	2W Analog Loop Design/TAG/FL (%)				YES
B.1.14.9.1	O-11	2W Analog Loop Non-Design/EDI/FL (%)				
B.1.14.9.2	O-11	2W Analog Loop Non-Design/TAG/FL (%)				YES
B.1.14.10.1	O-11	2W Analog Loop w/INP Design/EDI/FL (%)				
B.1.14.10.2	O-11	2W Analog Loop w/INP Design/TAG/FL (%)				
B.1.14.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/FL (%)				
B.1.14.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/FL (%)				YES
B.1.14.12.1	O-11	2W Analog Loop w/LNP Design/EDI/FL (%)				NO
B.1.14.12.2	O-11	2W Analog Loop w/LNP Design/TAG/FL (%)				NO
B.1.14.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/FL (%)				
B.1.14.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/FL (%)				NO
B.1.14.14.1	O-11	Other Design/EDI/FL (%)				NO
B.1.14.14.2	O-11	Other Design/TAG/FL (%)				YES
B.1.14.15.1	O-11	Other Non-Design/EDI/FL (%)				YES
B.1.14.15.2	O-11	Other Non-Design/TAG/FL (%)				NO
B.1.14.16.1	O-11	INP Standalone/EDI/FL (%)				YES
B.1.14.16.2	O-11	INP Standalone/TAG/FL (%)				
B.1.14.17.1	O-11	LNP Standalone/EDI/FL (%)				YES
B.1.14.17.2	O-11	LNP Standalone/TAG/FL (%)				YES
B.1.14.18.1	O-11	Digital Loop < DS1/EDI/FL (%)				NO
B.1.14.18.2	O-11	Digital Loop < DS1/TAG/FL (%)				NO
B.1.14.19.1	O-11	Digital Loop >= DS1/EDI/FL (%)				NO
B.1.14.19.2	O-11	Digital Loop >= DS1/TAG/FL (%)				YES
B.1.14.20.1	O-11	EELs/EDI/FL (%)				
B.1.14.20.2	O-11	EELs/TAG/FL (%)				
B.1.14.21.1	O-11	Line Splitting/EDI/FL (%)				
B.1.14.21.2	O-11	Line Splitting/TAG/FL (%)				
		FOC & Reject Response Completeness - Partially Mechanized				
B.1.15.1.1	O-11	Switch Ports/EDI/FL (%)				
B.1.15.1.2	O-11	Switch Ports/TAG/FL (%)				
B.1.15.2.1	O-11	Local Interoffice Transport/EDI/FL (%)				
B.1.15.2.2	O-11	Local Interoffice Transport/TAG/FL (%)				
B.1.15.3.1	O-11	Loop + Port Combinations/EDI/FL (%)				YES
B.1.15.3.2	O-11	Loop + Port Combinations/TAG/FL (%)				YES
B.1.15.4.1	O-11	Combo (other)/EDI/FL (%)				
B.1.15.4.2	O-11	Combo (other)/TAG/FL (%)				
B.1.15.5.1	O-11	xDSL (ADSL, HDSL and UCL)/EDI/FL (%)				NO
B.1.15.5.2	O-11	xDSL (ADSL, HDSL and UCL)/TAG/FL (%)				NO
B.1.15.6.1	O-11	ISDN Loop (UDN, UDC)/EDI/FL (%)				YES
B.1.15.6.2	O-11	ISDN Loop (UDN, UDC)/TAG/FL (%)				YES
B.1.15.7.1	O-11	Line Sharing/EDI/FL (%)				YES
B.1.15.7.2	O-11	Line Sharing/TAG/FL (%)				YES
B.1.15.8.1	O-11	2W Analog Loop Design/EDI/FL (%)				YES
B.1.15.8.2	O-11	2W Analog Loop Design/TAG/FL (%)				YES
B.1.15.9.1	O-11	2W Analog Loop Non-Design/EDI/FL (%)				
B.1.15.9.2	O-11	2W Analog Loop Non-Design/TAG/FL (%)				YES
B.1.15.10.1	O-11	2W Analog Loop w/INP Design/EDI/FL (%)				
B.1.15.10.2	O-11	2W Analog Loop w/INP Design/TAG/FL (%)				
B.1.15.11.1	O-11	2W Analog Loop w/INP Non-Design/EDI/FL (%)				
B.1.15.11.2	O-11	2W Analog Loop w/INP Non-Design/TAG/FL (%)				
B.1.15.12.1	O-11	2W Analog Loop w/LNP Design/EDI/FL (%)				YES
B.1.15.12.2	O-11	2W Analog Loop w/LNP Design/TAG/FL (%)				YES
B.1.15.13.1	O-11	2W Analog Loop w/LNP Non-Design/EDI/FL (%)				
B.1.15.13.2	O-11	2W Analog Loop w/LNP Non-Design/TAG/FL (%)				YES
B.1.15.14.1	O-11	Other Design/EDI/FL (%)				NO
B.1.15.14.2	O-11	Other Design/TAG/FL (%)				YES
B.1.15.15.1	O-11	Other Non-Design/EDI/FL (%)				NO
B.1.15.15.2	O-11	Other Non-Design/TAG/FL (%)				YES
B.1.15.16.1	O-11	INP Standalone/EDI/FL (%)				
B.1.15.16.2	O-11	INP Standalone/TAG/FL (%)				
B.1.15.17.1	O-11	LNP Standalone/EDI/FL (%)				YES
B.1.15.17.2	O-11	LNP Standalone/TAG/FL (%)				YES
B.1.15.18.1	O-11	Digital Loop < DS1/EDI/FL (%)				YES
B.1.15.18.2	O-11	Digital Loop < DS1/TAG/FL (%)				YES
B.1.15.19.1	O-11	Digital Loop >= DS1/EDI/FL (%)				NO
B.1.15.19.2	O-11	Digital Loop >= DS1/TAG/FL (%)				YES
B.1.15.20.1	O-11	EELs/EDI/FL (%)				
B.1.15.20.2	O-11	EELs/TAG/FL (%)				
B.1.15.21.1	O-11	Line Splitting/EDI/FL (%)				
B.1.15.21.2	O-11	Line Splitting/TAG/FL (%)				
		FOC & Reject Response Completeness - Non-Mechanized				
B.1.16.1	O-11	Switch Ports/FL (%)				YES
B.1.16.2	O-11	Local Interoffice Transport/FL (%)				YES
B.1.16.3	O-11	Loop + Port Combinations/FL (%)				NO
B.1.16.4	O-11	Combo (other)/FL (%)				NO
B.1.16.5	O-11	xDSL (ADSL, HDSL and UCL)/FL (%)				NO
B.1.16.6	O-11	ISDN Loop (UDN, UDC)/FL (%)				YES
B.1.16.7	O-11	Line Sharing/FL (%)				YES
B.1.16.8	O-11	2W Analog Loop Design/FL (%)				NO
B.1.16.9	O-11	2W Analog Loop Non-Design/FL (%)				NO
B.1.16.10	O-11	2W Analog Loop w/INP Design/FL (%)				NO
B.1.16.11	O-11	2W Analog Loop w/INP Non-Design/FL (%)				YES
B.1.16.12	O-11	2W Analog Loop w/LNP Design/FL (%)				NO
B.1.16.13	O-11	2W Analog Loop w/LNP Non-Design/FL (%)				YES
B.1.16.14	O-11	Other Design/FL (%)				YES
B.1.16.15	O-11	Other Non-Design/FL (%)				NO
B.1.16.16	O-11	INP Standalone/FL (%)				NO
B.1.16.17	O-11	LNP Standalone/FL (%)				NO
B.1.16.18	O-11	Digital Loop < DS1/FL (%)				

BellSouth Monthly Performance Summary

Florida Permanent, September 2002 - May 2002

			Benchmark/ Analog	September BST Measure	September BST Volume	September CLEC Measure	September CLEC Volume	September Standard Deviation	September Standard Error	September Zscore	September Equity	August BST Measure	August BST Volume	August CLEC Measure	August CLEC Volume	August Standard Deviation	August Standard Error	August Zscore	August Equity
B.1.16.19	O-11	Digital Loop >= DS1/FL (%)	>= 95%			95.34%	237				YES			97.76%	402				YES
B.1.16.20	O-11	EELs/FL (%)	>= 95%			97.20%	107				YES			96.49%	171				YES
B.1.16.21	O-11	Line Splitting/FL (%)	>= 95%																

Unbundled Network Elements - Provisioning

Order Completion Interval

			Benchmark/ Analog	September BST Measure	September BST Volume	September CLEC Measure	September CLEC Volume	September Standard Deviation	September Standard Error	September Zscore	September Equity	August BST Measure	August BST Volume	August CLEC Measure	August CLEC Volume	August Standard Deviation	August Standard Error	August Zscore	August Equity
B.2.1.1.1.1	P-4	Switch Ports/<10 circuits/Dispatch/FL (days)	R&B (POTS)	5.01	45,657			6.229				4.54	59,577			5.447			
B.2.1.1.1.2	P-4	Switch Ports/<10 circuits/Non-Dispatch/FL (days)	R&B (POTS)	1.03	646,687			1.737				0.86	717,719			1.285			
B.2.1.1.2.1	P-4	Switch Ports/>=10 circuits/Dispatch/FL (days)	R&B (POTS)	11.33	330			21.626				11.42	318			18.832			
B.2.1.1.2.2	P-4	Switch Ports/>=10 circuits/Non-Dispatch/FL (days)	R&B (POTS)	0.83	10			1.147				1.00	22			1.392			
B.2.1.2.1.1	P-4	Local Interoffice Transport/<10 circuits/Dispatch/FL (days)	DS1/ DS3 - Interoffice	16.96	1,937	14.71	17	14.667	3.58009	0.6305	YES	15.33	2,385	21.00	4	12.923	6.46060	-0.8764	YES
B.2.1.2.1.2	P-4	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL (days)	DS1/ DS3 - Interoffice																
B.2.1.2.2.1	P-4	Local Interoffice Transport/>=10 circuits/Dispatch/FL (days)	DS1/ DS3 - Interoffice																
B.2.1.2.2.2	P-4	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL (days)	DS1/ DS3 - Interoffice																
B.2.1.3.1.1	P-4	Loop + Port Combinations/<10 circuits/Dispatch/FL (days)	R&B	5.00	46,659	4.18	1,139	6.260	0.18775	4.3583	YES	4.50	61,633	3.97	1,835	5.586	0.13996	3.7951	YES
B.2.1.3.1.2	P-4	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL (days)	R&B	1.04	650,009	0.93	24,803	1.767	0.01143	9.8103	YES	0.87	720,347	0.79	47,428	1.398	0.00663	12.7488	YES
B.2.1.3.2.1	P-4	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL (days)	R&B	0.33	357,838	0.33	10,707	0.000	0.00000		YES	0.33	400,833	0.33	23,517	0.000	0.00000		YES
B.2.1.3.2.2	P-4	Loop + Port Combinations/>=10 circuits/Dispatch In/FL (days)	R&B	1.90	292,171	1.38	14,066	2.365	0.02039	25.7742	YES	1.55	319,414	1.23	25,911	1.694	0.01270	24.7006	YES
B.2.1.3.4.1	P-4	Loop + Port Combinations/>=10 circuits/Dispatch/FL (days)	R&B	11.16	367	3.33	9	20.784	7.01238	1.1155	YES	11.32	369	3.37	9	17.966	6.06787	1.3098	YES
B.2.1.3.2.1	P-4	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL (days)	R&B	3.89	169			11.897				2.87	285	6.50	2	4.627	3.28337	-1.1968	YES
B.2.1.3.2.2	P-4	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL (days)	R&B	0.33	67			0.000				0.33	118			0.000			
B.2.1.3.2.3	P-4	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL (days)	R&B	5.95	107			14.582				4.27	169	6.50	2	5.453	3.88557	-0.5734	YES
B.2.1.3.2.4	P-4	Loop + Port Combinations/>=10 circuits/Dispatch In/FL (days)	R&B	5.92	49,240			9.263				5.38	64,325			8.668			
B.2.1.4.1.1	P-4	Combo (other)/<10 circuits/Dispatch/FL (days)	R&B&D - Disp	5.92	49,240			9.263				5.38	64,325			8.668			
B.2.1.4.1.4	P-4	Combo (other)/<10 circuits/Dispatch In/FL (days)	R&B&D - Disp	18.49	676			23.926				16.54	636			18.275			
B.2.1.4.2.1	P-4	Combo (other)/>=10 circuits/Dispatch/FL (days)	R&B&D - Disp	18.49	676			23.926				16.54	636			18.275			
B.2.1.4.2.4	P-4	Combo (other)/>=10 circuits/Dispatch In/FL (days)	ISDN - BRI	13.15	256	10.62	185	11.740	1.11587	2.2713	YES	13.84	248	10.69	205	9.525	0.90076	3.2770	YES
B.2.1.5.3.1	P-4	UNE ISDN/<5 circuits/Dispatch/FL (days)	ISDN - BRI	3.49	346			3.686				4.06	338			6.323			
B.2.1.5.3.2	P-4	UNE ISDN/<5 circuits/Non-Dispatch/FL (days)	ISDN - BRI	11.00	2			0.000				12.00	1			0.000			
B.2.1.5.4.1	P-4	UNE ISDN/5-13 circuits/Dispatch/FL (days)	ISDN - BRI	1.76	3			1.347				2.00	2			1.414			
B.2.1.5.4.2	P-4	UNE ISDN/5-13 circuits/Non-Dispatch/FL (days)	ISDN - BRI																
B.2.1.5.5.1	P-4	UNE ISDN/>=14 circuits/Dispatch/FL (days)	ISDN - BRI																
B.2.1.5.5.2	P-4	UNE ISDN/>=14 circuits/Non-Dispatch/FL (days)	ISDN - BRI																
B.2.1.6.3.1	P-4	Line Sharing/<5 circuits/Dispatch/FL (days)	ADSL to Retail	2.73	12,471	3.54	41	2.270	0.35502	-2.2692	NO	2.58	9,474	3.45	22	2.017	0.43046	-2.0360	NO
B.2.1.6.3.2	P-4	Line Sharing/<5 circuits/Non-Dispatch/FL (days)	ADSL to Retail	2.38	7,796	2.20	215	0.859	0.05938	3.0072	YES	2.20	6,263	2.16	135	0.778	0.06789	0.6347	YES
B.2.1.6.4.1	P-4	Line Sharing/5-13 circuits/Dispatch/FL (days)	ADSL to Retail	2.21	14			0.609				2.07	15			0.704			
B.2.1.6.4.2	P-4	Line Sharing/5-13 circuits/Non-Dispatch/FL (days)	ADSL to Retail																
B.2.1.6.5.1	P-4	Line Sharing/>=14 circuits/Dispatch/FL (days)	ADSL to Retail	3.00	2			1.414				3.00	1			0.000			
B.2.1.6.5.2	P-4	Line Sharing/>=14 circuits/Non-Dispatch/FL (days)	ADSL to Retail																
B.2.1.7.1.1	P-4	2W Analog Loop Design/<10 circuits/Dispatch/FL (days)	R&B - Disp	5.00	46,659	4.21	154	6.260	0.50331	1.5462	YES	4.50	61,633	4.58	202	5.586	0.39370	-0.2069	YES
B.2.1.7.1.2	P-4	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL (days)	R&B - Disp	5.00	46,659			6.260				4.50	61,633			5.586			
B.2.1.7.2.1	P-4	2W Analog Loop Design/>=10 circuits/Dispatch/FL (days)	R&B - Disp	11.16	367	5.67	3	20.784	12.04852	0.4555	YES	11.32	369	6.00	2	17.966	12.75215	0.4170	YES
B.2.1.7.2.2	P-4	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL (days)	R&B - Disp	11.16	367			20.784				11.32	369			17.966			
B.2.1.8.1.1	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL (days)	R&B (POTS) excl SB Or	5.01	45,657	3.76	609	6.229	0.25408	4.9315	YES	4.54	59,577	3.75	534	5.447	0.23677	3.3597	YES
B.2.1.8.1.4	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL (days)	R&B (POTS) excl SB Or	1.89	289,599	4.22	9	2.325	0.77511	-3.0144	NO	1.52	317,662	3.50	4	1.716	0.85777	-2.3060	NO
B.2.1.8.2.1	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL (days)	R&B (POTS) excl SB Or	11.33	330	5.90	10	21.626	6.94169	0.7817	YES	11.42	318	7.92	12	18.832	5.53789	0.6321	YES
B.2.1.8.2.4	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL (days)	R&B (POTS) excl SB Or	2.00	3			1.732				2.43	7	14.00	1	1.813	1.93781	-5.9714	NO
B.2.1.9.1.1	P-4	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL (days)	R&B - Disp	5.00	46,659			6.260				4.50	61,633			5.586			
B.2.1.9.1.2	P-4	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL (days)	R&B - Disp	5.00	46,659			6.260				4.50	61,633			5.586			
B.2.1.9.2.1	P-4	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL (days)	R&B - Disp	11.16	367			20.784				11.32	369			17.966			
B.2.1.9.2.2	P-4	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL (days)	R&B - Disp	11.16	367			20.784				11.32	369			17.966			
B.2.1.10.1.1	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL (days)	R&B (POTS) excl SB Or	5.01	45,657	4.50	2	6.229	4.40456	0.1156	YES	4.54	59,577	4.00	1	5.447	5.44713	0.0997	YES
B.2.1.10.1.4	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL (days)	R&B (POTS) excl SB Or	1.89	289,599			2.325				1.52	317,662			1.716			
B.2.1.10.2.1	P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL (days)	R&B (POTS) excl SB Or	11.33	330			21.626				11.42	318			18.832			
B.2.1.10.2.4	P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL (days)	R&B (POTS) excl SB Or	2.00	3			1.732				2.43	7			1.813			
B.2.1.11.1.1	P-4	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL (days)	R&B - Disp	5.00	46,659	4.58	34	6.260	1.07405	0.3793	YES	4.50	61,633	5.46	52	5.586	0.77501	-1.2372	YES
B.2.1.11.1.2	P-4	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL (days)	R&B - Disp	5.00	46,659			6.260				4.50	61,633			5.586			
B.2.1.11.2.1	P-4	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL (days)	R&B - Disp	11.16	367			20.784				11.32	369	9.00	1	17.966	18.00994	0.1267	YES
B.2.1.11.2.2	P-4	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL (days)	R&B - Disp	11.16	367			20.784				11.32	369			17.966			
B.2.1.12.1.1	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL (days)	R&B (POTS) excl SB Or	5.01	45,657	5.22	147	6.229	0.51457	-0.4026	YES	4.54	59,577	5.32	158	5.447	0.43392	-1.7984	NO
B.2.1.12.1.4	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL (days)	R&B (POTS) excl SB Or	1.89	289,599	5.20	189	2.325	0.16920	-19.5633	NO	1.52	317,662	5.27	136	1.716	0.14714	-25.4872	NO
B.2.1.12.2.1	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL (days)	R&B (POTS) excl SB Or	11.33	330	10.33	6	21.626	8.90882	0.1115	YES	11.42	318	8.31	13	18.832	5.32888	0.5835	YES
B.2.1.12.2.4	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL (days)	R&B (POTS) excl SB Or	2.00	3	7.50	4	1.732	1.32288	-4.1578	NO	2.43	7	7.25	8	1.813	0.93814	-5.1304	NO
B.2.1.13.1.1	P-4	Other Design/<10 circuits/Dispatch/FL (days)	Design	22.88	2,581	20.67	3	25.287	14.60798	0.1378	YES	25.42	2,692			25.726			
B.2.1.13.1.2	P-4	Other Design/<10 circuits/Non-Dispatch/FL (days)	Design	8.42	585			12.507				7.72	800			9.150			
B.2.1.13.2.1	P-4	Other Design/>=10 circuits/Dispatch/FL (days)	Design	27.19	309			24.520				23.76	267			16.111			
B.2.1.13.2.2	P-4	Other Design/>=10 circuits/Non-Dispatch/FL (days)	Design	20.44	39			26.147				45.87	45			36.060			
B.2.1.14.1.1	P-4	Other Non-Design/<10 circuits/Dispatch/FL (days)	R&B	5.00	46,659	12.00	1	6.260	6.26049	-1.1186	YES	4.50	61,633	6.00	1	5.586	5.56637	-0.2680	YES
B.2.1.14.1.2	P-4	Other Non-Design/<10 circuits/Non-Dispatch/FL (days)	R&B	1.04	650,009			1.767				0.87	720,347			1.398			
B.2.1.14.2.1	P-4	Other Non-Design/>=10 circuits/Dispatch/FL (days)	R&B	11.16	367			20.784				11.32	369			17.966			
B.2.1.14.2.2	P-4	Other Non-Design/>=10 circuits/Non-Dispatch/FL (days)	R&B	3.89	169			11.897				2.87	285			4.627			
B.2.1.15.1.1	P-4	INP (Standalone)/<10 circuits/Dispatch/FL (days)	R&B (POTS)	5.01	45,657			6.229				4.54	59,577			5.447			
B.2.1.15.1.2	P-4	INP (Standalone)/<10 circuits/Non-Dispatch/FL (days)	R&B (POTS)	1.03	646,687			1.737				0.86	717,719			1.285			
B.2.1.15.2.1	P-4	INP (Standalone)/>=10 circuits/Dispatch/FL (days)	R&B (POTS)	11.33	330			21.626				11.42	318			18.832			
B.2.1.15.2.2	P-4	INP (Standalone)/>=10 circuits/Non-Dispatch/FL (days)	R&B (POTS)	0.83	10			1.147				1.00	22			1.392			
B.2.1.16.1.1	P-4	LNP (Standalone)/<10 circuits/Dispatch/FL (days)	R&B (POTS)	5.01	45,657			6.229				4.54	59,577	0.33	3	5.447	3.14406	1.3386	YES
B.2.1.16.1.2	P-4	LNP (Standalone)/<10 circuits/Non-Dispatch/FL (days)	R&B (POTS)	1.03	646,687	0.85	2,844	1.737	0.03264	5.3348	YES	0.86	717,719	0.71	3,247	1.285	0.02260	6.3061	YES
B.2.1.16.2.1	P-4	LNP (Standalone)/>=10 circuits/Dispatch/FL (days)	R&B (POTS)	11.33	330			21.626				11.42	318			18.832			
B.2.1.16.2.2	P-4	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL (days)	R&B (POTS)	0.83	10	0.33	3	1.147	0.75482	0.6624	YES	1.00	22	0.89	6	1.392	0.64092	0.1734	YES
B.2.1.17.1.1	P-4	Digital Loop < DS1/<10 circuits/Dispatch/FL (days)	Digital Loop < DS1	3.17	13,000	7.99	335	3.948	0.21644	-22.0603	NO	3.27	10,295	8.24	343	4.510	0.24752	-20.0807	NO
B.2.1.17.1.2	P-4	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL (days)	Digital Loop < DS1	2.37	8,532			1.900				2.80	7,340			2.725			
B.2.1.17.2.1	P-4	Digital Loop < DS1/>=10 circuits/Dispatch/FL (days)	Digital Loop < DS1	2.33	15			0.816				1.96	16			0.806			
B.2.1.17.2.2	P-4	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL (days)	Digital Loop < DS1									2.00	1			0.000			
B.2.1.18.1.1	P-4	Digital Loop >= DS1/<10 circuits/Dispatch/FL (days)	Digital Loop >= DS1	13.70	704	6.55	215	18.814	1.46596	4.8755	YES	7.42	1,580	6.42	204	13.589	1.01170	0.9621	YES
B.2.1.18.1.2	P-4	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL (days)	Digital Loop >= DS1	3.50	1,151			9.251				4.06	862			7.913			
B.2.1.18.2.1	P-4	Digital Loop >= DS1/>=10 circuits/Dispatch/FL (days)	Digital Loop >= DS1	26.43	105			26.112				27.02	146			17.192			
B.2.1.18.2.2	P-4	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL (days)	Digital Loop >= DS1	19.58	77			20.335				23.77	87			30.833			
B.2.1.19.1.1	P-4	Line Splitting/<10 circuits/Dispatch/FL (days)	ADSL to Retail	2.73	12,486			2.270				2.58	9,491			2.016			
B.2.1.19.1.2	P-4	Line Splitting/<10 circuits/Non-Dispatch/FL (days)	ADSL to Retail	2.38	7,821			0.863				2.19	6,297			0.788			
B.2.1.19.2.1	P-4	Line Splitting/>=10 circuits/Dispatch/FL (days)	ADSL to Retail	2.33	15			0.816				1.96	16			0.806			
B.2.1.19.2.2	P-4	Line Splitting/>=10 circuits/Non-Dispatch/FL (days)	ADSL to Retail																
B.2.1.20.1.1	P-4	EELs/<10 circuits/Dispatch/FL (days)	DS1/ DS3 - Interoffice	16.96	1,937	12.21	106	14.667	1.46601	3.2439	YES	15.33	2,385	11.44	68	12.923	1.58027	2.4485	YES
B.2.1.20.1.2	P-4	EELs/<10 circuits/Non-Dispatch/FL (days)	DS1/ DS3 - Interoffice																
B.2.1.20.2.1	P-4	EELs/>=10 circuits/Dispatch/FL (days)	DS1/ DS3 - Interoffice																
B.2.1.20.2.2	P-4	EELs/>=10 circuits/Non-Dispatch/FL (days)	DS1/ DS3 - Interoffice																

Average Order Completion and Completion Notice Interval - Mechanized

			Benchmark/ Analog	September BST Measure	September BST Volume	September CLEC Measure	September CLEC Volume	September Standard Deviation	September Standard Error	September Zscore	September Equity	August BST Measure	August BST Volume	August CLEC Measure	August CLEC Volume	August Standard Deviation	August Standard Error	August Zscore	August Equity
B.2.2.1.1.1	P-4A	Switch Ports/<10 circuits/Dispatch/FL (days)	R&B (POTS)	4.89	39,306			3.704				4.52	52,892			3.579			
B.2.2.1.1.2	P-4A	Switch Ports/<10 circuits/Non-Dispatch/FL (days)	R&B (POTS)	1.24	637,063			1.620				1.06	706,517			1.253			
B.2.2.1.2.1	P-4A	Switch Ports/>=10 circuits/Dispatch/FL (days)	R&B (POTS)	5.61	218			3.937				7.30	214			9.189			
B.2.2.1.2.2	P-4A	Switch Ports/>=10 circuits/Non-Dispatch/FL (days)	R&B (POTS)	1.15	2			0.150				3.86	5			1.712			
B.2.2.2.1.1	P-4A	Local Interoffice Transport/<10 circuits/Dispatch/FL (days)	R&B (POTS)	16.49	784			9.240				14.05	1,190			7.216			
B.2.2.2.1.2	P-4A	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL (days)	DS1/ DS3 - Interoffice																
B.2.2.2.2.1	P-4A	Local Interoffice Transport/>=10 circuits/Dispatch/FL (days)	DS1/ DS3 - Interoffice																
			DS1/ DS3 - Interoffice																

BellSouth Monthly Performance Summary

Florida Permanent, September 2002 - May 2002

			July								June								May			
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.1.16.19	O-11	Digital Loop >= DS1/FL (%)			96.37%	358				YES			98.28%	408				YES			95.42%	590
B.1.16.20	O-11	EELs/FL (%)			94.32%	68				NO			96.55%	116				YES			96.55%	29
B.1.16.21	O-11	Line Splitting/FL (%)																				
		Unbundled Network Elements - Provisioning																				
		Order Completion Interval																				
B.2.1.1.1.1	P-4	Switch Ports/<10 circuits/Dispatch/FL (days)	5.31	52,380			6.264				4.81	47,314			5.932				4.01	68,137		
B.2.1.1.1.2	P-4	Switch Ports/<10 circuits/Non-Dispatch/FL (days)	0.87	681,545			1.274				0.88	643,741			1.367				0.89	674,661		
B.2.1.1.2.1	P-4	Switch Ports/>=10 circuits/Dispatch/FL (days)	10.18	331			11.619				9.89	313			13.253				9.03	344		
B.2.1.1.2.2	P-4	Switch Ports/>=10 circuits/Non-Dispatch/FL (days)	0.50	8			0.309				13.72	18			29.920				4.07	10		
B.2.1.2.1.1	P-4	Local Interoffice Transport/<10 circuits/Dispatch/FL (days)	14.80	2,079	14.50	6	12.523	5.11991	0.0585	YES	15.61	2,096	16.15	13	14.282	3.97328	-0.1374	YES	16.35	2,107	15.73	26
B.2.1.2.1.2	P-4	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL (days)																				
B.2.1.2.2.1	P-4	Local Interoffice Transport/>=10 circuits/Dispatch/FL (days)											18.00	1					13.00	1		
B.2.1.2.2.2	P-4	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL (days)																				
B.2.1.3.1.1	P-4	Loop + Port Combinations/<10 circuits/Dispatch/FL (days)	5.33	53,054	4.16	1,438	6.401	0.17107	6.8846	YES	4.82	47,879	3.62	1,273	5.949	0.16894	7.1478	YES	4.03	68,740	3.35	1,092
B.2.1.3.1.2	P-4	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL (days)	0.88	683,871	0.74	43,349	1.340	0.00663	21.5089	YES	0.89	645,770	0.69	34,055	1.384	0.00770	25.2536	YES	0.89	677,664	0.85	33,452
B.2.1.3.1.3	P-4	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL (days)	0.33	390,763	0.33	23,864	0.000	0.00000		YES	0.33	350,092	0.33	19,725	0.000	0.00000		YES	0.33	380,507	0.33	21,371
B.2.1.3.1.4	P-4	Loop + Port Combinations/<10 circuits/Dispatch In/FL (days)	1.61	293,108	1.23	19,485	1.805	0.01335	28.2193	YES	1.54	295,678	1.18	14,330	1.843	0.01577	22.5595	YES	1.61	297,157	1.20	12,081
B.2.1.3.2.1	P-4	Loop + Port Combinations/>=10 circuits/Dispatch/FL (days)	10.61	390	5.36	11	11.378	3.47859	1.5073	YES	11.41	377	4.20	10	15.675	5.02231	1.4365	YES	10.00	393	5.06	6
B.2.1.3.2.2	P-4	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL (days)	3.10	256	4.11	3	8.789	3.94223	-0.2578	YES	7.04	166	7.00	1	14.758	14.60239	0.0030	YES	3.38	259	0.44	6
B.2.1.3.2.3	P-4	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL (days)	0.33	53	0.33	1	0.000	0.00000		YES	0.33	34			0.000			YES	0.33	89	0.33	5
B.2.1.3.2.4	P-4	Loop + Port Combinations/>=10 circuits/Dispatch In/FL (days)	3.81	205	6.00	2	7.454	5.29663	-0.4135	YES	8.77	132	7.00	1	16.113	16.17387	0.1096	YES	4.98	170	1.00	1
B.2.1.4.1.1	P-4	Combo (other)/<10 circuits/Dispatch/FL (days)	6.31	56,075	16.50	2	9.347	6.60941	-1.5412	YES	5.79	50,613	12.00	1	8.822	8.82255	-0.7040	YES	4.72	71,511	12.00	1
B.2.1.4.1.4	P-4	Combo (other)/<10 circuits/Dispatch In/FL (days)	6.31	56,075			9.347				5.79	50,613			8.822				4.72	71,511		
B.2.1.4.2.1	P-4	Combo (other)/>=10 circuits/Dispatch/FL (days)	10.73	395			11.404				14.61	467			18.252				10.72	413		
B.2.1.4.2.4	P-4	Combo (other)/>=10 circuits/Dispatch In/FL (days)	10.73	395			11.404				14.61	467			18.252				10.72	413		
B.2.1.5.3.1	P-4	UNE ISDN/<6 circuits/Dispatch/FL (days)	14.88	318	9.92	226	16.813	1.46276	3.3864	YES	13.80	240	10.02	235	12.343	1.13271	3.3384	YES	11.88	246	10.70	419
B.2.1.5.3.2	P-4	UNE ISDN/<6 circuits/Non-Dispatch/FL (days)	3.01	227			6.920				2.14	239			4.510				3.34	338		
B.2.1.5.4.1	P-4	UNE ISDN/6-13 circuits/Dispatch/FL (days)			12.00	1																
B.2.1.5.4.2	P-4	UNE ISDN/6-13 circuits/Non-Dispatch/FL (days)	1.00	1			0.000				4.00	1			0.000				0.67	2		
B.2.1.5.5.1	P-4	UNE ISDN/>=14 circuits/Dispatch/FL (days)																				
B.2.1.5.5.2	P-4	UNE ISDN/>=14 circuits/Non-Dispatch/FL (days)																				
B.2.1.6.3.1	P-4	Line Sharing/<6 circuits/Dispatch/FL (days)	2.75	8,554	6.69	29	1.991	0.37042	-10.6489	NO	2.80	7,820	4.67	31	3.056	0.55002	-3.7560	NO	3.77	6,742	6.30	46
B.2.1.6.3.2	P-4	Line Sharing/<6 circuits/Non-Dispatch/FL (days)	2.33	5,651	3.65	108	0.684	0.06448	-20.4845	NO	2.32	5,649	3.77	125	1.038	0.09391	-15.4542	NO	3.49	4,577	3.81	129
B.2.1.6.4.1	P-4	Line Sharing/6-13 circuits/Dispatch/FL (days)	3.00	7			1.291				2.31	13			1.316				8.43	14		
B.2.1.6.4.2	P-4	Line Sharing/6-13 circuits/Non-Dispatch/FL (days)																	4.00	1		
B.2.1.6.5.1	P-4	Line Sharing/>=14 circuits/Dispatch/FL (days)																				
B.2.1.6.5.2	P-4	Line Sharing/>=14 circuits/Non-Dispatch/FL (days)																	8.00	1		
B.2.1.7.1.1	P-4	2W Analog Loop Design/<10 circuits/Dispatch/FL (days)	5.33	53,054	4.83	255	6.401	0.40181	1.7534	YES	4.82	47,879	4.52	250	5.949	0.37067	0.8063	YES	4.03	68,740	4.83	232
B.2.1.7.1.2	P-4	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL (days)	5.33	53,054			6.401				4.82	47,879			5.949				4.03	68,740		
B.2.1.7.2.1	P-4	2W Analog Loop Design/>=10 circuits/Dispatch/FL (days)	10.61	390	7.00	1	11.378	11.39240	0.3166	YES	11.41	377	6.50	2	15.675	11.11354	0.4422	YES	10.00	393	8.00	1
B.2.1.7.2.2	P-4	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL (days)	10.61	390			11.378				11.41	377			15.675				10.00	393		
B.2.1.8.1.1	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL (days)	5.31	52,380	4.29	567	6.264	0.26449	3.8314	YES	4.81	47,314	3.73	629	5.932	0.23808	4.5617	YES	4.01	68,137	3.64	751
B.2.1.8.1.4	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL (days)	1.59	291,522	3.64	14	1.698	0.45381	-4.5151	NO	1.53	294,261	3.11	28	1.819	0.34372	-4.5820	NO	1.61	295,055	3.33	18
B.2.1.8.2.1	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL (days)	10.18	331	8.08	12	11.819	3.41438	0.6139	YES	9.89	313	6.14	14	13.253	3.62028	1.0357	YES	9.03	344	4.55	11
B.2.1.8.2.4	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL (days)	1.00	2	2.00	1	0.000	0.00000		NO	16.40	15			32.264				5.00	8		
B.2.1.9.1.1	P-4	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL (days)	5.33	53,054	3.00	1	6.401	6.40107	0.3645	YES	4.82	47,879			5.949				4.03	68,740	5.00	1
B.2.1.9.1.2	P-4	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL (days)	5.33	53,054			6.401				4.82	47,879			5.949				4.03	68,740		
B.2.1.9.2.1	P-4	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL (days)	10.61	390			11.378				11.41	377			15.675				10.00	393		
B.2.1.9.2.2	P-4	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL (days)	10.61	390			11.378				11.41	377			15.675				10.00	393		
B.2.1.10.1.1	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL (days)	5.31	52,380			6.264				4.81	47,314	3.00	1	5.932	5.93167	0.3056	YES	4.01	68,137		
B.2.1.10.1.4	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL (days)	1.59	291,522	4.00	1	1.698	1.69797	-1.4171	YES	1.53	294,261			1.819				1.61	295,055		
B.2.1.10.2.1	P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL (days)	10.18	331			11.819				9.89	313			13.253				9.03	344		
B.2.1.10.2.4	P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL (days)	1.00	2			0.000				16.40	15			32.264				5.00	8		
B.2.1.11.1.1	P-4	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL (days)	5.33	53,054	5.42	67	6.401	0.78250	-0.1081	YES	4.82	47,879	5.59	91	5.949	0.82424	-1.2347	YES	4.03	68,740	5.57	188
B.2.1.11.1.2	P-4	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL (days)	5.33	53,054			6.401				4.82	47,879			5.949				4.03	68,740		
B.2.1.11.2.1	P-4	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL (days)	10.61	390	6.33	3	11.378	6.59421	0.6481	YES	11.41	377	6.50	4	15.675	7.87917	0.8238	YES	10.00	393	8.38	8
B.2.1.11.2.2	P-4	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL (days)	10.61	390			11.378				11.41	377			15.675				10.00	393		
B.2.1.12.1.1	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL (days)	5.31	52,380	5.16	243	6.264	0.40278	0.3714	YES	4.81	47,314	4.94	306	5.932	0.34018	-0.3780	YES	4.01	68,137	4.98	583
B.2.1.12.1.4	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL (days)	1.59	291,522	5.30	115	1.698	0.15837	-23.4297	NO	1.53	294,261	5.18	172	1.819	0.13872	-26.1726	NO	1.61	295,055	4.96	260
B.2.1.12.2.1	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL (days)	10.18	331	7.67	12	11.819	3.41438	0.7359	YES	9.89	313	7.35	17	13.253	3.30039	0.7695	YES	9.03	344	7.74	23
B.2.1.12.2.4	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL (days)	1.00	2	7.36	11	0.000	0.00000		NO	16.40	15	7.00	3	32.264	20.40560	0.4607	YES	5.00	8	7.00	6
B.2.1.13.1.1	P-4	Other Design/<10 circuits/Dispatch/FL (days)	23.53	3,021	17.00	1	24.272	24.27609	0.2688	YES	22.71	2,734			22.775				21.96	2,771		
B.2.1.13.1.2	P-4	Other Design/<10 circuits/Non-Dispatch/FL (days)	10.68	646			13.095				9.33	553			11.743				11.37	781		
B.2.1.13.2.1	P-4	Other Design/>=10 circuits/Dispatch/FL (days)	20.60	5			9.813				28.00	90			21.948				24.95	20		
B.2.1.13.2.2	P-4	Other Design/>=10 circuits/Non-Dispatch/FL (days)	10.00	3			10.817				30.25	4			38.413				32.33	3		
B.2.1.14.1.1	P-4	Other Non-Design/<10 circuits/Dispatch/FL (days)	5.33	53,054	10.57	7	6.401	2.41951	-2.1650	NO									4.03	68,740	7.25	53
B.2.1.14.1.2	P-4	Other Non-Design/<10 circuits/Non-Dispatch/FL (days)	0.88	683,871			1.340												0.89	677,664		
B.2.1.14.2.1	P-4	Other Non-Design/>=10 circuits/Dispatch/FL (days)	10.61	390			11.378												10.00	393		
B.2.1.14.2.2	P-4	Other Non-Design/>=10 circuits/Non-Dispatch/FL (days)	3.10	258			6.789												3.38	259		
B.2.1.15.1.1	P-4	INP (Standalone)/<10 circuits/Dispatch/FL (days)	5.31	52,380			6.264				4.81	47,314			5.932				4.01	68,137		
B.2.1.15.1.2	P-4	INP (Standalone)/<10 circuits/Non-Dispatch/FL (days)	0.87	681,545			1.274				0.88	643,741			1.367				0.89	674,661		
B.2.1.15.2.1	P-4	INP (Standalone)/>=10 circuits/Dispatch/FL (days)	10.18	331			11.619				9.89	313			13.253				9.03	344		
B.2.1.15.2.2	P-4	INP (Standalone)/>=10 circuits/Non-Dispatch/FL (days)	0.50	8			0.309				13.72	18			29.920				4.07	10		
B.2.1.16.1.1	P-4	LNP (Standalone)/<10 circuits/Dispatch/FL (days)	5.31	52,380	0.33	5	6.264	2.80159	1.7754	YES	4.81	47,314	0.33	12	5.932	1.71252	2.6156	YES	4.01	68,137	5.00	1
B.2.1.16.1.2	P-4	LNP (Standalone)/<10 circuits/Non-Dispatch/FL (days)	0.87	681,545	0.72	3,039	1.274	0.02315	6.7767	YES	0.88	643,741	0.77	2,743	1.367	0.02618	4.1934	YES	0.89	674,661	0.77	3,347
B.2.1.16.2.1	P-4	LNP (Standalone)/>=10 circuits/Dispatch/FL (days)	10.18	331			11.619				9.89	313			13.253				9.03	344		
B.2.1.16.2.2	P-4	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL (days)	0.50	8	0.33	13	0.309	0.13868	1.2019	YES	13.72	18	0.57	7	29.920	13.32762	0.9867	YES	4.07	10	0.33	7
B.2.1.17.1.1	P-4	Digital Loop < DS1/<10 circuits/Dispatch/FL (days)	3.58	9,199	7.77	380	5.211	0.27278	-15.3698	NO	3.89	6,431	7.64	422	6.546	0.32652	-12.0953	NO	4.77	7,466	8.69	593
B.2.1.17.1.2	P-4	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL (days)	2.68	6,557			2.526				2.72	6,431			2.930				4.26	5,689		
B.2.1.17.2.1	P-4	Digital Loop < DS1/>=10 circuits/Dispatch/FL (days)	2.83	6	12.00	1	1.329	1.43566	-6.3850	NO	2.31	13	4.00	1	1.316	1.36525	-1.2398	YES	7.50	12		
B.2.1.17.2.2	P-4	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL (days)									2.00	1			0.000				8.00	1		
B.2.1.18.1.1	P-4	Digital Loop >= DS1/<10 circuits/Dispatch/FL (days)	21.80	660	6.70	226	25.596	1.07285	7.6506	YES	20.86	362	6.00	201	30.789	2.70626	5.4698	YES	28.05	315	6.29	280
B.2.1.18.1.2	P-4	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL (days)	4.76	699			10.707				2.90	524			5.008				1.99	1,178		
B.2.1.18.2.1	P-4	Digital Loop >= DS1/>=10 circuits/Dispatch/FL (days)	23.75	4			7.890				13.33	9	9.00	1	9.206	9.70395	0.4466	YES	11.50	2		
B.2.1.18.2.2	P-4	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL (days)	4.64	92			6.113				9.55	67			9.899				3.20	57		
B.2.1.19.1.1	P-4	Line Splitting/<10 circuits/Dispatch/FL (days)	2.74	8,572			1.991				2.80	7,839			3.058				3.77	6,792		
B.2.1.19.1.2	P-4	Line Splitting/<10 circuits/Non-Dispatch/FL (days)	2.32	5,879			0.676				2.31	3,676			1.044				3.47	4,626		
B.2.1.19.2.1	P-4	Line Splitting/>=10 circuits/Dispatch/FL (days)	2.83	6			1.329				2.31	13			1.316				7.50	12		
B.2.1.19.2.2	P-4	Line Splitting/>=10 circuits/Non-Dispatch/FL (days)																	8.00	1		
B.2.1.20.1.1	P-4	EELs/<10 circuits/Dispatch/FL (days)	14.80	2,079	11.95	63	12.523	1.60149	1.7777	YES	15.61	2,096	11.56	82	14.282	1.60770	2.5172	YES	16.35	2,107	11.97	121
B.2.1.20.1.2	P-4	EELs/<10 circuits/Non-Dispatch/FL (days)																				
B.2.1.20.2.1	P-4	EELs/>=10 circuits/Dispatch/FL (days)																	13.00	1		
B.2.1.20.2.2	P-4	EELs/>=10 circuits/Non-Dispatch/FL (days)																				
		Average Order Completion and Completion Notice Interval - Mechanized																				
B.2.2.1.1.1	P-4A	Switch Ports/<10 circuits/Dispatch/FL (days)	5.12	45,482			3.571				4.61	40,348			3.309				3.74	60,094		
B.2.2.1.1.2	P-4A	Switch Ports/<10 circuits/Non-Dispatch/FL (days)	1.09	671,638			1.285				1.13	634,543			1.252				1.09	664,487		
B.2.2.1.2.1	P-4A	Switch Ports/>=10 circuits/Dispatch/FL (days)	7.36	228			7.126				6.79	221			6.097				7.13	231		
B.2.2.1.2.2	P-4A	Switch Ports/>=10 circuits/Non-Dispatch/FL (days)	1.36	7			2.371				3.58	15			3.155				2.79	8		
B.2.2.2.1.1	P-4A	Local Interoffice Transport/<10 circuits/Dispatch/FL (days)	13.01	1,089			7.246				14.07	1,039			9.095				16.32	843		
B.2.2.2.1.2	P-4A	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL (days)																	14.20	1		
B.2.2.2.2.1	P-4A	Local Interoffice Transport/>=10 circuits/Dispatch/FL (days)																				

BellSouth Monthly Performance Summary

Florida Permanent, September 2002 - May 2002

			Standard Deviation	Standard Error	Zscore	Equity
B.1.16.19	[illegible]	[illegible]	[redacted]			YES
B.1.16.20	O-11	EELs/FL (%)	[redacted]			YES
B.1.16.21	O-11	Line Splitting/FL (%)	[redacted]			

Unbundled Network Elements - Provisioning

Order Completion Interval

B.2.1.1.1.1	P-4	Switch Ports/<10 circuits/Dispatch/FL (days)	5.992			
B.2.1.1.1.2	P-4	Switch Ports/<10 circuits/Non-Dispatch/FL (days)	1.551			
B.2.1.1.2.1	P-4	Switch Ports/>=10 circuits/Dispatch/FL (days)	14.906			
B.2.1.1.2.2	P-4	Switch Ports/>=10 circuits/Non-Dispatch/FL (days)	4.863			
B.2.1.2.1.1	P-4	Local Interoffice Transport/<10 circuits/Dispatch/FL (days)	14.725	2.90555	0.2117	YES
B.2.1.2.1.2	P-4	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL (days)				
B.2.1.2.2.1	P-4	Local Interoffice Transport/>=10 circuits/Dispatch/FL (days)	0.000			
B.2.1.2.2.2	P-4	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL (days)				
B.2.1.3.1.1	P-4	Loop + Port Combinations/<10 circuits/Dispatch/FL (days)	6.013	0.18341	3.7211	YES
B.2.1.3.1.2	P-4	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL (days)	1.590	0.00891	27.7361	YES
B.2.1.3.1.3	P-4	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL (days)	0.000	0.00000		YES
B.2.1.3.1.4	P-4	Loop + Port Combinations/<10 circuits/Dispatch In/FL (days)	2.202	0.02044	20.0727	YES
B.2.1.3.2.1	P-4	Loop + Port Combinations/>=10 circuits/Dispatch/FL (days)	17.233	7.08890	0.6074	YES
B.2.1.3.2.2	P-4	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL (days)	6.599	2.72511	1.0790	YES
B.2.1.3.2.3	P-4	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL (days)	0.000	0.00000		YES
B.2.1.3.2.4	P-4	Loop + Port Combinations/>=10 circuits/Dispatch In/FL (days)	7.662	7.70446	0.5169	YES
B.2.1.4.1.1	P-4	Combo (other)/<10 circuits/Dispatch/FL (days)	6.396	6.39568	-0.8668	YES
B.2.1.4.1.4	P-4	Combo (other)/<10 circuits/Dispatch In/FL (days)	8.396			
B.2.1.4.2.1	P-4	Combo (other)/>=10 circuits/Dispatch/FL (days)	17.268			
B.2.1.4.2.4	P-4	Combo (other)/>=10 circuits/Dispatch In/FL (days)	17.268			
B.2.1.5.3.1	P-4	UNE ISDN/<6 circuits/Dispatch/FL (days)	8.550	0.68676	1.7222	YES
B.2.1.5.3.2	P-4	UNE ISDN/<6 circuits/Non-Dispatch/FL (days)	6.186			
B.2.1.5.4.1	P-4	UNE ISDN/6-13 circuits/Dispatch/FL (days)				
B.2.1.5.4.2	P-4	UNE ISDN/6-13 circuits/Non-Dispatch/FL (days)	0.471			
B.2.1.5.5.1	P-4	UNE ISDN/>=14 circuits/Dispatch/FL (days)				
B.2.1.5.5.2	P-4	UNE ISDN/>=14 circuits/Non-Dispatch/FL (days)				
B.2.1.6.3.1	P-4	Line Sharing/<6 circuits/Dispatch/FL (days)	2.628	0.38881	-6.5111	NO
B.2.1.6.3.2	P-4	Line Sharing/<6 circuits/Non-Dispatch/FL (days)	1.006	0.08984	-3.4671	NO
B.2.1.6.4.1	P-4	Line Sharing/6-13 circuits/Dispatch/FL (days)	10.647			
B.2.1.6.4.2	P-4	Line Sharing/6-13 circuits/Non-Dispatch/FL (days)	0.000			
B.2.1.6.5.1	P-4	Line Sharing/>=14 circuits/Dispatch/FL (days)				
B.2.1.6.5.2	P-4	Line Sharing/>=14 circuits/Non-Dispatch/FL (days)	0.000			
B.2.1.7.1.1	P-4	2W Analog Loop Design/<10 circuits/Dispatch/FL (days)	6.013	0.39546	-2.0400	NO
B.2.1.7.1.2	P-4	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL (days)	6.013			
B.2.1.7.2.1	P-4	2W Analog Loop Design/>=10 circuits/Dispatch/FL (days)	17.233	17.25505	0.1159	YES
B.2.1.7.2.2	P-4	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL (days)	17.233			
B.2.1.8.1.1	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL (days)	5.992	0.21966	1.8654	YES
B.2.1.8.1.4	P-4	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL (days)	2.143	0.50503	-3.4187	NO
B.2.1.8.2.1	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL (days)	14.906	4.56555	0.9825	YES
B.2.1.8.2.4	P-4	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL (days)	5.043			
B.2.1.9.1.1	P-4	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL (days)	6.013	6.01336	-0.1616	YES
B.2.1.9.1.2	P-4	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL (days)	6.013			
B.2.1.9.2.1	P-4	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL (days)	17.233			
B.2.1.9.2.2	P-4	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL (days)	17.233			
B.2.1.10.1.1	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL (days)	5.992			
B.2.1.10.1.4	P-4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL (days)	2.143			
B.2.1.10.2.1	P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL (days)	14.906			
B.2.1.10.2.4	P-4	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL (days)	5.043			
B.2.1.11.1.1	P-4	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL (days)	6.013	0.43917	-3.5213	NO
B.2.1.11.1.2	P-4	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL (days)	6.013			
B.2.1.11.2.1	P-4	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL (days)	17.233	6.15454	0.2639	YES
B.2.1.11.2.2	P-4	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL (days)	17.233			
B.2.1.12.1.1	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL (days)	5.992	0.24923	-3.8784	NO
B.2.1.12.1.4	P-4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL (days)	2.143	0.13294	-25.2068	NO
B.2.1.12.2.1	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL (days)	14.906	3.21029	0.4024	YES
B.2.1.12.2.4	P-4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL (days)	5.043	2.72336	-0.7344	YES
B.2.1.13.1.1	P-4	Other Design/<10 circuits/Dispatch/FL (days)	24.759			
B.2.1.13.1.2	P-4	Other Design/<10 circuits/Non-Dispatch/FL (days)	15.820			
B.2.1.13.2.1	P-4	Other Design/>=10 circuits/Dispatch/FL (days)	10.724			
B.2.1.13.2.2	P-4	Other Design/>=10 circuits/Non-Dispatch/FL (days)	48.211			
B.2.1.14.1.1	P-4	Other Non-Design/<10 circuits/Dispatch/FL (days)	6.013	0.82631	-3.8935	NO
B.2.1.14.1.2	P-4	Other Non-Design/<10 circuits/Non-Dispatch/FL (days)	1.590			
B.2.1.14.2.1	P-4	Other Non-Design/>=10 circuits/Dispatch/FL (days)	17.233			
B.2.1.14.2.2	P-4	Other Non-Design/>=10 circuits/Non-Dispatch/FL (days)	6.599			
B.2.1.15.1.1	P-4	INP (Standalone)/<10 circuits/Dispatch/FL (days)	5.992			
B.2.1.15.1.2	P-4	INP (Standalone)/<10 circuits/Non-Dispatch/FL (days)	1.551			
B.2.1.15.2.1	P-4	INP (Standalone)/>=10 circuits/Dispatch/FL (days)	14.906			
B.2.1.15.2.2	P-4	INP (Standalone)/>=10 circuits/Non-Dispatch/FL (days)	4.863			
B.2.1.16.1.1	P-4	LNP (Standalone)/<10 circuits/Dispatch/FL (days)	5.992	5.99226	-0.1650	YES
B.2.1.16.1.2	P-4	LNP (Standalone)/<10 circuits/Non-Dispatch/FL (days)	1.551	0.02688	4.6283	YES
B.2.1.16.2.1	P-4	LNP (Standalone)/>=10 circuits/Dispatch/FL (days)	14.906			
B.2.1.16.2.2	P-4	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL (days)	4.863	2.39654	1.5578	YES
B.2.1.17.1.1	P-4	Digital Loop < DS1/<10 circuits/Dispatch/FL (days)	5.538	0.24055	-17.1047	NO
B.2.1.17.1.2	P-4	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL (days)	5.861			
B.2.1.17.2.1	P-4	Digital Loop < DS1/>=10 circuits/Dispatch/FL (days)	10.992			
B.2.1.17.2.2	P-4	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL (days)	0.000			
B.2.1.18.1.1	P-4	Digital Loop >= DS1/<10 circuits/Dispatch/FL (days)	41.790	3.50160	6.2132	YES
B.2.1.18.1.2	P-4	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL (days)	5.898			
B.2.1.18.2.1	P-4	Digital Loop >= DS1/>=10 circuits/Dispatch/FL (days)	2.121			
B.2.1.18.2.2	P-4	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL (days)	4.635			
B.2.1.19.1.1	P-4	Line Splitting/<10 circuits/Dispatch/FL (days)	2.642			
B.2.1.19.1.2	P-4	Line Splitting/<10 circuits/Non-Dispatch/FL (days)	1.092			
B.2.1.19.2.1	P-4	Line Splitting/>=10 circuits/Dispatch/FL (days)	10.992			
B.2.1.19.2.2	P-4	Line Splitting/>=10 circuits/Non-Dispatch/FL (days)	0.000			
B.2.1.20.1.1	P-4	EELs/<10 circuits/Dispatch/FL (days)	14.726	1.37653	3.1811	YES
B.2.1.20.1.2	P-4	EELs/<10 circuits/Non-Dispatch/FL (days)				
B.2.1.20.2.1	P-4	EELs/>=10 circuits/Dispatch/FL (days)	0.000			
B.2.1.20.2.2	P-4	EELs/>=10 circuits/Non-Dispatch/FL (days)				

Average Order Completion and Completion Notice Interval - Mechanized

B.2.2.1.1.1	P-4A	Switch Ports/<10 circuits/Dispatch/FL (days)	3.201			
B.2.2.1.1.2	P-4A	Switch Ports/<10 circuits/Non-Dispatch/FL (days)	1.406			
B.2.2.1.2.1	P-4A	Switch Ports/>=10 circuits/Dispatch/FL (days)	8.919			
B.2.2.1.2.2	P-4A	Switch Ports/>=10 circuits/Non-Dispatch/FL (days)	3.928			
B.2.2.2.1.1	P-4A	Local Interoffice Transport/<10 circuits/Dispatch/FL (days)	10.855			
B.2.2.2.1.2	P-4A	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL (days)				
B.2.2.2.2.1	P-4A	Local Interoffice Transport/>=10 circuits/Dispatch/FL (days)	0.000			

BellSouth Monthly Performance Summary

Florida Permanent, September 2002 - May 2002

			Benchmark/ Analog	September BST Measure	September BST Volume	September CLEC Measure	September CLEC Volume	September Standard Deviation	September Standard Error	September Zscore	September Equity	August BST Measure	August BST Volume	August CLEC Measure	August CLEC Volume	August Standard Deviation	August Standard Error	August Zscore	August Equity
8.2.2.2.2.2	P-4A	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL (days)	DS1/ DS3 - Interoffice																
8.2.2.3.1.1	P-4A	Loop + Port Combinations/<10 circuits/Dispatch/FL (days)	R&B	4.87	40,174	4.73	828	3.719	0.13056	1.0824	YES	4.49	54,797	4.66	1,128	3.561	0.10712	-1.6004	YES
8.2.2.3.1.2	P-4A	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL (days)	R&B	1.25	640,313	1.16	23,839	1.646	0.01085	8.4951	YES	1.09	711,040	1.02	45,483	1.348	0.00552	9.6620	YES
8.2.2.3.1.3	P-4A	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL (days)	R&B	0.43	353,307	0.38	10,034	0.442	0.00448	12.2853	YES	0.44	367,464	0.38	22,062	0.479	0.00332	18.8452	YES
8.2.2.3.1.4	P-4A	Loop + Port Combinations/<10 circuits/Dispatch In/FL (days)	R&B	2.26	267,006	1.73	13,805	1.989	0.01733	30.7292	YES	1.90	313,576	1.63	23,411	1.625	0.01101	24.8114	YES
8.2.2.3.2.1	P-4A	Loop + Port Combinations/>=10 circuits/Dispatch/FL (days)	R&B	5.87	241	3.31	3	4.175	2.42521	1.0557	YES	7.38	241	3.73	6	8.920	3.68655	0.9894	YES
8.2.2.3.2.2	P-4A	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL (days)	R&B	2.63	148			5.217				2.42	250			3.845			
8.2.2.3.2.3	P-4A	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL (days)	R&B	0.54	53			0.697				0.53	99			0.917			
8.2.2.3.2.4	P-4A	Loop + Port Combinations/>=10 circuits/Dispatch In/FL (days)	R&B	3.80	95			6.201				3.66	151			4.196			
8.2.2.4.1.1	P-4A	Combo (other)/<10 circuits/Dispatch/FL (days)	R&B&D - Disp	5.19	41,365			4.398				4.71	55,873			4.005			
8.2.2.4.1.4	P-4A	Combo (other)/<10 circuits/Dispatch In/FL (days)	R&B&D - Disp	5.19	41,365			4.398				4.71	55,873			4.005			
8.2.2.4.2.1	P-4A	Combo (other)/>=10 circuits/Dispatch/FL (days)	R&B&D - Disp	9.45	317			11.661				9.20	289			8.601			
8.2.2.4.2.4	P-4A	Combo (other)/>=10 circuits/Dispatch In/FL (days)	R&B&D - Disp	9.45	317			11.661				9.20	289			9.601			
8.2.2.5.3.1	P-4A	UNE ISDN/<6 circuits/Dispatch/FL (days)	ISDN - BRI	14.23	143	13.19	45	6.151	1.05135	0.9909	YES	14.82	149	14.41	41	7.003	1.25092	0.3291	YES
8.2.2.5.3.2	P-4A	UNE ISDN/<6 circuits/Non-Dispatch/FL (days)	ISDN - BRI	3.88	338			3.920				4.69	307			6.004			
8.2.2.5.4.1	P-4A	UNE ISDN/6-13 circuits/Dispatch/FL (days)	ISDN - BRI	13.18	2			0.020				16.96	1			0.000			
8.2.2.5.4.2	P-4A	UNE ISDN/6-13 circuits/Non-Dispatch/FL (days)	ISDN - BRI	1.89	3			1.464				2.14	2			1.504			
8.2.2.5.5.1	P-4A	UNE ISDN/>=14 circuits/Dispatch/FL (days)	ISDN - BRI																
8.2.2.5.5.2	P-4A	UNE ISDN/>=14 circuits/Non-Dispatch/FL (days)	ISDN - BRI																
8.2.2.6.3.1	P-4A	Line Sharing/<6 circuits/Dispatch/FL (days)	ADSL to Retail	3.33	10,554	2.01	1	1.984	1.66422	0.8646	YES	3.10	8,323			1.722			
8.2.2.6.3.2	P-4A	Line Sharing/<6 circuits/Non-Dispatch/FL (days)	ADSL to Retail	2.72	7,607	0.33	1	0.854	0.85376	2.7918	YES	2.52	6,134	4.20	1	0.935	0.65549	-2.5658	NO
8.2.2.6.4.1	P-4A	Line Sharing/6-13 circuits/Dispatch/FL (days)	ADSL to Retail	2.74	11			0.756				2.78	13			0.998			
8.2.2.6.4.2	P-4A	Line Sharing/6-13 circuits/Non-Dispatch/FL (days)	ADSL to Retail																
8.2.2.6.5.1	P-4A	Line Sharing/>=14 circuits/Dispatch/FL (days)	ADSL to Retail	2.08	1							3.60	1			0.000			
8.2.2.6.5.2	P-4A	Line Sharing/>=14 circuits/Non-Dispatch/FL (days)	ADSL to Retail																
8.2.2.7.1.1	P-4A	2W Analog Loop Design/<10 circuits/Dispatch/FL (days)	R&B - Disp	4.87	40,174	5.49	88	3.719	0.45132	-1.3738	YES	4.49	54,797	5.91	115	3.561	0.33243	-4.2653	NO
8.2.2.7.1.2	P-4A	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL (days)	R&B - Disp	4.87	40,174			3.719				4.49	54,797			3.561			
8.2.2.7.2.1	P-4A	2W Analog Loop Design/>=10 circuits/Dispatch/FL (days)	R&B - Disp	5.87	241	7.06	1	4.175	4.18333	-0.2846	YES	7.38	241	9.12	1	8.920	8.93830	-0.1944	YES
8.2.2.7.2.2	P-4A	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL (days)	R&B - Disp	5.87	241			4.175				7.38	241			8.920			
8.2.2.8.1.1	P-4A	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL (days)	R&B (POTS) excl SB Or	4.89	39,308	4.40	509	3.704	0.16525	2.9487	YES	4.52	52,892	4.43	462	3.579	0.16723	0.5398	YES
8.2.2.8.1.4	P-4A	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL (days)	R&B (POTS) excl SB Or	2.24	284,465	4.03	3	1.953	(.12790	-1.5839	YES	1.88	311,918	4.97	2	1.455	1.02853	-3.0013	NO
8.2.2.8.2.1	P-4A	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL (days)	R&B (POTS) excl SB Or	5.61	218	6.37	10	3.937	1.27309	-0.5923	YES	7.30	214	6.27	9	9.189	3.12663	-0.3116	YES
8.2.2.8.2.4	P-4A	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL (days)	R&B (POTS) excl SB Or	1.15	2			0.150				3.87	4			1.977			
8.2.2.9.1.1	P-4A	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL (days)	R&B - Disp	4.87	40,174			3.719				4.49	54,797			3.561			
8.2.2.9.1.2	P-4A	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL (days)	R&B - Disp	4.87	40,174			3.719				4.49	54,797			3.561			
8.2.2.9.2.1	P-4A	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL (days)	R&B - Disp	5.87	241			4.175				7.38	241			8.920			
8.2.2.9.2.2	P-4A	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL (days)	R&B - Disp	5.87	241			4.175				7.38	241			8.920			
8.2.2.10.1.1	P-4A	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL (days)	R&B (POTS) excl SB Or	4.89	39,308			3.704				4.52	52,892			3.579			
8.2.2.10.1.4	P-4A	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL (days)	R&B (POTS) excl SB Or	2.24	284,465			1.953				1.88	311,918			1.455			
8.2.2.10.2.1	P-4A	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL (days)	R&B (POTS) excl SB Or	5.61	218			3.937				7.30	214			9.189			
8.2.2.10.2.4	P-4A	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL (days)	R&B (POTS) excl SB Or	1.15	2			0.150				3.87	4			1.977			
8.2.2.11.1.1	P-4A	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL (days)	R&B - Disp	4.87	40,174	5.29	26	3.719	0.70298	-0.5926	YES	4.49	54,797	6.11	44	3.561	0.53708	-3.0107	NO
8.2.2.11.1.2	P-4A	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL (days)	R&B - Disp	4.87	40,174			3.719				4.49	54,797			3.561			
8.2.2.11.2.1	P-4A	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL (days)	R&B - Disp	5.87	241			4.175				7.38	241	9.87	1	8.920	8.93830	-0.2788	YES
8.2.2.11.2.2	P-4A	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL (days)	R&B - Disp	5.87	241			4.175				7.38	241			8.920			
8.2.2.12.1.1	P-4A	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL (days)	R&B (POTS) excl SB Or	4.89	39,308	5.59	127	3.704	0.32923	-2.1396	NO	4.52	52,892	5.67	138	3.579	0.30506	-3.7612	NO
8.2.2.12.1.4	P-4A	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL (days)	R&B (POTS) excl SB Or	2.24	284,465	5.63	179	1.953	0.14601	-23.1858	NO	1.88	311,918	5.85	132	1.455	0.12663	-31.3259	NO
8.2.2.12.2.1	P-4A	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL (days)	R&B (POTS) excl SB Or	5.61	218	8.54	5	3.937	1.76057	-1.6468	NO	7.30	214	6.99	8	9.189	3.30884	0.0939	YES
8.2.2.12.2.4	P-4A	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL (days)	R&B (POTS) excl SB Or	1.15	2	8.02	4	0.150	0.13013	-52.7641	NO	3.87	4	8.36	6	1.977	1.27608	-3.5360	NO
8.2.2.13.1.1	P-4A	Other Design/<10 circuits/Dispatch/FL (days)	Design	15.89	1,191			9.342				15.85	1,076			7.792			
8.2.2.13.1.2	P-4A	Other Design/<10 circuits/Non-Dispatch/FL (days)	Design	7.02	470			6.652				6.73	784			5.623			
8.2.2.13.2.1	P-4A	Other Design/>=10 circuits/Dispatch/FL (days)	Design	20.81	76			18.632				18.31	48			7.542			
8.2.2.13.2.2	P-4A	Other Design/>=10 circuits/Non-Dispatch/FL (days)	Design	9.49	11			5.090				6.50	7			5.349			
8.2.2.14.1.1	P-4A	Other Non-Design/<10 circuits/Dispatch/FL (days)	R&B	4.87	40,174			3.719				4.49	54,797			3.561			
8.2.2.14.1.2	P-4A	Other Non-Design/<10 circuits/Non-Dispatch/FL (days)	R&B	1.25	640,313			1.646				1.09	711,040			1.348			
8.2.2.14.2.1	P-4A	Other Non-Design/>=10 circuits/Dispatch/FL (days)	R&B	5.87	241			4.175				7.38	241			8.920			
8.2.2.14.2.2	P-4A	Other Non-Design/>=10 circuits/Non-Dispatch/FL (days)	R&B	2.63	148			5.217				2.42	250			3.845			
8.2.2.15.1.1	P-4A	INP (Standalone)/<10 circuits/Dispatch/FL (days)	R&B (POTS)	4.89	39,308			3.704				4.52	52,892			3.579			
8.2.2.15.1.2	P-4A	INP (Standalone)/<10 circuits/Non-Dispatch/FL (days)	R&B (POTS)	1.24	637,063			1.620				1.08	708,517			1.353			
8.2.2.15.2.1	P-4A	INP (Standalone)/>=10 circuits/Dispatch/FL (days)	R&B (POTS)	5.61	218			3.937				7.30	214			9.189			
8.2.2.15.2.2	P-4A	INP (Standalone)/>=10 circuits/Non-Dispatch/FL (days)	R&B (POTS)	1.15	2			0.150				3.86	5			1.712			
8.2.2.16.1.1	P-4A	LNP (Standalone)/<10 circuits/Dispatch/FL (days)	R&B (POTS)	4.89	39,308			3.704				4.52	52,892			3.579			
8.2.2.16.1.2	P-4A	LNP (Standalone)/<10 circuits/Non-Dispatch/FL (days)	R&B (POTS)	1.24	637,063	1.28	2,836	1.620	0.03162	-1.2712	YES	1.08	708,517	1.04	2,633	1.253	0.02358	1.7076	YES
8.2.2.16.2.1	P-4A	LNP (Standalone)/>=10 circuits/Dispatch/FL (days)	R&B (POTS)	5.61	218			3.937				7.30	214			9.189			
8.2.2.16.2.2	P-4A	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL (days)	R&B (POTS)	1.15	2	0.33	1	0.150	0.18403	4.4528	YES	3.86	5	0.06	3	1.712	1.25055	2.2077	YES
8.2.2.17.1.1	P-4A	Digital Loop < DS1/<10 circuits/Dispatch/FL (days)	Digital Loop < DS1	3.57	10,851	9.22	97	2.745	0.28000	-20.1556	NO	3.43	8,821	9.04	112	2.936	0.27949	-20.0833	NO
8.2.2.17.1.2	P-4A	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL (days)	Digital Loop < DS1	2.87	8,308			1.752				2.82	7,143			2.076			
8.2.2.17.2.1	P-4A	Digital Loop < DS1/>=10 circuits/Dispatch/FL (days)	Digital Loop < DS1	2.71	11			0.779				2.65	14			0.966			
8.2.2.17.2.2	P-4A	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL (days)	Digital Loop < DS1									2.25	1			0.000			
8.2.2.18.1.1	P-4A	Digital Loop >= DS1/<10 circuits/Dispatch/FL (days)	Digital Loop >= DS1	4.64	384	7.34	135	6.338	0.63421	-4.2599	NO	4.34	1,243	7.20	144	4.412	0.38834	-7.3829	NO
8.2.2.18.1.2	P-4A	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL (days)	Digital Loop >= DS1	2.45	1,059			3.791				3.17	793			4.702			
8.2.2.18.2.1	P-4A	Digital Loop >= DS1/>=10 circuits/Dispatch/FL (days)	Digital Loop >= DS1	13.88	18			10.267				16.33	18			8.228			
8.2.2.18.2.2	P-4A	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL (days)	Digital Loop >= DS1	4.26	55			8.099				4.11	48			4.438			
8.2.2.19.1.1	P-4A	Line Splitting/<10 circuits/Dispatch/FL (days)	ADSL to Retail	3.33	10,556			1.986				3.10	8,339			1.722			
8.2.2.19.1.2	P-4A	Line Splitting/<10 circuits/Non-Dispatch/FL (days)	ADSL to Retail	2.71	7,629			0.890				2.51	6,168			0.670			
8.2.2.19.2.1	P-4A	Line Splitting/>=10 circuits/Dispatch/FL (days)	ADSL to Retail	2.71	11			0.779				2.65	14			0.965			
8.2.2.19.2.2	P-4A	Line Splitting/>=10 circuits/Non-Dispatch/FL (days)	ADSL to Retail																
8.2.2.20.1.1	P-4A	EELs/<10 circuits/Dispatch/FL (days)	DS1/ DS3 - Interoffice	16.49	784	13.01	34	9.240	1.61868	2.1474	YES	14.03	1,160	12.90	22	7.216	1.55290	0.7366	YES
8.2.2.20.1.2	P-4A	EELs/<10 circuits/Non-Dispatch/FL (days)	DS1/ DS3 - Interoffice																
8.2.2.20.2.1	P-4A	EELs/>=10 circuits/Dispatch/FL (days)	DS1/ DS3 - Interoffice																
8.2.2.20.2.2	P-4A	EELs/>=10 circuits/Non-Dispatch/FL (days)	DS1/ DS3 - Interoffice																
		Average Order Completion and Completion Notice Interval - Non-Mechanized																	
8.2.3.1.1.1	P-4A	Switch Ports/<10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
8.2.3.1.1.2	P-4A	Switch Ports/<10 circuits/Non-Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
8.2.3.1.2.1	P-4A	Switch Ports/>=10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
8.2.3.1.2.2	P-4A	Switch Ports/>=10 circuits/Non-Dispatch/FL (days)	Diagnostic			18.31	8				Diagnostic			19.41	3				Diagnostic
8.2.3.2.1.1	P-4A	Local Interoffice Transport/<10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
8.2.3.2.1.2	P-4A	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
8.2.3.2.2.1	P-4A	Local Interoffice Transport/>=10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
8.2.3.2.2.2	P-4A	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL (days)	Diagnostic								Diagnostic			4.90	349				Diagnostic
8.2.3.3.1.1	P-4A	Loop + Port Combinations/<10 circuits/Dispatch/FL (days)	Diagnostic			4.90	209				Diagnostic			2.54	1,115				Diagnostic
8.2.3.3.1.2	P-4A	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL (days)	Diagnostic			1.64	554				Diagnostic			2.30	887				Diagnostic
8.2.3.3.1.3	P-4A	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL (days)	Diagnostic			0.99	426				Diagnostic			3.48	228				Diagnostic
8.2.3.3.1.4	P-4A	Loop + Port Combinations/<10 circuits/Dispatch In/FL (days)	Diagnostic			3.84	128				Diagnostic			4.38	2				Diagnostic
8.2.3.3.2.1	P-4A	Loop + Port Combinations/>=10 circuits/Dispatch/FL (days)	Diagnostic			4.95	2				Diagnostic			9.18	2				Diagnostic
8.2.3.3.2.2	P-4A	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
8.2.3.3.2.3	P-4A	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL (days)	Diagnostic								Diagnostic			9.18	2				Diagnostic
8.2.3.3.2.4	P-4A	Loop + Port Combinations/>=10 circuits/Dispatch In/FL (days)	Diagnostic								Diagnostic								Diagnostic
8.2.3.4.1.1	P-4A	Combo (other)/<10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
8.2.3.4.1.4	P-4A	Combo (other)/<10 circuits/Dispatch In/FL (days)	Diagnostic								Diagnostic								Diagnostic
8.2.3.4.2.1	P-4A	Combo (other)/>=10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
8.2.3.4.2.4	P-4A	Combo (other)/>=10 circuits/Dispatch In/FL (days)	Diagnostic								Diagnostic								Diagnostic

BellSouth Monthly Performance Summary

Florida Permanent, September 2002 - May 2002

			July							June								May			
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume
2.2.2.2.2	P-4A	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL (days)																				
2.2.3.1.1	P-4A	Loop + Port Combinations/<10 circuits/Dispatch/FL (days)	5.14	46,016	4.76	1,002	3.641	0.11626	3.3978	YES	4.61	40,807	4.93	888	3.325	0.11280	2.5105	YES	3.76	60,600	4.06	747
2.2.3.1.2	P-4A	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL (days)	1.09	673,740	1.04	41,673	1.290	0.00651	6.9504	YES	1.14	636,501	1.04	31,837	1.259	0.00723	13.9586	YES	1.09	667,414	0.92	29,985
2.2.3.1.3	P-4A	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL (days)	0.46	398,512	0.45	22,673	0.557	0.00381	1.5275	YES	0.52	346,660	0.51	18,026	0.639	0.00486	2.5346	YES	0.46	375,032	0.45	18,524
2.2.3.1.4	P-4A	Loop + Port Combinations/<10 circuits/Dispatch In/FL (days)	1.94	287,228	1.75	19,000	1.491	0.01117	16.9042	YES	1.87	289,821	1.72	13,811	1.415	0.01232	12.1351	YES	1.92	291,382	1.68	11,441
2.2.3.2.1	P-4A	Loop + Port Combinations/>=10 circuits/Dispatch/FL (days)	7.84	270	6.17	5	6.918	3.12216	0.5345	YES	7.98	265	4.19	5	7.309	3.01746	1.2593	YES	7.04	262	5.53	4
2.2.3.2.2	P-4A	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL (days)	2.30	230			2.464				2.25	124			2.662				3.19	245		
2.2.3.2.3	P-4A	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL (days)	0.65	52			1.098				0.38	33			0.246				0.45	58		
2.2.3.2.4	P-4A	Loop + Port Combinations/>=10 circuits/Dispatch In/FL (days)	2.78	178			2.543				2.93	91			2.838				4.72	158		
2.2.4.1.1	P-4A	Combo (other)/<10 circuits/Dispatch/FL (days)	5.40	47,253			4.229				4.90	41,832			4.158				3.99	61,776		
2.2.4.1.4	P-4A	Combo (other)/<10 circuits/Dispatch In/FL (days)	5.40	47,253			4.229				4.90	41,832			4.158				3.99	61,776		
2.2.4.2.1	P-4A	Combo (other)/>=10 circuits/Dispatch/FL (days)	7.86	271			6.914				8.12	268			7.401				7.50	267		
2.2.4.2.4	P-4A	Combo (other)/>=10 circuits/Dispatch In/FL (days)	7.86	271			6.914				8.12	268			7.401				7.50	267		
2.2.5.3.1	P-4A	UNE ISDN/<6 circuits/Dispatch/FL (days)	13.93	168	12.79	30	8.831	1.75044	0.6517	YES	13.94	143	12.90	40	6.358	1.05247	0.9883	YES	12.09	119	12.60	66
2.2.5.3.2	P-4A	UNE ISDN/<6 circuits/Non-Dispatch/FL (days)	2.33	202			4.430				2.18	231			3.475				3.14	314		
2.2.5.4.1	P-4A	UNE ISDN/6-13 circuits/Dispatch/FL (days)																				
2.2.5.4.2	P-4A	UNE ISDN/6-13 circuits/Non-Dispatch/FL (days)	1.31	1			0.000				4.08	1			0.000				0.80	2		
2.2.5.5.1	P-4A	UNE ISDN/>=14 circuits/Dispatch/FL (days)																				
2.2.5.5.2	P-4A	UNE ISDN/>=14 circuits/Non-Dispatch/FL (days)																				
2.2.6.3.1	P-4A	Line Sharing/<6 circuits/Dispatch/FL (days)	3.28	7,762	7.02	1	1.968	1.96808	-1.8983	NO	3.64	5,911			2.989				4.49	5,435		
2.2.6.3.2	P-4A	Line Sharing/<6 circuits/Non-Dispatch/FL (days)	2.67	5,740			0.773				2.89	4,662			1.027				3.86	4,446	3.22	1
2.2.6.4.1	P-4A	Line Sharing/6-13 circuits/Dispatch/FL (days)	4.40	6			2.131				2.92	9			1.626				9.36	10		
2.2.6.4.2	P-4A	Line Sharing/6-13 circuits/Non-Dispatch/FL (days)																	4.47	1		
2.2.6.5.1	P-4A	Line Sharing/>=14 circuits/Dispatch/FL (days)																				
2.2.6.5.2	P-4A	Line Sharing/>=14 circuits/Non-Dispatch/FL (days)																	8.26	1		
2.2.7.1.1	P-4A	2W Analog Loop Design/<10 circuits/Dispatch/FL (days)	5.14	46,016	5.80	151	3.641	0.29676	-2.2147	NO	4.61	40,807	5.56	158	3.325	0.26508	-3.5774	NO	3.76	60,600	6.22	121
2.2.7.1.2	P-4A	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL (days)	5.14	46,016			3.641				4.61	40,807			3.325				3.76	60,600		
2.2.7.2.1	P-4A	2W Analog Loop Design/>=10 circuits/Dispatch/FL (days)	7.84	270	7.37	1	6.918	6.93041	0.0675	YES	7.98	265			7.309				7.04	262		
2.2.7.2.2	P-4A	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL (days)	7.84	270			6.918				7.98	265			7.309				7.04	262		
2.2.8.1.1	P-4A	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL (days)	5.12	45,482	5.02	462	3.571	0.15697	0.6415	YES	4.61	40,348	4.43	515	3.309	0.14675	1.2357	YES	3.74	60,094	4.46	542
2.2.8.1.4	P-4A	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL (days)	1.94	285,861	3.50	11	1.482	0.44664	-3.4990	NO	1.87	288,470	4.02	18	1.404	0.35111	-6.1334	NO	1.92	289,345	4.15	8
2.2.8.2.1	P-4A	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL (days)	7.36	228	8.16	6	7.126	2.94733	-0.2724	YES	8.79	221	5.64	7	6.097	2.34062	0.4885	YES	7.13	231	4.87	8
2.2.8.2.4	P-4A	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL (days)	1.15	1	4.80	1	0.000	0.00000		NO	4.40	12			3.011				3.61	6		
2.2.9.1.1	P-4A	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL (days)	5.14	46,016			3.641				4.61	40,807			3.325				3.76	60,600		
2.2.9.1.2	P-4A	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL (days)	5.14	46,016			3.641				4.61	40,807			3.325				3.76	60,600		
2.2.9.2.1	P-4A	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL (days)	7.84	270			6.918				7.98	265			7.309				7.04	262		
2.2.9.2.2	P-4A	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL (days)	7.84	270			6.918				7.98	265			7.309				7.04	262		
2.2.10.1.1	P-4A	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL (days)	5.12	45,482			3.571				4.61	40,348			3.309				3.74	60,094		
2.2.10.1.4	P-4A	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL (days)	1.94	285,861			1.482				1.87	288,470			1.404				1.92	289,345		
2.2.10.2.1	P-4A	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL (days)	7.36	228			7.126				8.79	221			6.097				7.13	231		
2.2.10.2.4	P-4A	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL (days)	1.15	1			0.000				4.40	12			3.011				3.61	6		
2.2.11.1.1	P-4A	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL (days)	5.14	46,016	5.71	56	3.641	0.48680	-1.1727	YES	4.61	40,807	6.00	79	3.325	0.37448	-3.7115	NO	3.76	60,600	5.18	154
2.2.11.1.2	P-4A	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL (days)	5.14	46,016			3.641				4.61	40,807			3.325				3.76	60,600		
2.2.11.2.1	P-4A	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL (days)	7.84	270	7.08	1	6.918	6.93041	0.1097	YES	7.98	265	7.05	4	7.309	3.66195	0.2529	YES	7.04	262	9.00	6
2.2.11.2.2	P-4A	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL (days)	7.84	270			6.918				7.98	265			7.309				7.04	262		
2.2.12.1.1	P-4A	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL (days)	5.12	45,482	5.73	226	3.571	0.25707	-2.5449	NO	4.61	40,348	5.46	266	3.309	0.19637	-4.3271	NO	3.74	60,094	5.52	546
2.2.12.1.4	P-4A	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL (days)	1.94	285,861	5.66	106	1.482	0.14397	-26.0751	NO	1.87	288,470	5.75	166	1.404	0.10905	-35.9627	NO	1.92	289,345	5.54	248
2.2.12.2.1	P-4A	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL (days)	7.36	228	8.04	12	7.126	2.11063	-0.3209	YES	8.79	221	8.05	15	6.097	1.62676	-0.7775	YES	7.13	231	8.43	21
2.2.12.2.4	P-4A	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL (days)	1.15	1	8.21	11	0.000	0.00000		NO	4.40	12	7.54	3	3.011	1.94364	-1.6172	YES	3.61	6	8.10	6
2.2.13.1.1	P-4A	Other Design/<10 circuits/Dispatch/FL (days)	15.11	1,237			9.671				16.30	1,025			11.501				15.97	1,178		
2.2.13.1.2	P-4A	Other Design/<10 circuits/Non-Dispatch/FL (days)	8.58	485			5.921				7.39	445			5.706				8.05	550		
2.2.13.2.1	P-4A	Other Design/>=10 circuits/Dispatch/FL (days)	13.77	1			0.000				20.68	3			4.608				31.55	5		
2.2.13.2.2	P-4A	Other Design/>=10 circuits/Non-Dispatch/FL (days)	16.14	2			12.652				2.19	1			0.000				5.34	2		
2.2.14.1.1	P-4A	Other Non-Design/<10 circuits/Dispatch/FL (days)	5.14	46,016			3.641												3.76	60,600		
2.2.14.1.2	P-4A	Other Non-Design/<10 circuits/Non-Dispatch/FL (days)	1.09	673,740			1.290												1.09	667,414		
2.2.14.2.1	P-4A	Other Non-Design/>=10 circuits/Dispatch/FL (days)	7.84	270			6.918												7.04	262		
2.2.14.2.2	P-4A	Other Non-Design/>=10 circuits/Non-Dispatch/FL (days)	2.30	230			2.464												3.19	245		
2.2.15.1.1	P-4A	INP (Standalone)/<10 circuits/Dispatch/FL (days)	5.12	45,482			3.571				4.61	40,348			3.309				3.74	60,094		
2.2.15.1.2	P-4A	INP (Standalone)/<10 circuits/Non-Dispatch/FL (days)	1.09	671,638			1.285				1.13	634,543			1.252				1.09	664,487		
2.2.15.2.1	P-4A	INP (Standalone)/>=10 circuits/Dispatch/FL (days)	7.36	228			7.126				6.79	221			6.097				7.13	231		
2.2.15.2.2	P-4A	INP (Standalone)/>=10 circuits/Non-Dispatch/FL (days)	1.36	7			2.371				3.58	15			3.155				2.79	8		
2.2.16.1.1	P-4A	LNP (Standalone)/<10 circuits/Dispatch/FL (days)	5.12	45,482	2.78	3	3.571	2.06164	1.1363	YES	4.61	40,348	2.71	7	3.309	1.25090	1.5216	YES	3.74	60,094	5.30	1
2.2.16.1.2	P-4A	LNP (Standalone)/<10 circuits/Non-Dispatch/FL (days)	1.09	671,638	1.02	2,898	1.285	0.02479	2.5923	YES	1.13	634,543	1.21	2,387	1.252	0.02567	-3.0721	NO	1.09	664,487	1.16	2,989
2.2.16.2.1	P-4A	LNP (Standalone)/>=10 circuits/Dispatch/FL (days)	7.36	228			7.126				6.79	221			6.097				7.13	231		
2.2.16.2.2	P-4A	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL (days)	1.36	7	0.33	2	2.371	1.90113	0.5391	YES	3.58	15			3.155				2.79	8		
2.2.17.1.1	P-4A	Digital Loop < DS1/<10 circuits/Dispatch/FL (days)	3.66	8,086	9.26	84	3.122	0.39179	-14.2989	NO	4.12	6,220	9.25	101	3.912	0.39245	-13.0798	NO	4.97	5,616	12.60	66
2.2.17.1.2	P-4A	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL (days)	2.89	8,342			1.960				3.14	5,378			2.250				4.09	5,367		
2.2.17.2.1	P-4A	Digital Loop < DS1/>=10 circuits/Dispatch/FL (days)	3.63	5			1.100				2.92	9			1.626				9.36	10		
2.2.17.2.2	P-4A	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL (days)									2.19	1			0.000				8.26	1		
2.2.18.1.1	P-4A	Digital Loop >= DS1/<10 circuits/Dispatch/FL (days)	10.87	223	7.07	153	9.604	1.00824	3.7709	YES	4.48	133	6.90	140	6.456	0.78174	-3.0972	NO	12.90	94	7.28	142
2.2.18.1.2	P-4A	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL (days)	3.37	626			4.375				2.65	471			2.571				1.63	1,125		
2.2.18.2.1	P-4A	Digital Loop >= DS1/>=10 circuits/Dispatch/FL (days)																				
2.2.18.2.2	P-4A	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL (days)	3.36	81			3.621				2.60	34			3.806				2.44	53		
2.2.19.1.1	P-4A	Line Splitting/<10 circuits/Dispatch/FL (days)	3.28	7,777			1.969				3.63	5,920			2.987				4.46	5,481		
2.2.19.1.2	P-4A	Line Splitting/<10 circuits/Non-Dispatch/FL (days)	2.66	5,758			0.786				2.88	4,880			1.039				3.84	4,491		
2.2.19.2.1	P-4A	Line Splitting/>=10 circuits/Dispatch/FL (days)	3.83	5			1.100				2.92	9			1.626				9.36	10		
2.2.19.2.2	P-4A	Line Splitting/>=10 circuits/Non-Dispatch/FL (days)																	8.26	1		
2.2.20.1.1	P-4A	EELs/<10 circuits/Dispatch/FL (days)	13.01	1,080	13.73	9	7.246	2.57119	-0.2797	YES	14.07	1,039	11.39	3	9.095	5.25879	0.5091	YES	16.32	843		
2.2.20.1.2	P-4A	EELs/<10 circuits/Non-Dispatch/FL (days)																	14.20	1		
2.2.20.2.1	P-4A	EELs/>=10 circuits/Dispatch/FL (days)																				
2.2.20.2.2	P-4A	EELs/>=10 circuits/Non-Dispatch/FL (days)																				
		Average Order Completion and Completion Notice Interval - Non-Mechanized																				
9.2.3.1.1.1	P-4A	Switch Ports/<10 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic				
9.2.3.1.1.2	P-4A	Switch Ports/<10 circuits/Non-Dispatch/FL (days)								Diagnostic								Diagnostic				
9.2.3.1.2.1	P-4A	Switch Ports/>=10 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic				
9.2.3.1.2.2	P-4A	Switch Ports/>=10 circuits/Non-Dispatch/FL (days)			18.45	3				Diagnostic			18.42	12				Diagnostic			18.12	23
9.2.3.2.1.1	P-4A	Local Interoffice Transport/<10 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic				
9.2.3.2.1.2	P-4A	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL (days)								Diagnostic								Diagnostic				
9.2.3.2.2.1	P-4A	Local Interoffice Transport/>=10 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic			4.15	202
9.2.3.2.2.2	P-4A	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL (days)			5.16	264				Diagnostic			4.28	244				Diagnostic			1.80	635
9.2.3.3.1.1	P-4A	Loop + Port Combinations/<10 circuits/Dispatch/FL (days)			1.50	1,075				Diagnostic			1.84	997				Diagnostic			1.12	521
9.2.3.3.1.2	P-4A	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL (days)			0.97	809				Diagnostic			1.48	761				Diagnostic			2.94	314
9.2.3.3.1.3	P-4A	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL (days)			3.12	266				Diagnostic			2.99	236				Diagnostic			6.68	1
9.2.3.3.1.4	P-4A	Loop + Port Combinations/<10 circuits/Dispatch In/FL (days)			6.26	4				Diagnostic			6.54	3				Diagnostic			2.22	6
9.2.3.3.2.1	P-4A	Loop + Port Combinations/>=10 circuits/Dispatch/FL (days)			4.04	2				Diagnostic			8.64	1				Diagnostic			2.14	5
9.2.3.3.2.2	P-4A	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL (days)			0.33	1				Diagnostic								Diagnostic			2.83	1
9.2.3.3.2.3	P-4A	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL (days)			7.75	1				Diagnostic			8.64	1				Diagnostic			14.82	1
9.2.3.3.2.4	P-4A	Loop + Port Combinations/>=10 circuits/Dispatch In/FL (days)			17.61	2				Diagnostic			13.98	1				Diagnostic				
9.2.3.4.1.1	P-4A	Combo (other)/<10 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic				
9.2.3.4.1.4	P-4A	Combo (other)/<10 circuits/Dispatch In/FL (days)								Diagnostic								Diagnostic				
9.2.3.4.2.1	P-4A	Combo (other)/>=10 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic				
9.2.3.4.2.4	P-4A	Combo (other)/>=10 circuits/Dispatch In/FL (days)								Diagnostic								Diagnostic				

BellSouth Monthly Performance Summary

Florida Permanent, September 2002 - May 2002

			Standard Deviation	Standard Error	Zscore	Equity
B.2.2.2.2.2	P-4A	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL (days)				
B.2.2.3.1.1	P-4A	Loop + Port Combinations/<10 circuits/Dispatch/FL (days)	3.221	0.11859	-2.5117	NO
B.2.2.3.1.2	P-4A	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL (days)	1.411	0.00833	20.7394	YES
B.2.2.3.1.3	P-4A	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL (days)	0.529	0.00398	1.1565	YES
B.2.2.3.1.4	P-4A	Loop + Port Combinations/<10 circuits/Dispatch In/FL (days)	1.731	0.01650	14.2798	YES
B.2.2.3.2.1	P-4A	Loop + Port Combinations/>=10 circuits/Dispatch/FL (days)	8.472	4.26830	0.3538	YES
B.2.2.3.2.2	P-4A	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL (days)	6.272			
B.2.2.3.2.3	P-4A	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL (days)	0.759			
B.2.2.3.2.4	P-4A	Loop + Port Combinations/>=10 circuits/Dispatch In/FL (days)	7.382			
B.2.2.4.1.1	P-4A	Combo (other)/<10 circuits/Dispatch/FL (days)	3.872			
B.2.2.4.1.4	P-4A	Combo (other)/<10 circuits/Dispatch In/FL (days)	3.872			
B.2.2.4.2.1	P-4A	Combo (other)/>=10 circuits/Dispatch/FL (days)	9.372			
B.2.2.4.2.4	P-4A	Combo (other)/>=10 circuits/Dispatch In/FL (days)	9.372			
B.2.2.5.3.1	P-4A	UNE ISDN/<6 circuits/Dispatch/FL (days)	5.661	0.90253	-0.6696	YES
B.2.2.5.3.2	P-4A	UNE ISDN/<6 circuits/Non-Dispatch/FL (days)	5.978			
B.2.2.5.4.1	P-4A	UNE ISDN/6-13 circuits/Dispatch/FL (days)				
B.2.2.5.4.2	P-4A	UNE ISDN/6-13 circuits/Non-Dispatch/FL (days)	0.657			
B.2.2.5.5.1	P-4A	UNE ISDN/>=14 circuits/Dispatch/FL (days)				
B.2.2.5.5.2	P-4A	UNE ISDN/>=14 circuits/Non-Dispatch/FL (days)				
B.2.2.6.3.1	P-4A	Line Sharing/<6 circuits/Dispatch/FL (days)	2.388			
B.2.2.6.3.2	P-4A	Line Sharing/<6 circuits/Non-Dispatch/FL (days)	0.999	0.99881	0.8422	YES
B.2.2.6.4.1	P-4A	Line Sharing/6-13 circuits/Dispatch/FL (days)	11.765			
B.2.2.6.4.2	P-4A	Line Sharing/6-13 circuits/Non-Dispatch/FL (days)	0.000			
B.2.2.6.5.1	P-4A	Line Sharing/>=14 circuits/Dispatch/FL (days)				
B.2.2.6.5.2	P-4A	Line Sharing/>=14 circuits/Non-Dispatch/FL (days)	0.000			
B.2.2.7.1.1	P-4A	2W Analog Loop Design/<10 circuits/Dispatch/FL (days)	3.221	0.29314	-8.3953	NO
B.2.2.7.1.2	P-4A	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL (days)	3.221			
B.2.2.7.2.1	P-4A	2W Analog Loop Design/>=10 circuits/Dispatch/FL (days)	8.472			
B.2.2.7.2.2	P-4A	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL (days)	8.472			
B.2.2.8.1.1	P-4A	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL (days)	3.201	0.13811	-5.1635	NO
B.2.2.8.1.4	P-4A	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL (days)	1.725	0.60978	-3.8810	NO
B.2.2.8.2.1	P-4A	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL (days)	8.919	3.20740	0.7042	YES
B.2.2.8.2.4	P-4A	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL (days)	4.158			
B.2.2.9.1.1	P-4A	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL (days)	3.221			
B.2.2.9.1.2	P-4A	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL (days)	3.221			
B.2.2.9.2.1	P-4A	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL (days)	8.472			
B.2.2.9.2.2	P-4A	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL (days)	8.472			
B.2.2.10.1.1	P-4A	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL (days)	3.201			
B.2.2.10.1.4	P-4A	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL (days)	1.725			
B.2.2.10.2.1	P-4A	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL (days)	8.919			
B.2.2.10.2.4	P-4A	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL (days)	4.158			
B.2.2.11.1.1	P-4A	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL (days)	3.221	0.25991	-9.3214	NO
B.2.2.11.1.2	P-4A	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL (days)	3.221			
B.2.2.11.2.1	P-4A	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL (days)	8.472	3.49813	-0.5580	YES
B.2.2.11.2.2	P-4A	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL (days)	8.472			
B.2.2.12.1.1	P-4A	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL (days)	3.201	0.13760	-12.9410	NO
B.2.2.12.1.4	P-4A	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL (days)	1.725	0.11001	-32.9674	NO
B.2.2.12.2.1	P-4A	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL (days)	8.919	2.03278	-0.6384	YES
B.2.2.12.2.4	P-4A	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL (days)	4.158	2.40083	-1.9676	NO
B.2.2.13.1.1	P-4A	Other Design/<10 circuits/Dispatch/FL (days)	10.323			
B.2.2.13.1.2	P-4A	Other Design/<10 circuits/Non-Dispatch/FL (days)	6.356			
B.2.2.13.2.1	P-4A	Other Design/>=10 circuits/Dispatch/FL (days)	20.517			
B.2.2.13.2.2	P-4A	Other Design/>=10 circuits/Non-Dispatch/FL (days)	1.356			
B.2.2.14.1.1	P-4A	Other Non-Design/<10 circuits/Dispatch/FL (days)	3.221			
B.2.2.14.1.2	P-4A	Other Non-Design/<10 circuits/Non-Dispatch/FL (days)	1.411			
B.2.2.14.2.1	P-4A	Other Non-Design/>=10 circuits/Dispatch/FL (days)	8.472			
B.2.2.14.2.2	P-4A	Other Non-Design/>=10 circuits/Non-Dispatch/FL (days)	6.272			
B.2.2.15.1.1	P-4A	INP (Standalone)/<10 circuits/Dispatch/FL (days)	3.201			
B.2.2.15.1.2	P-4A	INP (Standalone)/<10 circuits/Non-Dispatch/FL (days)	1.406			
B.2.2.15.2.1	P-4A	INP (Standalone)/>=10 circuits/Dispatch/FL (days)	8.919			
B.2.2.15.2.2	P-4A	INP (Standalone)/>=10 circuits/Non-Dispatch/FL (days)	3.828			
B.2.2.16.1.1	P-4A	LNP (Standalone)/<10 circuits/Dispatch/FL (days)	3.201	3.20087	-0.4859	YES
B.2.2.16.1.2	P-4A	LNP (Standalone)/<10 circuits/Non-Dispatch/FL (days)	1.406	0.02578	-2.6141	NO
B.2.2.16.2.1	P-4A	LNP (Standalone)/>=10 circuits/Dispatch/FL (days)	8.919			
B.2.2.16.2.2	P-4A	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL (days)	3.828			
B.2.2.17.1.1	P-4A	Digital Loop < DS1/<10 circuits/Dispatch/FL (days)	3.339	0.41328	-16.5831	NO
B.2.2.17.1.2	P-4A	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL (days)	2.793			
B.2.2.17.2.1	P-4A	Digital Loop < DS1/>=10 circuits/Dispatch/FL (days)	11.765			
B.2.2.17.2.2	P-4A	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL (days)	0.000			
B.2.2.18.1.1	P-4A	Digital Loop >= DS1/<10 circuits/Dispatch/FL (days)	7.826	1.04067	5.3934	YES
B.2.2.18.1.2	P-4A	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL (days)	1.783			
B.2.2.18.2.1	P-4A	Digital Loop >= DS1/>=10 circuits/Dispatch/FL (days)				
B.2.2.18.2.2	P-4A	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL (days)	1.964			
B.2.2.19.1.1	P-4A	Line Splitting/<10 circuits/Dispatch/FL (days)	2.399			
B.2.2.19.1.2	P-4A	Line Splitting/<10 circuits/Non-Dispatch/FL (days)	1.042			
B.2.2.19.2.1	P-4A	Line Splitting/>=10 circuits/Dispatch/FL (days)	11.765			
B.2.2.19.2.2	P-4A	Line Splitting/>=10 circuits/Non-Dispatch/FL (days)	0.000			
B.2.2.20.1.1	P-4A	EELs/<10 circuits/Dispatch/FL (days)	10.655			
B.2.2.20.1.2	P-4A	EELs/<10 circuits/Non-Dispatch/FL (days)				
B.2.2.20.2.1	P-4A	EELs/>=10 circuits/Dispatch/FL (days)	0.000			
B.2.2.20.2.2	P-4A	EELs/>=10 circuits/Non-Dispatch/FL (days)				
		Average Order Completion and Completion Notice Interval - Non-Mechanized				
B.2.3.1.1.1	P-4A	Switch Ports/<10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.1.1.2	P-4A	Switch Ports/<10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.1.2.1	P-4A	Switch Ports/>=10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.1.2.2	P-4A	Switch Ports/>=10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.2.1.1	P-4A	Local Interoffice Transport/<10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.2.1.2	P-4A	Local Interoffice Transport/<10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.2.2.1	P-4A	Local Interoffice Transport/>=10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.2.2.2	P-4A	Local Interoffice Transport/>=10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.3.1.1	P-4A	Loop + Port Combinations/<10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.3.1.2	P-4A	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.3.1.3	P-4A	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL (days)				Diagnostic
B.2.3.3.1.4	P-4A	Loop + Port Combinations/<10 circuits/Dispatch In/FL (days)				Diagnostic
B.2.3.3.2.1	P-4A	Loop + Port Combinations/>=10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.3.2.2	P-4A	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.3.2.3	P-4A	Loop + Port Combinations/>=10 circuits/Switch Based Orders/FL (days)				Diagnostic
B.2.3.3.2.4	P-4A	Loop + Port Combinations/>=10 circuits/Dispatch In/FL (days)				Diagnostic
B.2.3.4.1.1	P-4A	Combo (other)/<10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.4.1.4	P-4A	Combo (other)/<10 circuits/Dispatch In/FL (days)				Diagnostic
B.2.3.4.2.1	P-4A	Combo (other)/>=10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.4.2.4	P-4A	Combo (other)/>=10 circuits/Dispatch In/FL (days)				Diagnostic

BellSouth Monthly Performance Summary

Florida Permanent, September 2002 - May 2002

			Benchmark/ Analog	September BST Measure	September BST Volume	September CLEC Measure	September CLEC Volume	September Standard Deviation	September Standard Error	September Zscore	September Equity	August BST Measure	August BST Volume	August CLEC Measure	August CLEC Volume	August Standard Deviation	August Standard Error	August Zscore	August Equity
B.2.3.5.3.1	P-4A	UNE ISDN/<6 circuits/Dispatch/FL (days)	Diagnostic			14.29	35				Diagnostic			12.87	34				Diagnostic
B.2.3.5.3.2	P-4A	UNE ISDN/<6 circuits/Non-Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.5.4.1	P-4A	UNE ISDN/6-13 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.5.4.2	P-4A	UNE ISDN/6-13 circuits/Non-Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.5.5.1	P-4A	UNE ISDN/>=14 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic			4.83	7				Diagnostic
B.2.3.5.5.2	P-4A	UNE ISDN/>=14 circuits/Non-Dispatch/FL (days)	Diagnostic			3.74	32				Diagnostic			4.93	27				Diagnostic
B.2.3.6.3.1	P-4A	Line Sharing/<6 circuits/Dispatch/FL (days)	Diagnostic			3.41	107				Diagnostic								Diagnostic
B.2.3.6.3.2	P-4A	Line Sharing/<6 circuits/Non-Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.6.4.1	P-4A	Line Sharing/6-13 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.6.4.2	P-4A	Line Sharing/6-13 circuits/Non-Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.6.5.1	P-4A	Line Sharing/>=14 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic			6.16	21				Diagnostic
B.2.3.6.5.2	P-4A	Line Sharing/>=14 circuits/Non-Dispatch/FL (days)	Diagnostic			5.70	29				Diagnostic								Diagnostic
B.2.3.7.1.1	P-4A	2W Analog Loop Design/<10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.7.1.2	P-4A	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.7.2.1	P-4A	2W Analog Loop Design/>=10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic			4.33	19				Diagnostic
B.2.3.7.2.2	P-4A	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL (days)	Diagnostic			5.31	18				Diagnostic			5.95	1				Diagnostic
B.2.3.8.1.1	P-4A	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL (days)	Diagnostic			6.14	6				Diagnostic			9.63	1				Diagnostic
B.2.3.8.1.4	P-4A	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL (days)	Diagnostic								Diagnostic			14.22	1				Diagnostic
B.2.3.8.2.1	P-4A	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.8.2.4	P-4A	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.9.1.1	P-4A	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.9.1.2	P-4A	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.9.2.1	P-4A	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic			5.72	1				Diagnostic
B.2.3.9.2.2	P-4A	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL (days)	Diagnostic			6.51	2				Diagnostic								Diagnostic
B.2.3.10.1.1	P-4A	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.10.1.4	P-4A	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.10.2.1	P-4A	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic			8.23	2				Diagnostic
B.2.3.10.2.4	P-4A	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL (days)	Diagnostic			8.05	4				Diagnostic								Diagnostic
B.2.3.11.1.1	P-4A	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.11.1.2	P-4A	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.11.2.1	P-4A	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic			5.91	6				Diagnostic
B.2.3.11.2.2	P-4A	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL (days)	Diagnostic			5.40	5				Diagnostic			5.59	1				Diagnostic
B.2.3.12.1.1	P-4A	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL (days)	Diagnostic			5.42	2				Diagnostic								Diagnostic
B.2.3.12.1.4	P-4A	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.12.2.1	P-4A	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.12.2.4	P-4A	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL (days)	Diagnostic			32.14	1				Diagnostic								Diagnostic
B.2.3.13.1.1	P-4A	Other Design/<10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.13.1.2	P-4A	Other Design/<10 circuits/Non-Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.13.2.1	P-4A	Other Design/>=10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic			7.70	1				Diagnostic
B.2.3.13.2.2	P-4A	Other Design/>=10 circuits/Non-Dispatch/FL (days)	Diagnostic			13.90	1				Diagnostic								Diagnostic
B.2.3.14.1.1	P-4A	Other Non-Design/<10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.14.1.2	P-4A	Other Non-Design/<10 circuits/Non-Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.14.2.1	P-4A	Other Non-Design/>=10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.14.2.2	P-4A	Other Non-Design/>=10 circuits/Non-Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.15.1.1	P-4A	INP (Standalone)/<10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.15.1.2	P-4A	INP (Standalone)/<10 circuits/Non-Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.15.2.1	P-4A	INP (Standalone)/>=10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic			4.67	3				Diagnostic
B.2.3.15.2.2	P-4A	INP (Standalone)/>=10 circuits/Non-Dispatch/FL (days)	Diagnostic								Diagnostic			0.75	403				Diagnostic
B.2.3.16.1.1	P-4A	LNP (Standalone)/<10 circuits/Dispatch/FL (days)	Diagnostic			0.69	197				Diagnostic								Diagnostic
B.2.3.16.1.2	P-4A	LNP (Standalone)/<10 circuits/Non-Dispatch/FL (days)	Diagnostic								Diagnostic			1.09	2				Diagnostic
B.2.3.16.2.1	P-4A	LNP (Standalone)/>=10 circuits/Dispatch/FL (days)	Diagnostic			1.49	2				Diagnostic			12.28	59				Diagnostic
B.2.3.16.2.2	P-4A	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL (days)	Diagnostic			13.35	41				Diagnostic								Diagnostic
B.2.3.17.1.1	P-4A	Digital Loop < DS1/<10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.17.1.2	P-4A	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.17.2.1	P-4A	Digital Loop < DS1/>=10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic			9.15	33				Diagnostic
B.2.3.17.2.2	P-4A	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL (days)	Diagnostic			9.31	26				Diagnostic								Diagnostic
B.2.3.18.1.1	P-4A	Digital Loop >= DS1/<10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.18.1.2	P-4A	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.18.2.1	P-4A	Digital Loop >= DS1/>=10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.18.2.2	P-4A	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.19.1.1	P-4A	Line Splitting/<10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.19.1.2	P-4A	Line Splitting/<10 circuits/Non-Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.19.2.1	P-4A	Line Splitting/>=10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic			13.84	35				Diagnostic
B.2.3.19.2.2	P-4A	Line Splitting/>=10 circuits/Non-Dispatch/FL (days)	Diagnostic			14.64	57				Diagnostic								Diagnostic
B.2.3.20.1.1	P-4A	EELs/<10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.20.1.2	P-4A	EELs/<10 circuits/Non-Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.20.2.1	P-4A	EELs/>=10 circuits/Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
B.2.3.20.2.2	P-4A	EELs/>=10 circuits/Non-Dispatch/FL (days)	Diagnostic								Diagnostic								Diagnostic
		Order Completion Interval within X days	<= 12 days											4.61	144				YES
B.2.4.1	P-4	xDSL (ADSL, HDSL and UCL) Loop with Conditioning/<6 circuits/Dispatch/FL (days)	<= 5 days			4.31	146				YES								
B.2.4.2	P-4	xDSL (ADSL, HDSL and UCL) Loop w/o Conditioning/<6 circuits/Dispatch/FL (days)																	
		Average Order Completion and Completion Notice Interval - Mechanized	<= 12 days											5.92	74				NO
B.2.5.1	P-4A	xDSL (ADSL, HDSL and UCL) Loop with Conditioning/<6 circuits/Dispatch/FL (days)	<= 5 days			5.83	55				NO								
B.2.5.2	P-4A	xDSL (ADSL, HDSL and UCL) Loop w/o Conditioning/<6 circuits/Dispatch/FL (days)																	
		Average Order Completion and Completion Notice Interval - Non-Mechanized	<= 12 days											6.06	6				NO
B.2.6.1	P-4A	xDSL (ADSL, HDSL and UCL) Loop with Conditioning/<6 circuits/Dispatch/FL (days)	<= 5 days			7.87	6				NO								
B.2.6.2	P-4A	xDSL (ADSL, HDSL and UCL) Loop w/o Conditioning/<6 circuits/Dispatch/FL (days)																	
		Held Orders	R&B (POTS)	8.94	325			11.232				8.17	456			8.322			
B.2.7.1.1.1.1	P-1	Switch Ports/<10 circuits/Dispatch/Facility/FL (days)	R&B (POTS)	0.00	0							0.00	0						
B.2.7.1.1.1.2	P-1	Switch Ports/<10 circuits/Dispatch/Equipment/FL (days)	R&B (POTS)	6.68	148			9.347				6.68	93			6.804			
B.2.7.1.1.1.3	P-1	Switch Ports/<10 circuits/Dispatch/Other/FL (days)	R&B (POTS)	0.00	0							0.00	0						
B.2.7.1.1.2.1	P-1	Switch Ports/<10 circuits/Non-Dispatch/Facility/FL (days)	R&B (POTS)	0.00	0							26.50	4			16.902			
B.2.7.1.1.2.2	P-1	Switch Ports/<10 circuits/Non-Dispatch/Equipment/FL (days)	R&B (POTS)	8.67	3			8.083				0.00	0						
B.2.7.1.1.2.3	P-1	Switch Ports/<10 circuits/Non-Dispatch/Other/FL (days)	R&B (POTS)	0.00	0							0.00	0						
B.2.7.1.2.1.1	P-1	Switch Ports/>=10 circuits/Dispatch/Facility/FL (days)	R&B (POTS)	0.00	0							0.00	0						
B.2.7.1.2.1.2	P-1	Switch Ports/>=10 circuits/Dispatch/Equipment/FL (days)	R&B (POTS)	0.00	0							0.00	0						
B.2.7.1.2.1.3	P-1	Switch Ports/>=10 circuits/Dispatch/Other/FL (days)	R&B (POTS)	0.00	0							0.00	0						
B.2.7.1.2.2.1	P-1	Switch Ports/>=10 circuits/Non-Dispatch/Facility/FL (days)	R&B (POTS)	0.00	0							0.00	0						YES
B.2.7.1.2.2.2	P-1	Switch Ports/>=10 circuits/Non-Dispatch/Equipment/FL (days)	R&B (POTS)	0.00	0						YES	0.00	0	0.00	0				YES
B.2.7.1.2.2.3	P-1	Switch Ports/>=10 circuits/Non-Dispatch/Other/FL (days)	DS1/ DS3 - Interoffice	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.2.1.1.1	P-1	Local Interoffice Transport/<10 circuits/Dispatch/Facility/FL (days)	DS1/ DS3 - Interoffice	0.00	0	0.00	0	5.529			YES	16.75	12	0.00	0	16.831			YES
B.2.7.2.1.1.2	P-1	Local Interoffice Transport/<10 circuits/Dispatch/Equipment/FL (days)	DS1/ DS3 - Interoffice	7.17	6	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.2.1.1.3	P-1	Local Interoffice Transport/<10 circuits/Dispatch/Other/FL (days)	DS1/ DS3 - Interoffice	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.2.1.2.1	P-1	Local Interoffice Transport/<10 circuits/Non-Dispatch/Facility/FL (days)	DS1/ DS3 - Interoffice	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.2.1.2.2	P-1	Local Interoffice Transport/<10 circuits/Non-Dispatch/Equipment/FL (days)	DS1/ DS3 - Interoffice	0.00	0	0.00	0												
B.2.7.2.1.2.3	P-1	Local Interoffice Transport/<10 circuits/Non-Dispatch/Other/FL (days)	DS1/ DS3 - Interoffice																
B.2.7.2.2.1.1	P-1	Local Interoffice Transport/>=10 circuits/Dispatch/Facility/FL (days)	DS1/ DS3 - Interoffice																
B.2.7.2.2.1.2	P-1	Local Interoffice Transport/>=10 circuits/Dispatch/Equipment/FL (days)	DS1/ DS3 - Interoffice																
B.2.7.2.2.1.3	P-1	Local Interoffice Transport/>=10 circuits/Dispatch/Other/FL (days)	DS1/ DS3 - Interoffice																
B.2.7.2.2.2.1	P-1	Local Interoffice Transport/>=10 circuits/Non-Dispatch/Facility/FL (days)	DS1/ DS3 - Interoffice																
B.2.7.2.2.2.2	P-1	Local Interoffice Transport/>=10 circuits/Non-Dispatch/Equipment/FL (days)	DS1/ DS3 - Interoffice																

BellSouth Monthly Performance Summary

Florida Permanent, September 2002 - May 2002

			July								June								May			
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.2.3.5.3.1	P-4A	UNE ISDN/<6 circuits/Dispatch/FL (days)			12.16	73				Diagnostic			11.34	54				Diagnostic			13.34	167
B.2.3.5.3.2	P-4A	UNE ISDN/<6 circuits/Non-Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.5.4.1	P-4A	UNE ISDN/6-13 circuits/Dispatch/FL (days)			13.78	1				Diagnostic								Diagnostic				
B.2.3.5.4.2	P-4A	UNE ISDN/6-13 circuits/Non-Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.5.5.1	P-4A	UNE ISDN/>=14 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.5.5.2	P-4A	UNE ISDN/>=14 circuits/Non-Dispatch/FL (days)								Diagnostic			4.97	28				Diagnostic			6.85	37
B.2.3.6.3.1	P-4A	Line Sharing/<6 circuits/Dispatch/FL (days)			7.33	22				Diagnostic			4.20	124				Diagnostic			4.14	125
B.2.3.6.3.2	P-4A	Line Sharing/<6 circuits/Non-Dispatch/FL (days)			4.00	99				Diagnostic								Diagnostic				
B.2.3.6.4.1	P-4A	Line Sharing/6-13 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.6.4.2	P-4A	Line Sharing/6-13 circuits/Non-Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.6.5.1	P-4A	Line Sharing/>=14 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic			6.45	41
B.2.3.6.5.2	P-4A	Line Sharing/>=14 circuits/Non-Dispatch/FL (days)								Diagnostic			6.24	21				Diagnostic				
B.2.3.7.1.1	P-4A	2W Analog Loop Design/<10 circuits/Dispatch/FL (days)			5.96	27				Diagnostic								Diagnostic			9.77	1
B.2.3.7.1.2	P-4A	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL (days)								Diagnostic			11.04	1				Diagnostic				
B.2.3.7.2.1	P-4A	2W Analog Loop Design/>=10 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic			4.19	140
B.2.3.7.2.2	P-4A	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL (days)								Diagnostic			5.12	50				Diagnostic			5.42	7
B.2.3.8.1.1	P-4A	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL (days)			5.37	38				Diagnostic			4.89	11				Diagnostic			7.56	2
B.2.3.8.1.4	P-4A	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL (days)			8.68	3				Diagnostic			10.97	3				Diagnostic				
B.2.3.8.2.1	P-4A	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL (days)			13.43	3				Diagnostic								Diagnostic			6.67	1
B.2.3.8.2.4	P-4A	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL (days)								Diagnostic								Diagnostic				
B.2.3.9.1.1	P-4A	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.9.1.2	P-4A	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.9.2.1	P-4A	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.9.2.2	P-4A	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL (days)								Diagnostic			4.85	1				Diagnostic				
B.2.3.10.1.1	P-4A	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL (days)			5.83	1				Diagnostic								Diagnostic				
B.2.3.10.1.4	P-4A	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL (days)								Diagnostic								Diagnostic				
B.2.3.10.2.1	P-4A	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic			6.59	14
B.2.3.10.2.4	P-4A	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL (days)			7.01	4				Diagnostic			7.03	3				Diagnostic				
B.2.3.11.1.1	P-4A	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.11.1.2	P-4A	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL (days)			6.13	1				Diagnostic								Diagnostic				
B.2.3.11.2.1	P-4A	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL (days)								Diagnostic			3.93	2				Diagnostic			6.59	8
B.2.3.11.2.2	P-4A	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL (days)			6.69	7				Diagnostic			5.44	3				Diagnostic			6.10	5
B.2.3.12.1.1	P-4A	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL (days)			5.86	4				Diagnostic								Diagnostic			9.43	2
B.2.3.12.1.4	P-4A	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL (days)								Diagnostic								Diagnostic				
B.2.3.12.2.1	P-4A	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.12.2.4	P-4A	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL (days)								Diagnostic								Diagnostic				
B.2.3.13.1.1	P-4A	Other Design/<10 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.13.1.2	P-4A	Other Design/<10 circuits/Non-Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.13.2.1	P-4A	Other Design/>=10 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic			7.87	38
B.2.3.13.2.2	P-4A	Other Design/>=10 circuits/Non-Dispatch/FL (days)			9.64	5				Diagnostic								Diagnostic				
B.2.3.14.1.1	P-4A	Other Non-Design/<10 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.14.1.2	P-4A	Other Non-Design/<10 circuits/Non-Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.14.2.1	P-4A	Other Non-Design/>=10 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.14.2.2	P-4A	Other Non-Design/>=10 circuits/Non-Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.15.1.1	P-4A	INP (Standalone)/<10 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.15.1.2	P-4A	INP (Standalone)/<10 circuits/Non-Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.15.2.1	P-4A	INP (Standalone)/>=10 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.15.2.2	P-4A	INP (Standalone)/>=10 circuits/Non-Dispatch/FL (days)								Diagnostic			2.68	5				Diagnostic				
B.2.3.16.1.1	P-4A	LNP (Standalone)/<10 circuits/Dispatch/FL (days)			2.86	2				Diagnostic			1.00	340				Diagnostic			0.94	338
B.2.3.16.1.2	P-4A	LNP (Standalone)/<10 circuits/Non-Dispatch/FL (days)			0.98	337				Diagnostic								Diagnostic				
B.2.3.16.2.1	P-4A	LNP (Standalone)/>=10 circuits/Dispatch/FL (days)								Diagnostic			0.63	7				Diagnostic			0.67	7
B.2.3.16.2.2	P-4A	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL (days)			0.47	11				Diagnostic			9.65	87				Diagnostic			10.82	259
B.2.3.17.1.1	P-4A	Digital Loop < DS1/<10 circuits/Dispatch/FL (days)			9.58	126				Diagnostic								Diagnostic				
B.2.3.17.1.2	P-4A	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.17.2.1	P-4A	Digital Loop < DS1/>=10 circuits/Dispatch/FL (days)			13.78	1				Diagnostic								Diagnostic				
B.2.3.17.2.2	P-4A	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL (days)								Diagnostic			7.67	29				Diagnostic			7.79	71
B.2.3.18.1.1	P-4A	Digital Loop >= DS1/<10 circuits/Dispatch/FL (days)			8.70	27				Diagnostic								Diagnostic				
B.2.3.18.1.2	P-4A	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.18.2.1	P-4A	Digital Loop >= DS1/>=10 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.18.2.2	P-4A	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.19.1.1	P-4A	Line Splitting/<10 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.19.1.2	P-4A	Line Splitting/<10 circuits/Non-Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.19.2.1	P-4A	Line Splitting/>=10 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic			14.23	92
B.2.3.19.2.2	P-4A	Line Splitting/>=10 circuits/Non-Dispatch/FL (days)			14.44	36				Diagnostic			14.14	63				Diagnostic				
B.2.3.20.1.1	P-4A	EELs/<10 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.20.1.2	P-4A	EELs/<10 circuits/Non-Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.20.2.1	P-4A	EELs/>=10 circuits/Dispatch/FL (days)								Diagnostic								Diagnostic				
B.2.3.20.2.2	P-4A	EELs/>=10 circuits/Non-Dispatch/FL (days)																			5.00	1
		Order Completion Interval within X days																			4.56	188
B.2.4.1	P-4	xDSL (ADSL, HDSL and UCL) Loop with Conditioning/<6 circuits/Dispatch/FL (days)								YES			4.09	208				YES				
B.2.4.2	P-4	xDSL (ADSL, HDSL and UCL) Loop w/o Conditioning/<6 circuits/Dispatch/FL (days)			4.66	166																
		Average Order Completion and Completion Notice Interval - Mechanized																				
B.2.5.1	P-4A	xDSL (ADSL, HDSL and UCL) Loop with Conditioning/<6 circuits/Dispatch/FL (days)								NO			5.80	56				NO				
B.2.5.2	P-4A	xDSL (ADSL, HDSL and UCL) Loop w/o Conditioning/<6 circuits/Dispatch/FL (days)			6.09	36																
		Average Order Completion and Completion Notice Interval - Non-Mechanized																			7.23	1
B.2.6.1	P-4A	xDSL (ADSL, HDSL and UCL) Loop with Conditioning/<6 circuits/Dispatch/FL (days)								NO			6.81	39				NO			6.21	100
B.2.6.2	P-4A	xDSL (ADSL, HDSL and UCL) Loop w/o Conditioning/<6 circuits/Dispatch/FL (days)			6.02	58																
		Held Orders	8.84	382			8.466				9.15	412			7.989				6.54	404		
B.2.7.1.1.1.1	P-1	Switch Ports/<10 circuits/Dispatch/Facility/FL (days)	0.00	0							0.00	0							0.00	0		
B.2.7.1.1.1.2	P-1	Switch Ports/<10 circuits/Dispatch/Equipment/FL (days)	9.23	31			11.523				9.48	54			14.821				8.92	53		
B.2.7.1.1.1.3	P-1	Switch Ports/<10 circuits/Dispatch/Other/FL (days)	0.00	0							0.00	0							0.00	0		
B.2.7.1.1.2.1	P-1	Switch Ports/<10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0							0.00	0							0.00	0		
B.2.7.1.1.2.2	P-1	Switch Ports/<10 circuits/Non-Dispatch/Equipment/FL (days)	2.50	2			0.707				5.00	1			0.000				0.00	0		
B.2.7.1.1.2.3	P-1	Switch Ports/<10 circuits/Non-Dispatch/Other/FL (days)	0.00	0							0.00	0							0.00	0		
B.2.7.1.2.1.1	P-1	Switch Ports/>=10 circuits/Dispatch/Facility/FL (days)	0.00	0							0.00	0							0.00	0		
B.2.7.1.2.1.2	P-1	Switch Ports/>=10 circuits/Dispatch/Equipment/FL (days)	0.00	0							0.00	0							0.00	0		
B.2.7.1.2.1.3	P-1	Switch Ports/>=10 circuits/Dispatch/Other/FL (days)	0.00	0							0.00	0							0.00	0		
B.2.7.1.2.2.1	P-1	Switch Ports/>=10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0							0.00	0							0.00	0		
B.2.7.1.2.2.2	P-1	Switch Ports/>=10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0							0.00	0						YES	0.00	0	0.00	0
B.2.7.1.2.2.3	P-1	Switch Ports/>=10 circuits/Non-Dispatch/Other/FL (days)	34.00	1	0.00	0	0.000			YES	3.00	1	0.00	0	0.000			YES	0.00	0	0.00	0
B.2.7.2.1.1.1	P-1	Local Interoffice Transport/<10 circuits/Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	10.71	7	0.00	0
B.2.7.2.1.1.2	P-1	Local Interoffice Transport/<10 circuits/Dispatch/Equipment/FL (days)	6.80	10	0.00	0	4.467			YES	6.50	2	0.00	0	4.950			YES	0.00	0	0.00	0
B.2.7.2.1.1.3	P-1	Local Interoffice Transport/<10 circuits/Dispatch/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.2.1.2.1	P-1	Local Interoffice Transport/<10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.2.1.2.2	P-1	Local Interoffice Transport/<10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0	0.00	0							0.00	0					0.00	0		
B.2.7.2.1.2.3	P-1	Local Interoffice Transport/<10 circuits/Non-Dispatch/Other/FL (days)											0.00	0					0.00	0		
B.2.7.2.2.1.1	P-1	Local Interoffice Transport/>=10 circuits/Dispatch/Facility/FL (days)											0.00	0					0.00	0		
B.2.7.2.2.1.2	P-1	Local Interoffice Transport/>=10 circuits/Dispatch/Equipment/FL (days)											0.00	0					0.00	0		
B.2.7.2.2.1.3	P-1	Local Interoffice Transport/>=10 circuits/Dispatch/Other/FL (days)											0.00	0					0.00	0		
B.2.7.2.2.2.1	P-1	Local Interoffice Transport/>=10 circuits/Non-Dispatch/Facility/FL (days)																				
B.2.7.2.2.2.2	P-1	Local Interoffice Transport/>=10 circuits/Non-Dispatch/Equipment/FL (days)																				

BellSouth Monthly Performance Summary

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			Standard Deviation	Standard Error	Zscore	Equity
B.2.3.5.3.1	P-4A	UNE ISDN/<5 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.5.3.2	P-4A	UNE ISDN/<5 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.5.4.1	P-4A	UNE ISDN/5-13 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.5.4.2	P-4A	UNE ISDN/5-13 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.5.5.1	P-4A	UNE ISDN/>=14 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.5.5.2	P-4A	UNE ISDN/>=14 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.6.3.1	P-4A	Line Sharing/<5 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.6.3.2	P-4A	Line Sharing/<5 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.6.4.1	P-4A	Line Sharing/5-13 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.6.4.2	P-4A	Line Sharing/5-13 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.6.5.1	P-4A	Line Sharing/>=14 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.6.5.2	P-4A	Line Sharing/>=14 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.7.1.1	P-4A	2W Analog Loop Design/<10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.7.1.2	P-4A	2W Analog Loop Design/<10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.7.2.1	P-4A	2W Analog Loop Design/>=10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.7.2.2	P-4A	2W Analog Loop Design/>=10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.8.1.1	P-4A	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.8.1.4	P-4A	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL (days)				Diagnostic
B.2.3.8.2.1	P-4A	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.8.2.4	P-4A	2W Analog Loop Non-Design/>=10 circuits/Dispatch In/FL (days)				Diagnostic
B.2.3.9.1.1	P-4A	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.9.1.2	P-4A	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.9.2.1	P-4A	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.9.2.2	P-4A	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.10.1.1	P-4A	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.10.1.4	P-4A	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL (days)				Diagnostic
B.2.3.10.2.1	P-4A	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.10.2.4	P-4A	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch In/FL (days)				Diagnostic
B.2.3.11.1.1	P-4A	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.11.1.2	P-4A	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.11.2.1	P-4A	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.11.2.2	P-4A	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.12.1.1	P-4A	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.12.1.4	P-4A	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL (days)				Diagnostic
B.2.3.12.2.1	P-4A	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.12.2.4	P-4A	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL (days)				Diagnostic
B.2.3.13.1.1	P-4A	Other Design/<10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.13.1.2	P-4A	Other Design/<10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.13.2.1	P-4A	Other Design/>=10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.13.2.2	P-4A	Other Design/>=10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.14.1.1	P-4A	Other Non-Design/<10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.14.1.2	P-4A	Other Non-Design/<10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.14.2.1	P-4A	Other Non-Design/>=10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.14.2.2	P-4A	Other Non-Design/>=10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.15.1.1	P-4A	INP (Standalone)/<10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.15.1.2	P-4A	INP (Standalone)/<10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.15.2.1	P-4A	INP (Standalone)/>=10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.15.2.2	P-4A	INP (Standalone)/>=10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.16.1.1	P-4A	LNP (Standalone)/<10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.16.1.2	P-4A	LNP (Standalone)/<10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.16.2.1	P-4A	LNP (Standalone)/>=10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.16.2.2	P-4A	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.17.1.1	P-4A	Digital Loop < DS1/<10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.17.1.2	P-4A	Digital Loop < DS1/<10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.17.2.1	P-4A	Digital Loop < DS1/>=10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.17.2.2	P-4A	Digital Loop < DS1/>=10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.18.1.1	P-4A	Digital Loop >= DS1/<10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.18.1.2	P-4A	Digital Loop >= DS1/<10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.18.2.1	P-4A	Digital Loop >= DS1/>=10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.18.2.2	P-4A	Digital Loop >= DS1/>=10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.19.1.1	P-4A	Line Splitting/<10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.19.1.2	P-4A	Line Splitting/<10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.19.2.1	P-4A	Line Splitting/>=10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.19.2.2	P-4A	Line Splitting/>=10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.20.1.1	P-4A	EELs/<10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.20.1.2	P-4A	EELs/<10 circuits/Non-Dispatch/FL (days)				Diagnostic
B.2.3.20.2.1	P-4A	EELs/>=10 circuits/Dispatch/FL (days)				Diagnostic
B.2.3.20.2.2	P-4A	EELs/>=10 circuits/Non-Dispatch/FL (days)				Diagnostic
		Order Completion Interval within X days				
B.2.4.1	P-4	xDSL (ADSL, HDSL and UCL) Loop with Conditioning/<6 circuits/Dispatch/FL (days)				YES
B.2.4.2	P-4	xDSL (ADSL, HDSL and UCL) Loop w/o Conditioning/<6 circuits/Dispatch/FL (days)				YES
		Average Order Completion and Completion Notice Interval - Mechanized				
B.2.5.1	P-4A	xDSL (ADSL, HDSL and UCL) Loop with Conditioning/<6 circuits/Dispatch/FL (days)				
B.2.5.2	P-4A	xDSL (ADSL, HDSL and UCL) Loop w/o Conditioning/<6 circuits/Dispatch/FL (days)				
		Average Order Completion and Completion Notice Interval - Non-Mechanized				
B.2.6.1	P-4A	xDSL (ADSL, HDSL and UCL) Loop with Conditioning/<6 circuits/Dispatch/FL (days)				YES
B.2.6.2	P-4A	xDSL (ADSL, HDSL and UCL) Loop w/o Conditioning/<6 circuits/Dispatch/FL (days)				NO
		Held Orders				
B.2.7.1.1.1.1	P-1	Switch Ports/<10 circuits/Dispatch/Facility/FL (days)	6.936			
B.2.7.1.1.1.2	P-1	Switch Ports/<10 circuits/Dispatch/Equipment/FL (days)	10.486			
B.2.7.1.1.1.3	P-1	Switch Ports/<10 circuits/Dispatch/Other/FL (days)				
B.2.7.1.1.2.1	P-1	Switch Ports/<10 circuits/Non-Dispatch/Facility/FL (days)				
B.2.7.1.1.2.2	P-1	Switch Ports/<10 circuits/Non-Dispatch/Equipment/FL (days)				
B.2.7.1.1.2.3	P-1	Switch Ports/<10 circuits/Non-Dispatch/Other/FL (days)				
B.2.7.1.2.1.1	P-1	Switch Ports/>=10 circuits/Dispatch/Facility/FL (days)				
B.2.7.1.2.1.2	P-1	Switch Ports/>=10 circuits/Dispatch/Equipment/FL (days)				
B.2.7.1.2.1.3	P-1	Switch Ports/>=10 circuits/Dispatch/Other/FL (days)				
B.2.7.1.2.2.1	P-1	Switch Ports/>=10 circuits/Non-Dispatch/Facility/FL (days)				
B.2.7.1.2.2.2	P-1	Switch Ports/>=10 circuits/Non-Dispatch/Equipment/FL (days)				
B.2.7.1.2.2.3	P-1	Switch Ports/>=10 circuits/Non-Dispatch/Other/FL (days)				YES
B.2.7.2.1.1.1	P-1	Local Interoffice Transport/<10 circuits/Dispatch/Facility/FL (days)				YES
B.2.7.2.1.1.2	P-1	Local Interoffice Transport/<10 circuits/Dispatch/Equipment/FL (days)				YES
B.2.7.2.1.1.3	P-1	Local Interoffice Transport/<10 circuits/Dispatch/Other/FL (days)	9.759			YES
B.2.7.2.1.2.1	P-1	Local Interoffice Transport/<10 circuits/Non-Dispatch/Facility/FL (days)				YES
B.2.7.2.1.2.2	P-1	Local Interoffice Transport/<10 circuits/Non-Dispatch/Equipment/FL (days)				YES
B.2.7.2.1.2.3	P-1	Local Interoffice Transport/<10 circuits/Non-Dispatch/Other/FL (days)				
B.2.7.2.2.1.1	P-1	Local Interoffice Transport/>=10 circuits/Dispatch/Facility/FL (days)				
B.2.7.2.2.1.2	P-1	Local Interoffice Transport/>=10 circuits/Dispatch/Equipment/FL (days)				
B.2.7.2.2.1.3	P-1	Local Interoffice Transport/>=10 circuits/Dispatch/Other/FL (days)				
B.2.7.2.2.2.1	P-1	Local Interoffice Transport/>=10 circuits/Non-Dispatch/Facility/FL (days)				
B.2.7.2.2.2.2	P-1	Local Interoffice Transport/>=10 circuits/Non-Dispatch/Equipment/FL (days)				

BellSouth Monthly Performance Summary

Florida Permanent, September 2002 - May 2002

			Benchmark/ Analog	September BST Measure	September BST Volume	September CLEC Measure	September CLEC Volume	September Standard Deviation	September Standard Error	September Zscore	September Equity	August BST Measure	August BST Volume	August CLEC Measure	August CLEC Volume	August Standard Deviation	August Standard Error	August Zscore	August Equity
B.2.7.2.2.2.3	P-1	Local Interoffice Transport/>=10 circuits/Non-Dispatch/Other/FL (days)	DS1/DS3 - Interoffice									8.23	466	4.21	14	8.261	2.24087	1.7930	YES
B.2.7.3.1.1.1	P-1	Loop + Port Combinations/<10 circuits/Dispatch/Facility/FL (days)	R&B	9.10	331	1.00	1	11.304	11.32071	0.7152	YES	0.00	0	0.00	0				YES
B.2.7.3.1.1.2	P-1	Loop + Port Combinations/<10 circuits/Dispatch/Equipment/FL (days)	R&B	0.00	0	0.00	0				YES	6.68	93	9.00	1	8.804	8.85081	-0.2624	YES
B.2.7.3.1.1.3	P-1	Loop + Port Combinations/<10 circuits/Dispatch/Other/FL (days)	R&B	6.68	148	0.00	0	9.347			YES	0.00	0	0.00	0				YES
B.2.7.3.1.2.1	P-1	Loop + Port Combinations/<10 circuits/Non-Dispatch/Facility/FL (days)	R&B	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.3.1.2.2	P-1	Loop + Port Combinations/<10 circuits/Non-Dispatch/Equipment/FL (days)	R&B	0.00	0	0.00	0				YES	21.80	5	4.00	1	16.264	20.02898	0.8787	YES
B.2.7.3.1.2.3	P-1	Loop + Port Combinations/<10 circuits/Non-Dispatch/Other/FL (days)	R&B	8.67	3	0.00	0	8.083			YES	0.00	0	0.00	0				YES
B.2.7.3.2.1.1	P-1	Loop + Port Combinations/<10 circuits/Switch Based Orders/Facility/FL (days)	R&B	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.3.2.1.2	P-1	Loop + Port Combinations/<10 circuits/Switch Based Orders/Equipment/FL (days)	R&B	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.3.2.1.3	P-1	Loop + Port Combinations/<10 circuits/Switch Based Orders/Other/FL (days)	R&B	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.3.2.2.1	P-1	Loop + Port Combinations/<10 circuits/Dispatch In/Facility/FL (days)	R&B	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.3.2.2.2	P-1	Loop + Port Combinations/<10 circuits/Dispatch In/Equipment/FL (days)	R&B	0.00	0	0.00	0				YES	21.80	5	4.00	1	16.264	20.02898	0.8787	YES
B.2.7.3.2.2.3	P-1	Loop + Port Combinations/<10 circuits/Dispatch In/Other/FL (days)	R&B	8.67	3	0.00	0	8.083			YES	0.00	0	0.00	0				YES
B.2.7.3.3.1.1	P-1	Loop + Port Combinations/>=10 circuits/Dispatch/Facility/FL (days)	R&B	5.00	1	0.00	0	0.000			YES	0.00	0	0.00	0				YES
B.2.7.3.3.1.2	P-1	Loop + Port Combinations/>=10 circuits/Dispatch/Equipment/FL (days)	R&B	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.3.3.1.3	P-1	Loop + Port Combinations/>=10 circuits/Dispatch/Other/FL (days)	R&B	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.3.3.2.1	P-1	Loop + Port Combinations/>=10 circuits/Non-Dispatch/Facility/FL (days)	R&B	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.3.3.2.2	P-1	Loop + Port Combinations/>=10 circuits/Non-Dispatch/Equipment/FL (days)	R&B	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.3.3.2.3	P-1	Loop + Port Combinations/>=10 circuits/Non-Dispatch/Other/FL (days)	R&B	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.3.4.1.1	P-1	Loop + Port Combinations/>=10 circuits/Switch Based Orders/Facility/FL (days)	R&B	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.3.4.1.2	P-1	Loop + Port Combinations/>=10 circuits/Switch Based Orders/Equipment/FL (days)	R&B	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.3.4.1.3	P-1	Loop + Port Combinations/>=10 circuits/Switch Based Orders/Other/FL (days)	R&B	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.3.4.2.1	P-1	Loop + Port Combinations/>=10 circuits/Dispatch In/Facility/FL (days)	R&B	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.3.4.2.2	P-1	Loop + Port Combinations/>=10 circuits/Dispatch In/Equipment/FL (days)	R&B	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.3.4.2.3	P-1	Loop + Port Combinations/>=10 circuits/Dispatch In/Other/FL (days)	R&B	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.4.1.1.1	P-1	Combo (other)/<10 circuits/Dispatch/Facility/FL (days)	R&B&D - Disp	9.25	338			11.698				8.31	475			8.355			
B.2.7.4.1.1.2	P-1	Combo (other)/<10 circuits/Dispatch/Equipment/FL (days)	R&B&D - Disp	0.00	0							0.00	0						
B.2.7.4.1.1.3	P-1	Combo (other)/<10 circuits/Dispatch/Other/FL (days)	R&B&D - Disp	7.03	152			10.332				7.44	102			9.497			
B.2.7.4.1.2.1	P-1	Combo (other)/<10 circuits/Non-Dispatch/Facility/FL (days)	R&B&D - Disp	9.25	338			11.698				8.31	475			8.355			
B.2.7.4.1.2.2	P-1	Combo (other)/<10 circuits/Non-Dispatch/Equipment/FL (days)	R&B&D - Disp	0.00	0							0.00	0						
B.2.7.4.1.2.3	P-1	Combo (other)/<10 circuits/Non-Dispatch/Other/FL (days)	R&B&D - Disp	7.03	152			10.332				7.44	102			9.497			
B.2.7.4.2.1.1	P-1	Combo (other)/>=10 circuits/Dispatch/Facility/FL (days)	R&B&D - Disp	5.00	1			0.000				0.00	0						
B.2.7.4.2.1.2	P-1	Combo (other)/>=10 circuits/Dispatch/Equipment/FL (days)	R&B&D - Disp	0.00	0							4.00	1			0.000			
B.2.7.4.2.1.3	P-1	Combo (other)/>=10 circuits/Dispatch/Other/FL (days)	R&B&D - Disp	5.00	1			0.000				0.00	0						
B.2.7.4.2.2.1	P-1	Combo (other)/>=10 circuits/Non-Dispatch/Facility/FL (days)	R&B&D - Disp	0.00	0							0.00	0						
B.2.7.4.2.2.2	P-1	Combo (other)/>=10 circuits/Non-Dispatch/Equipment/FL (days)	R&B&D - Disp	0.00	0							4.00	1			0.000			
B.2.7.4.2.2.3	P-1	Combo (other)/>=10 circuits/Non-Dispatch/Other/FL (days)	R&B&D - Disp	0.00	0														
B.2.7.5.1.1.1	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/Facility/FL (days)	ADSL to Retail	17.63	114	0.00	0	24.374			YES	10.20	54	0.00	0	9.079			YES
B.2.7.5.1.1.2	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/Equipment/FL (days)	ADSL to Retail	1.00	1	0.00	0	0.000			YES	4.00	1	0.00	0	0.000			YES
B.2.7.5.1.1.3	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/Other/FL (days)	ADSL to Retail	8.78	23	0.00	0	15.817			YES	6.80	5	2.00	1	4.656	5.10294	0.9406	YES
B.2.7.5.1.2.1	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/Facility/FL (days)	ADSL to Retail	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.5.1.2.2	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/Equipment/FL (days)	ADSL to Retail	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.5.1.2.3	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/Other/FL (days)	ADSL to Retail	8.50	2	0.00	0	6.364			YES	3.00	1	0.00	0	0.000			YES
B.2.7.5.2.1.1	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/Facility/FL (days)	ADSL to Retail	0.00	0							0.00	0						
B.2.7.5.2.1.2	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/Equipment/FL (days)	ADSL to Retail	0.00	0							0.00	0						
B.2.7.5.2.1.3	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/Other/FL (days)	ADSL to Retail	0.00	0							0.00	0						
B.2.7.5.2.2.1	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/Facility/FL (days)	ADSL to Retail	0.00	0							0.00	0						
B.2.7.5.2.2.2	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/Equipment/FL (days)	ADSL to Retail	0.00	0							0.00	0						
B.2.7.5.2.2.3	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/Other/FL (days)	ADSL to Retail	0.00	0							0.00	0						
B.2.7.6.1.1.1	P-1	UNE ISDN/<10 circuits/Dispatch/Facility/FL (days)	ISDN - BRI	0.00	0	0.00	0				YES	4.00	1	6.33	3	0.000	0.00000		NO
B.2.7.6.1.1.2	P-1	UNE ISDN/<10 circuits/Dispatch/Equipment/FL (days)	ISDN - BRI	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.6.1.1.3	P-1	UNE ISDN/<10 circuits/Dispatch/Other/FL (days)	ISDN - BRI	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.6.1.2.1	P-1	UNE ISDN/<10 circuits/Non-Dispatch/Facility/FL (days)	ISDN - BRI	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.6.1.2.2	P-1	UNE ISDN/<10 circuits/Non-Dispatch/Equipment/FL (days)	ISDN - BRI	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.6.1.2.3	P-1	UNE ISDN/<10 circuits/Non-Dispatch/Other/FL (days)	ISDN - BRI	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.6.2.1.1	P-1	UNE ISDN/>=10 circuits/Dispatch/Facility/FL (days)	ISDN - BRI																
B.2.7.6.2.1.2	P-1	UNE ISDN/>=10 circuits/Dispatch/Equipment/FL (days)	ISDN - BRI																
B.2.7.6.2.1.3	P-1	UNE ISDN/>=10 circuits/Dispatch/Other/FL (days)	ISDN - BRI																
B.2.7.6.2.2.1	P-1	UNE ISDN/>=10 circuits/Non-Dispatch/Facility/FL (days)	ISDN - BRI																
B.2.7.6.2.2.2	P-1	UNE ISDN/>=10 circuits/Non-Dispatch/Equipment/FL (days)	ISDN - BRI																
B.2.7.6.2.2.3	P-1	UNE ISDN/>=10 circuits/Non-Dispatch/Other/FL (days)	ISDN - BRI																
B.2.7.7.1.1.1	P-1	Line Sharing/<10 circuits/Dispatch/Facility/FL (days)	ADSL to Retail	17.63	114	0.00	0	24.374			YES	10.20	54	0.00	0	9.079			YES
B.2.7.7.1.1.2	P-1	Line Sharing/<10 circuits/Dispatch/Equipment/FL (days)	ADSL to Retail	1.00	1	0.00	0	0.000			YES	4.00	1	0.00	0	0.000			YES
B.2.7.7.1.1.3	P-1	Line Sharing/<10 circuits/Dispatch/Other/FL (days)	ADSL to Retail	8.78	23	0.00	0	15.817			YES	6.80	5	0.00	0	4.656			YES
B.2.7.7.1.2.1	P-1	Line Sharing/<10 circuits/Non-Dispatch/Facility/FL (days)	ADSL to Retail	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.7.1.2.2	P-1	Line Sharing/<10 circuits/Non-Dispatch/Equipment/FL (days)	ADSL to Retail	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.7.1.2.3	P-1	Line Sharing/<10 circuits/Non-Dispatch/Other/FL (days)	ADSL to Retail	8.50	2	0.00	0	6.364			YES	3.00	1	0.00	0	0.000			YES
B.2.7.7.2.1.1	P-1	Line Sharing/>=10 circuits/Dispatch/Facility/FL (days)	ADSL to Retail	0.00	0							0.00	0						
B.2.7.7.2.1.2	P-1	Line Sharing/>=10 circuits/Dispatch/Equipment/FL (days)	ADSL to Retail	0.00	0							0.00	0						
B.2.7.7.2.1.3	P-1	Line Sharing/>=10 circuits/Dispatch/Other/FL (days)	ADSL to Retail	0.00	0							0.00	0						
B.2.7.7.2.2.1	P-1	Line Sharing/>=10 circuits/Non-Dispatch/Facility/FL (days)	ADSL to Retail	0.00	0							0.00	0						
B.2.7.7.2.2.2	P-1	Line Sharing/>=10 circuits/Non-Dispatch/Equipment/FL (days)	ADSL to Retail	0.00	0							0.00	0						
B.2.7.7.2.2.3	P-1	Line Sharing/>=10 circuits/Non-Dispatch/Other/FL (days)	ADSL to Retail	0.00	0							0.00	0						
B.2.7.8.1.1.1	P-1	2W Analog Loop Design/<10 circuits/Dispatch/Facility/FL (days)	R&B - Disp	9.10	331	0.00	0	11.304			YES	8.23	466	9.00	2	8.261	5.85370	-0.1312	YES
B.2.7.8.1.1.2	P-1	2W Analog Loop Design/<10 circuits/Dispatch/Equipment/FL (days)	R&B - Disp	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.8.1.1.3	P-1	2W Analog Loop Design/<10 circuits/Dispatch/Other/FL (days)	R&B - Disp	6.68	148	0.00	0	9.347			YES	6.68	93	0.00	0	8.804			YES
B.2.7.8.1.2.1	P-1	2W Analog Loop Design/<10 circuits/Non-Dispatch/Facility/FL (days)	R&B - Disp	9.10	331	0.00	0	11.304			YES	8.23	466	0.00	0	8.261			YES
B.2.7.8.1.2.2	P-1	2W Analog Loop Design/<10 circuits/Non-Dispatch/Equipment/FL (days)	R&B - Disp	0.00	0						YES	0.00	0	0.00	0				YES
B.2.7.8.1.2.3	P-1	2W Analog Loop Design/<10 circuits/Non-Dispatch/Other/FL (days)	R&B - Disp	6.68	148	0.00	0	9.347			YES	6.68	93	0.00	0	8.804			YES
B.2.7.8.2.1.1	P-1	2W Analog Loop Design/>=10 circuits/Dispatch/Facility/FL (days)	R&B - Disp	5.00	1	0.00	0	0.000			YES	0.00	0	0.00	0				YES
B.2.7.8.2.1.2	P-1	2W Analog Loop Design/>=10 circuits/Dispatch/Equipment/FL (days)	R&B - Disp	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.8.2.1.3	P-1	2W Analog Loop Design/>=10 circuits/Dispatch/Other/FL (days)	R&B - Disp	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.8.2.2.1	P-1	2W Analog Loop Design/>=10 circuits/Non-Dispatch/Facility/FL (days)	R&B - Disp	5.00	1	0.00	0	0.000			YES	0.00	0	0.00	0				YES
B.2.7.8.2.2.2	P-1	2W Analog Loop Design/>=10 circuits/Non-Dispatch/Equipment/FL (days)	R&B - Disp	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.8.2.2.3	P-1	2W Analog Loop Design/>=10 circuits/Non-Dispatch/Other/FL (days)	R&B - Disp	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.9.1.1.1	P-1	2W Analog Loop Non-Design/<10 circuits/Dispatch/Facility/FL (days)	R&B (POTS) excl SB Or	8.64	325	13.33	3	11.232	6.51470	-0.6751	YES	8.17	456	2.75	4	8.322	4.17908	1.2961	YES
B.2.7.9.1.1.2	P-1	2W Analog Loop Non-Design/<10 circuits/Dispatch/Equipment/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.9.1.1.3	P-1	2W Analog Loop Non-Design/<10 circuits/Dispatch/Other/FL (days)	R&B (POTS) excl SB Or	6.68	148	0.00	0	9.347			YES	6.68	93	0.00	0	8.804			YES
B.2.7.9.1.2.1	P-1	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/Facility/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.9.1.2.2	P-1	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/Equipment/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES	26.50	4	0.00	0	16.902			YES
B.2.7.9.1.2.3	P-1	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/Other/FL (days)	R&B (POTS) excl SB Or	8.67	3	0.00	0	8.083			YES	0.00	0	0.00	0				YES
B.2.7.9.2.1.1	P-1	2W Analog Loop Non-Design/>=10 circuits/Dispatch/Facility/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.9.2.1.2	P-1	2W Analog Loop Non-Design/>=10 circuits/Dispatch/Equipment/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.9.2.1.3	P-1	2W Analog Loop Non-Design/>=10 circuits/Dispatch/Other/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.9.2.2.1	P-1	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/Facility/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.9.2.2.2	P-1	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/Equipment/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.9.2.2.3	P-1	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/Other/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
B.2.7.10.1.1.1	P-1	2W Analog Loop w/INP Design/<10 circuits/Dispatch/Facility/FL (days)	R&B - Disp	9.10	331			11.304				8.23	466	0.00	0	8.261			YES
B.2.7.10.1.1.2	P-1	2W Analog Loop w/INP Design/<10 circuits/Dispatch/Equipment/FL (days)	R&B - Disp	0.00	0							0.00	0	0.00	0				YES
B.2.7.10.1.1.3	P-1	2W Analog Loop w/INP Design/<10 circuits/Dispatch/Other/FL (days)	R&B - Disp	6.68	148			9.347				6.68	93	0.00	0	8.804			YES
B.2.7.10.1.2.1	P-1	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/Facility/FL (days)	R&B - Disp	9.10	331			11.304				8.23	466	0.00	0	8.261			YES
B.2.7.10.1.2.2	P-1	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/Equipment/FL (days)	R&B - Disp	0.00	0							0.00	0	0.00	0				

BellSouth Monthly Performance Summary

Florida Permanent, September 2002 - May 2002

		July								June								May			
		BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume
B.2.7.2.2.2.3	P-1	Loop + Port Combinations/<10 circuits/Non-Dispatch/Other/FL (days)										0.00	0					0.00	0		
B.2.7.3.1.1.1	P-1	Loop + Port Combinations/<10 circuits/Dispatch/Facility/FL (days)	8.84	382	5.80	10	8.466	2.71192	1.1201	YES	9.17	416	6.20	10	7.998	2.55951	1.1808	YES	6.57	407	2.25	4
B.2.7.3.1.1.2	P-1	Loop + Port Combinations/<10 circuits/Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.3.1.1.3	P-1	Loop + Port Combinations/<10 circuits/Dispatch/Other/FL (days)	9.23	31	1.50	2	11.523	6.40678	0.9190	YES	9.48	54	3.00	2	14.821	10.67206	0.8073	YES	8.63	56	3.00	3
B.2.7.3.1.2.1	P-1	Loop + Port Combinations/<10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.3.1.2.2	P-1	Loop + Port Combinations/<10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.3.1.2.3	P-1	Loop + Port Combinations/<10 circuits/Non-Dispatch/Other/FL (days)	2.50	2	0.00	0	0.707			YES	5.00	1	0.00	0	0.000			YES	0.00	0	0.00	0
B.2.7.3.2.1.1	P-1	Loop + Port Combinations/<10 circuits/Switch Based Orders/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.3.2.1.2	P-1	Loop + Port Combinations/<10 circuits/Switch Based Orders/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.3.2.1.3	P-1	Loop + Port Combinations/<10 circuits/Switch Based Orders/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.3.2.2.1	P-1	Loop + Port Combinations/<10 circuits/Dispatch In/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	0.000			YES	1.43	7	0.00	0
B.2.7.3.2.2.2	P-1	Loop + Port Combinations/<10 circuits/Dispatch In/Equipment/FL (days)	2.50	2	0.00	6	0.707			YES	5.00	1	0.00	0				YES	0.00	0	0.00	0
B.2.7.3.2.2.3	P-1	Loop + Port Combinations/<10 circuits/Dispatch In/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.3.3.1.1	P-1	Loop + Port Combinations/>=10 circuits/Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.3.3.1.2	P-1	Loop + Port Combinations/>=10 circuits/Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.3.3.1.3	P-1	Loop + Port Combinations/>=10 circuits/Dispatch/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.3.3.2.1	P-1	Loop + Port Combinations/>=10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.3.3.2.2	P-1	Loop + Port Combinations/>=10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.3.3.2.3	P-1	Loop + Port Combinations/>=10 circuits/Non-Dispatch/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.4.1.1	P-1	Loop + Port Combinations/<10 circuits/Switch Based Orders/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.4.1.2	P-1	Loop + Port Combinations/<10 circuits/Switch Based Orders/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.4.1.3	P-1	Loop + Port Combinations/<10 circuits/Switch Based Orders/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.4.2.1	P-1	Loop + Port Combinations/<10 circuits/Dispatch In/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.4.2.2	P-1	Loop + Port Combinations/<10 circuits/Dispatch In/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	8.001			YES	6.69	408	0.00	0
B.2.7.4.2.3	P-1	Loop + Port Combinations/<10 circuits/Dispatch In/Other/FL (days)	0.00	0	0.00	0	8.381			YES	9.19	421	0.00	0				YES	0.00	0	0.00	0
B.2.7.4.1.1.1	P-1	Combo (other)/<10 circuits/Dispatch/Facility/FL (days)	8.81	394	0.00	0				YES	0.00	0	0.00	0				YES	13.28	64	0.00	0
B.2.7.4.1.1.2	P-1	Combo (other)/<10 circuits/Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	11.58	74	0.00	0	14.361			YES	6.69	408	0.00	0
B.2.7.4.1.1.3	P-1	Combo (other)/<10 circuits/Dispatch/Other/FL (days)	8.79	38	0.00	0	10.690			YES	9.19	421	0.00	0	8.001			YES	0.00	0	0.00	0
B.2.7.4.1.2.1	P-1	Combo (other)/<10 circuits/Non-Dispatch/Facility/FL (days)	8.81	394	0.00	0	8.381			YES	0.00	0	0.00	0				YES	13.28	64	0.00	0
B.2.7.4.1.2.2	P-1	Combo (other)/<10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	11.58	74	0.00	0	14.361			YES	1.43	7		
B.2.7.4.1.2.3	P-1	Combo (other)/<10 circuits/Non-Dispatch/Other/FL (days)	8.79	38	0.00	0	10.690				0.00	0							0.00	0		
B.2.7.4.2.1.1	P-1	Combo (other)/>=10 circuits/Dispatch/Facility/FL (days)	0.00	0							0.00	0							0.00	0		
B.2.7.4.2.1.2	P-1	Combo (other)/>=10 circuits/Dispatch/Equipment/FL (days)	0.00	0							0.00	0							1.43	7		
B.2.7.4.2.1.3	P-1	Combo (other)/>=10 circuits/Dispatch/Other/FL (days)	0.00	0							0.00	0							0.00	0		
B.2.7.4.2.2.1	P-1	Combo (other)/>=10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0							0.00	0							0.00	0		
B.2.7.4.2.2.2	P-1	Combo (other)/>=10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0							0.00	0			9.384			YES	18.07	83	19.00	1
B.2.7.4.2.2.3	P-1	Combo (other)/>=10 circuits/Non-Dispatch/Other/FL (days)	0.00	0							0.00	0						YES				
B.2.7.5.1.1.1	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/Facility/FL (days)	7.48	40	3.00	1	6.008	6.08313	0.7356	YES	10.47	32	0.00	0				YES	1.00	9		
B.2.7.5.1.1.2	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0	16.263			YES				
B.2.7.5.1.1.3	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/Other/FL (days)	2.39	18	0.00	0	2.500			YES	15.50	2	0.00	0				YES				
B.2.7.5.1.2.1	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES				
B.2.7.5.1.2.2	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES				
B.2.7.5.1.2.3	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/Other/FL (days)	0.00	0							0.00	0	0.00	0				YES				
B.2.7.5.2.1.1	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/Facility/FL (days)	0.00	0							0.00	0	0.00	0				YES				
B.2.7.5.2.1.2	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/Equipment/FL (days)	0.00	0							0.00	0	0.00	0				YES				
B.2.7.5.2.1.3	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/Other/FL (days)	0.00	0							0.00	0	0.00	0				YES				
B.2.7.5.2.2.1	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0							0.00	0	0.00	0				NO	12.00	1	0.00	0
B.2.7.5.2.2.2	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0						YES	3.00	1	8.00	2	0.000	0.00000		YES	0.00	0	0.00	0
B.2.7.5.2.2.3	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/Other/FL (days)	13.67	3	10.80	5	7.024	5.12944	0.5586	YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.6.1.1.1	P-1	UNE ISDN/<10 circuits/Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	7.00	1	0.00	0	0.000			YES	0.00	0	0.00	0
B.2.7.6.1.1.2	P-1	UNE ISDN/<10 circuits/Dispatch/Equipment/FL (days)	1.00	2	0.00	0	0.000			YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.6.1.1.3	P-1	UNE ISDN/<10 circuits/Dispatch/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.6.1.2.1	P-1	UNE ISDN/<10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0								
B.2.7.6.1.2.2	P-1	UNE ISDN/<10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0	0.00	0																
B.2.7.6.1.2.3	P-1	UNE ISDN/<10 circuits/Non-Dispatch/Other/FL (days)			0.00	0																
B.2.7.6.2.1.1	P-1	UNE ISDN/>=10 circuits/Dispatch/Facility/FL (days)			0.00	0																
B.2.7.6.2.1.2	P-1	UNE ISDN/>=10 circuits/Dispatch/Equipment/FL (days)			0.00	0																
B.2.7.6.2.1.3	P-1	UNE ISDN/>=10 circuits/Dispatch/Other/FL (days)			0.00	0																
B.2.7.6.2.2.1	P-1	UNE ISDN/>=10 circuits/Non-Dispatch/Facility/FL (days)			0.00	0																
B.2.7.6.2.2.2	P-1	UNE ISDN/>=10 circuits/Non-Dispatch/Equipment/FL (days)			0.00	0																
B.2.7.6.2.2.3	P-1	UNE ISDN/>=10 circuits/Non-Dispatch/Other/FL (days)	7.48	40	0.00	0	6.008			YES	10.47	32	7.00	1	9.384	9.52972	0.3840	YES	18.07	83	0.00	0
B.2.7.7.1.1.1	P-1	Line Sharing/<10 circuits/Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.7.1.1.2	P-1	Line Sharing/<10 circuits/Dispatch/Equipment/FL (days)	2.39	18	0.00	0	2.500			YES	15.50	2	0.00	0	16.263			YES	1.00	9	0.00	0
B.2.7.7.1.1.3	P-1	Line Sharing/<10 circuits/Dispatch/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.7.1.2.1	P-1	Line Sharing/<10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.7.1.2.2	P-1	Line Sharing/<10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0								
B.2.7.7.1.2.3	P-1	Line Sharing/<10 circuits/Non-Dispatch/Other/FL (days)	0.00	0							0.00	0							0.00	0		
B.2.7.7.2.1.1	P-1	Line Sharing/>=10 circuits/Dispatch/Facility/FL (days)	0.00	0							0.00	0							0.00	0		
B.2.7.7.2.1.2	P-1	Line Sharing/>=10 circuits/Dispatch/Equipment/FL (days)	0.00	0							0.00	0							0.00	0		
B.2.7.7.2.1.3	P-1	Line Sharing/>=10 circuits/Dispatch/Other/FL (days)	0.00	0							0.00	0							0.00	0		
B.2.7.7.2.2.1	P-1	Line Sharing/>=10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0							0.00	0							0.00	0		
B.2.7.7.2.2.2	P-1	Line Sharing/>=10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0						YES	9.17	416	8.00	1	7.998	8.00793	0.3959	YES	6.57	407	0.00	0
B.2.7.7.2.2.3	P-1	Line Sharing/>=10 circuits/Non-Dispatch/Other/FL (days)	8.84	382	0.00	0	8.466			YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.8.1.1.1	P-1	2W Analog Loop Design/<10 circuits/Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	9.48	54	0.00	0	14.821			YES	8.63	56	0.00	0
B.2.7.8.1.1.2	P-1	2W Analog Loop Design/<10 circuits/Dispatch/Equipment/FL (days)	9.23	31	0.00	0	11.523			YES	9.17	416	0.00	0	7.998			YES	6.57	407	0.00	0
B.2.7.8.1.1.3	P-1	2W Analog Loop Design/<10 circuits/Dispatch/Other/FL (days)	8.84	382	0.00	0	8.466			YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.8.1.2.1	P-1	2W Analog Loop Design/<10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	9.48	54	0.00	0	14.821			YES	8.63	56	0.00	0
B.2.7.8.1.2.2	P-1	2W Analog Loop Design/<10 circuits/Non-Dispatch/Equipment/FL (days)	9.23	31	0.00	0	11.523			YES	9.17	416	0.00	0				YES	1.43	7	0.00	0
B.2.7.8.1.2.3	P-1	2W Analog Loop Design/<10 circuits/Non-Dispatch/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.8.2.1.1	P-1	2W Analog Loop Design/>=10 circuits/Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.8.2.1.2	P-1	2W Analog Loop Design/>=10 circuits/Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	1.43	7	0.00	0
B.2.7.8.2.1.3	P-1	2W Analog Loop Design/>=10 circuits/Dispatch/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.8.2.2.1	P-1	2W Analog Loop Design/>=10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.8.2.2.2	P-1	2W Analog Loop Design/>=10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.8.2.2.3	P-1	2W Analog Loop Design/>=10 circuits/Non-Dispatch/Other/FL (days)	8.84	382	9.00	1	8.466	8.47884	-0.0191	YES	9.15	412	7.00	2	7.989	5.66295	0.3802	YES	6.54	404	1.50	2
B.2.7.9.1.1.1	P-1	2W Analog Loop Non-Design/<10 circuits/Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.9.1.1.2	P-1	2W Analog Loop Non-Design/<10 circuits/Dispatch/Equipment/FL (days)	9.23	31	0.00	0	11.523			YES	9.48	54	0.00	0	14.821			YES	6.92	53	0.00	0
B.2.7.9.1.1.3	P-1	2W Analog Loop Non-Design/<10 circuits/Dispatch/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.9.1.2.1	P-1	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.9.1.2.2	P-1	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0	0.00	0	0.707			YES	5.00	1	0.00	0	0.000			YES	0.00	0	8.00	1
B.2.7.9.1.2.3	P-1	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/Other/FL (days)	2.50	2	0.00	0				NO	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.9.2.1.1	P-1	2W Analog Loop Non-Design/>=10 circuits/Dispatch/Facility/FL (days)	0.00	0	10.00	1				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.9.2.1.2	P-1	2W Analog Loop Non-Design/>=10 circuits/Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.9.2.1.3	P-1	2W Analog Loop Non-Design/>=10 circuits/Dispatch/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.9.2.2.1	P-1	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
B.2.7.9.2.2.2	P-1	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	6.57	407	0.00	0
B.2.7.9.2.2.3	P-1	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/Other/FL (days)	8.84	382	0.00	0	8.466			YES	9.17	416			7.998			YES	0.00	0	0.00	0
B.2.7.10.1.1.1	P-1	2W Analog Loop w/INP Design/<10 circuits/Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	9.48	54			14.821			YES	8.63	56	0.00	0
B.2.7.10.1.1.2	P-1	2W Analog Loop w/INP Design/<10 circuits/Dispatch/Equipment/FL (days)	9.23	31	0.00	0	11.523			YES	9.17	416			7.998			YES	6.57	407	0.00	0
B.2.7.10.1.1.3	P-1	2W Analog Loop w/INP Design/<10 circuits/Dispatch/Other/FL (days)	8.84	382	0.00	0	8.466			YES	0.00	0						YES	0.00	0	0.00	0
B.2.7.10.1.2.1	P-1	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0	0.00	0																
B.2.7.10.1.2.2	P-1	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/Equipment/FL (days)																				

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			Standard Deviation	Standard Error	Zscore	Equity
B.2.7.2.2.2.3	P-1	Loop + Port Combinations/>=10 circuits/Non-Dispatch/Other/FL (days)				
B.2.7.3.1.1.1	P-1	Loop + Port Combinations/<10 circuits/Dispatch/Facility/FL (days)	6.984	3.50916	1.2318	YES
B.2.7.3.1.1.2	P-1	Loop + Port Combinations/<10 circuits/Dispatch/Equipment/FL (days)				YES
B.2.7.3.1.1.3	P-1	Loop + Port Combinations/<10 circuits/Dispatch/Other/FL (days)	10.304	6.10610	0.9212	YES
B.2.7.3.1.2.1	P-1	Loop + Port Combinations/<10 circuits/Non-Dispatch/Facility/FL (days)				YES
B.2.7.3.1.2.2	P-1	Loop + Port Combinations/<10 circuits/Non-Dispatch/Equipment/FL (days)				YES
B.2.7.3.1.2.3	P-1	Loop + Port Combinations/<10 circuits/Non-Dispatch/Other/FL (days)				YES
B.2.7.3.2.1.1	P-1	Loop + Port Combinations/<10 circuits/Switch Based Orders/Facility/FL (days)				YES
B.2.7.3.2.1.2	P-1	Loop + Port Combinations/<10 circuits/Switch Based Orders/Equipment/FL (days)				YES
B.2.7.3.2.1.3	P-1	Loop + Port Combinations/<10 circuits/Switch Based Orders/Other/FL (days)				YES
B.2.7.3.2.2.1	P-1	Loop + Port Combinations/<10 circuits/Dispatch In/Facility/FL (days)				YES
B.2.7.3.2.2.2	P-1	Loop + Port Combinations/<10 circuits/Dispatch In/Equipment/FL (days)				YES
B.2.7.3.2.2.3	P-1	Loop + Port Combinations/<10 circuits/Dispatch In/Other/FL (days)				YES
B.2.7.3.3.1.1	P-1	Loop + Port Combinations/>=10 circuits/Dispatch/Facility/FL (days)	1.134			YES
B.2.7.3.3.1.2	P-1	Loop + Port Combinations/>=10 circuits/Dispatch/Equipment/FL (days)				YES
B.2.7.3.3.1.3	P-1	Loop + Port Combinations/>=10 circuits/Dispatch/Other/FL (days)				YES
B.2.7.3.3.2.1	P-1	Loop + Port Combinations/>=10 circuits/Non-Dispatch/Facility/FL (days)				YES
B.2.7.3.3.2.2	P-1	Loop + Port Combinations/>=10 circuits/Non-Dispatch/Equipment/FL (days)				YES
B.2.7.3.3.2.3	P-1	Loop + Port Combinations/>=10 circuits/Non-Dispatch/Other/FL (days)				YES
B.2.7.3.4.1.1	P-1	Loop + Port Combinations/>=10 circuits/Switch Based Orders/Facility/FL (days)				YES
B.2.7.3.4.1.2	P-1	Loop + Port Combinations/>=10 circuits/Switch Based Orders/Equipment/FL (days)				YES
B.2.7.3.4.1.3	P-1	Loop + Port Combinations/>=10 circuits/Switch Based Orders/Other/FL (days)				YES
B.2.7.3.4.2.1	P-1	Loop + Port Combinations/>=10 circuits/Dispatch In/Facility/FL (days)				YES
B.2.7.3.4.2.2	P-1	Loop + Port Combinations/>=10 circuits/Dispatch In/Equipment/FL (days)				YES
B.2.7.3.4.2.3	P-1	Loop + Port Combinations/>=10 circuits/Dispatch In/Other/FL (days)				YES
B.2.7.4.1.1.1	P-1	Combo (other)/<10 circuits/Dispatch/Facility/FL (days)	6.981			YES
B.2.7.4.1.1.2	P-1	Combo (other)/<10 circuits/Dispatch/Equipment/FL (days)				YES
B.2.7.4.1.1.3	P-1	Combo (other)/<10 circuits/Dispatch/Other/FL (days)	20.237			YES
B.2.7.4.1.2.1	P-1	Combo (other)/<10 circuits/Non-Dispatch/Facility/FL (days)	6.981			YES
B.2.7.4.1.2.2	P-1	Combo (other)/<10 circuits/Non-Dispatch/Equipment/FL (days)				YES
B.2.7.4.1.2.3	P-1	Combo (other)/<10 circuits/Non-Dispatch/Other/FL (days)	20.237			YES
B.2.7.4.2.1.1	P-1	Combo (other)/>=10 circuits/Dispatch/Facility/FL (days)	1.134			
B.2.7.4.2.1.2	P-1	Combo (other)/>=10 circuits/Dispatch/Equipment/FL (days)				
B.2.7.4.2.1.3	P-1	Combo (other)/>=10 circuits/Dispatch/Other/FL (days)				
B.2.7.4.2.2.1	P-1	Combo (other)/>=10 circuits/Non-Dispatch/Facility/FL (days)	1.134			
B.2.7.4.2.2.2	P-1	Combo (other)/>=10 circuits/Non-Dispatch/Equipment/FL (days)				
B.2.7.4.2.2.3	P-1	Combo (other)/>=10 circuits/Non-Dispatch/Other/FL (days)				
B.2.7.5.1.1.1	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/Facility/FL (days)	18.253	18.36312	-0.0505	YES
B.2.7.5.1.1.2	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/Equipment/FL (days)				
B.2.7.5.1.1.3	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/Other/FL (days)	0.000			
B.2.7.5.1.2.1	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/Facility/FL (days)				
B.2.7.5.1.2.2	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/Equipment/FL (days)				
B.2.7.5.1.2.3	P-1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/Other/FL (days)				
B.2.7.5.2.1.1	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/Facility/FL (days)				
B.2.7.5.2.1.2	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/Equipment/FL (days)				
B.2.7.5.2.1.3	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Dispatch/Other/FL (days)				
B.2.7.5.2.2.1	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/Facility/FL (days)				
B.2.7.5.2.2.2	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/Equipment/FL (days)				
B.2.7.5.2.2.3	P-1	xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/Other/FL (days)				
B.2.7.6.1.1.1	P-1	UNE ISDN/<10 circuits/Dispatch/Facility/FL (days)	0.000			YES
B.2.7.6.1.1.2	P-1	UNE ISDN/<10 circuits/Dispatch/Equipment/FL (days)				YES
B.2.7.6.1.1.3	P-1	UNE ISDN/<10 circuits/Dispatch/Other/FL (days)				YES
B.2.7.6.1.2.1	P-1	UNE ISDN/<10 circuits/Non-Dispatch/Facility/FL (days)				YES
B.2.7.6.1.2.2	P-1	UNE ISDN/<10 circuits/Non-Dispatch/Equipment/FL (days)				YES
B.2.7.6.1.2.3	P-1	UNE ISDN/<10 circuits/Non-Dispatch/Other/FL (days)				
B.2.7.6.2.1.1	P-1	UNE ISDN/>=10 circuits/Dispatch/Facility/FL (days)				
B.2.7.6.2.1.2	P-1	UNE ISDN/>=10 circuits/Dispatch/Equipment/FL (days)				
B.2.7.6.2.1.3	P-1	UNE ISDN/>=10 circuits/Dispatch/Other/FL (days)				
B.2.7.6.2.2.1	P-1	UNE ISDN/>=10 circuits/Non-Dispatch/Facility/FL (days)				
B.2.7.6.2.2.2	P-1	UNE ISDN/>=10 circuits/Non-Dispatch/Equipment/FL (days)				
B.2.7.6.2.2.3	P-1	UNE ISDN/>=10 circuits/Non-Dispatch/Other/FL (days)				
B.2.7.7.1.1.1	P-1	Line Sharing/<10 circuits/Dispatch/Facility/FL (days)	18.253			YES
B.2.7.7.1.1.2	P-1	Line Sharing/<10 circuits/Dispatch/Equipment/FL (days)				YES
B.2.7.7.1.1.3	P-1	Line Sharing/<10 circuits/Dispatch/Other/FL (days)	0.000			YES
B.2.7.7.1.2.1	P-1	Line Sharing/<10 circuits/Non-Dispatch/Facility/FL (days)				YES
B.2.7.7.1.2.2	P-1	Line Sharing/<10 circuits/Non-Dispatch/Equipment/FL (days)				YES
B.2.7.7.1.2.3	P-1	Line Sharing/<10 circuits/Non-Dispatch/Other/FL (days)				
B.2.7.7.2.1.1	P-1	Line Sharing/>=10 circuits/Dispatch/Facility/FL (days)				
B.2.7.7.2.1.2	P-1	Line Sharing/>=10 circuits/Dispatch/Equipment/FL (days)				
B.2.7.7.2.1.3	P-1	Line Sharing/>=10 circuits/Dispatch/Other/FL (days)				
B.2.7.7.2.2.1	P-1	Line Sharing/>=10 circuits/Non-Dispatch/Facility/FL (days)				
B.2.7.7.2.2.2	P-1	Line Sharing/>=10 circuits/Non-Dispatch/Equipment/FL (days)				
B.2.7.7.2.2.3	P-1	Line Sharing/>=10 circuits/Non-Dispatch/Other/FL (days)				
B.2.7.8.1.1.1	P-1	2W Analog Loop Design/<10 circuits/Dispatch/Facility/FL (days)	6.984			YES
B.2.7.8.1.1.2	P-1	2W Analog Loop Design/<10 circuits/Dispatch/Equipment/FL (days)				YES
B.2.7.8.1.1.3	P-1	2W Analog Loop Design/<10 circuits/Dispatch/Other/FL (days)	10.304			YES
B.2.7.8.1.2.1	P-1	2W Analog Loop Design/<10 circuits/Non-Dispatch/Facility/FL (days)	6.984			YES
B.2.7.8.1.2.2	P-1	2W Analog Loop Design/<10 circuits/Non-Dispatch/Equipment/FL (days)				YES
B.2.7.8.1.2.3	P-1	2W Analog Loop Design/<10 circuits/Non-Dispatch/Other/FL (days)	10.304			YES
B.2.7.8.2.1.1	P-1	2W Analog Loop Design/>=10 circuits/Dispatch/Facility/FL (days)	1.134			YES
B.2.7.8.2.1.2	P-1	2W Analog Loop Design/>=10 circuits/Dispatch/Equipment/FL (days)				YES
B.2.7.8.2.1.3	P-1	2W Analog Loop Design/>=10 circuits/Dispatch/Other/FL (days)				YES
B.2.7.8.2.2.1	P-1	2W Analog Loop Design/>=10 circuits/Non-Dispatch/Facility/FL (days)	1.134			YES
B.2.7.8.2.2.2	P-1	2W Analog Loop Design/>=10 circuits/Non-Dispatch/Equipment/FL (days)				YES
B.2.7.8.2.2.3	P-1	2W Analog Loop Design/>=10 circuits/Non-Dispatch/Other/FL (days)				YES
B.2.7.9.1.1.1	P-1	2W Analog Loop Non-Design/<10 circuits/Dispatch/Facility/FL (days)	6.936	4.91870	1.0260	YES
B.2.7.9.1.1.2	P-1	2W Analog Loop Non-Design/<10 circuits/Dispatch/Equipment/FL (days)				YES
B.2.7.9.1.1.3	P-1	2W Analog Loop Non-Design/<10 circuits/Dispatch/Other/FL (days)	10.486			YES
B.2.7.9.1.2.1	P-1	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/Facility/FL (days)				YES
B.2.7.9.1.2.2	P-1	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/Equipment/FL (days)				YES
B.2.7.9.1.2.3	P-1	2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/Other/FL (days)				NO
B.2.7.9.2.1.1	P-1	2W Analog Loop Non-Design/>=10 circuits/Dispatch/Facility/FL (days)				YES
B.2.7.9.2.1.2	P-1	2W Analog Loop Non-Design/>=10 circuits/Dispatch/Equipment/FL (days)				YES
B.2.7.9.2.1.3	P-1	2W Analog Loop Non-Design/>=10 circuits/Dispatch/Other/FL (days)				YES
B.2.7.9.2.2.1	P-1	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/Facility/FL (days)				YES
B.2.7.9.2.2.2	P-1	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/Equipment/FL (days)				YES
B.2.7.9.2.2.3	P-1	2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/Other/FL (days)				YES
B.2.7.10.1.1.1	P-1	2W Analog Loop w/INP Design/<10 circuits/Dispatch/Facility/FL (days)	6.984			YES
B.2.7.10.1.1.2	P-1	2W Analog Loop w/INP Design/<10 circuits/Dispatch/Equipment/FL (days)				YES
B.2.7.10.1.1.3	P-1	2W Analog Loop w/INP Design/<10 circuits/Dispatch/Other/FL (days)	10.304			YES
B.2.7.10.1.2.1	P-1	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/Facility/FL (days)	6.984			YES
B.2.7.10.1.2.2	P-1	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/Equipment/FL (days)				

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			Benchmark/ Analog	September BST Measure	September BST Volume	September CLEC Measure	September CLEC Volume	September Standard Deviation	September Standard Error	September Zscore	September Equity	August BST Measure	August BST Volume	August CLEC Measure	August CLEC Volume	August Standard Deviation	August Standard Error	August Zscore	August Equity
2.7.10.1.2.3	P-1	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/Other/FL (days)	R&B - Disp	6.68	148			9.347				6.68	93	0.00	0	8.804			YES
2.7.10.2.1.1	P-1	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/Facility/FL (days)	R&B - Disp	5.00	1			0.000				0.00	0						
2.7.10.2.1.2	P-1	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/Equipment/FL (days)	R&B - Disp	0.00	0							0.00	0						
2.7.10.2.1.3	P-1	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/Other/FL (days)	R&B - Disp	0.00	0							0.00	0						
2.7.10.2.2.1	P-1	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/Facility/FL (days)	R&B - Disp	5.00	1			0.000				0.00	0						
2.7.10.2.2.2	P-1	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/Equipment/FL (days)	R&B - Disp	0.00	0							0.00	0						
2.7.10.2.2.3	P-1	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/Other/FL (days)	R&B - Disp	0.00	0							0.00	0						
2.7.11.1.1.1	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/Facility/FL (days)	R&B (POTS) excl SB Or	8.94	325	0.00	0	11.232			YES	8.17	456	0.00	0	8.322			YES
2.7.11.1.1.2	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/Equipment/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.11.1.1.3	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/Other/FL (days)	R&B (POTS) excl SB Or	6.68	148	0.00	0	9.347			YES	6.68	93	0.00	0	8.804			YES
2.7.11.1.2.1	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/Facility/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.11.1.2.2	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/Equipment/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.11.1.2.3	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/Other/FL (days)	R&B (POTS) excl SB Or	8.67	3	0.00	0	8.083			YES	26.50	4	0.00	0	16.902			YES
2.7.11.2.1.1	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/Facility/FL (days)	R&B (POTS) excl SB Or	0.00	0							0.00	0						
2.7.11.2.1.2	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/Equipment/FL (days)	R&B (POTS) excl SB Or	0.00	0							0.00	0						
2.7.11.2.1.3	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/Other/FL (days)	R&B (POTS) excl SB Or	0.00	0							0.00	0						
2.7.11.2.2.1	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/Facility/FL (days)	R&B (POTS) excl SB Or	0.00	0							0.00	0						
2.7.11.2.2.2	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/Equipment/FL (days)	R&B (POTS) excl SB Or	0.00	0							0.00	0						
2.7.11.2.2.3	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/Other/FL (days)	R&B (POTS) excl SB Or	0.00	0							0.00	0						
2.7.12.1.1.1	P-1	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/Facility/FL (days)	R&B - Disp	9.10	331	5.00	1	11.304	11.32071	0.3619	YES	8.23	466	0.00	0	8.261			YES
2.7.12.1.1.2	P-1	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/Equipment/FL (days)	R&B - Disp	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.12.1.1.3	P-1	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/Other/FL (days)	R&B - Disp	6.68	148	0.00	0	9.347			YES	6.68	93	0.00	0	8.804			YES
2.7.12.1.2.1	P-1	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/Facility/FL (days)	R&B - Disp	9.10	331	0.00	0	11.304			YES	8.23	466	0.00	0	8.261			YES
2.7.12.1.2.2	P-1	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/Equipment/FL (days)	R&B - Disp	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.12.1.2.3	P-1	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/Other/FL (days)	R&B - Disp	6.68	148	0.00	0	9.347			YES	6.68	93	0.00	0	8.804			YES
2.7.12.2.1.1	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/Facility/FL (days)	R&B - Disp	5.00	1	0.00	0	0.000			YES	0.00	0	0.00	0				YES
2.7.12.2.1.2	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/Equipment/FL (days)	R&B - Disp	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.12.2.1.3	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/Other/FL (days)	R&B - Disp	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.12.2.2.1	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/Facility/FL (days)	R&B - Disp	5.00	1	0.00	0	0.000			YES	0.00	0	0.00	0				YES
2.7.12.2.2.2	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/Equipment/FL (days)	R&B - Disp	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.12.2.2.3	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/Other/FL (days)	R&B - Disp	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.13.1.1.1	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/Facility/FL (days)	R&B (POTS) excl SB Or	8.94	325	0.00	0	11.232			YES	8.17	456	0.00	0	8.322			YES
2.7.13.1.1.2	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/Equipment/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.13.1.1.3	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/Other/FL (days)	R&B (POTS) excl SB Or	6.68	148	0.00	0	9.347			YES	6.68	93	0.00	0	8.804			YES
2.7.13.1.2.1	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/Facility/FL (days)	R&B (POTS) excl SB Or	0.00	0	6.00	1				NO	0.00	0	9.00	1				NO
2.7.13.1.2.2	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/Equipment/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.13.1.2.3	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/Other/FL (days)	R&B (POTS) excl SB Or	8.67	3	0.00	0	8.083			YES	26.50	4	0.00	0	16.902			YES
2.7.13.2.1.1	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/Facility/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.13.2.1.2	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/Equipment/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.13.2.1.3	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/Other/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.13.2.2.1	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/Facility/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.13.2.2.2	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/Equipment/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.13.2.2.3	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/Other/FL (days)	R&B (POTS) excl SB Or	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.14.1.1.1	P-1	Other Design/<10 circuits/Dispatch/Facility/FL (days)	Design	18.71	7	0.00	0	24.329			YES	12.56	9			12.197			
2.7.14.1.1.2	P-1	Other Design/<10 circuits/Dispatch/Equipment/FL (days)	Design	0.00	0	0.00	0				YES	0.00	0						
2.7.14.1.1.3	P-1	Other Design/<10 circuits/Dispatch/Other/FL (days)	Design	20.00	4	0.00	0	29.348			YES	15.33	9			13.058			
2.7.14.1.2.1	P-1	Other Design/<10 circuits/Non-Dispatch/Facility/FL (days)	Design	0.00	0	0.00	0				YES	0.00	0						
2.7.14.1.2.2	P-1	Other Design/<10 circuits/Non-Dispatch/Equipment/FL (days)	Design	0.00	0	0.00	0				YES	0.00	0						
2.7.14.1.2.3	P-1	Other Design/<10 circuits/Non-Dispatch/Other/FL (days)	Design	0.00	0	0.00	0				YES	0.00	0						
2.7.14.2.1.1	P-1	Other Design/>=10 circuits/Dispatch/Facility/FL (days)	Design	0.00	0							0.00	0						
2.7.14.2.1.2	P-1	Other Design/>=10 circuits/Dispatch/Equipment/FL (days)	Design	0.00	0							0.00	0						
2.7.14.2.1.3	P-1	Other Design/>=10 circuits/Dispatch/Other/FL (days)	Design	0.00	0							4.00	1			0.000			
2.7.14.2.2.1	P-1	Other Design/>=10 circuits/Non-Dispatch/Facility/FL (days)	Design	0.00	0							0.00	0						
2.7.14.2.2.2	P-1	Other Design/>=10 circuits/Non-Dispatch/Equipment/FL (days)	Design	0.00	0							0.00	0						
2.7.14.2.2.3	P-1	Other Design/>=10 circuits/Non-Dispatch/Other/FL (days)	Design	0.00	0							0.00	0						
2.7.15.1.1.1	P-1	Other Non-Design/<10 circuits/Dispatch/Facility/FL (days)	R&B	9.10	331	0.00	0	11.304			YES	8.23	466	0.00	0	8.261			YES
2.7.15.1.1.2	P-1	Other Non-Design/<10 circuits/Dispatch/Equipment/FL (days)	R&B	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.15.1.1.3	P-1	Other Non-Design/<10 circuits/Dispatch/Other/FL (days)	R&B	6.68	148	0.00	0	9.347			YES	6.68	93	0.00	0	8.804			YES
2.7.15.1.2.1	P-1	Other Non-Design/<10 circuits/Non-Dispatch/Facility/FL (days)	R&B	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.15.1.2.2	P-1	Other Non-Design/<10 circuits/Non-Dispatch/Equipment/FL (days)	R&B	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.15.1.2.3	P-1	Other Non-Design/<10 circuits/Non-Dispatch/Other/FL (days)	R&B	8.67	3	0.00	0	8.083			YES	21.60	5	0.00	0	18.284			YES
2.7.15.2.1.1	P-1	Other Non-Design/>=10 circuits/Dispatch/Facility/FL (days)	R&B	5.00	1			0.000				0.00	0						
2.7.15.2.1.2	P-1	Other Non-Design/>=10 circuits/Dispatch/Equipment/FL (days)	R&B	0.00	0							0.00	0						
2.7.15.2.1.3	P-1	Other Non-Design/>=10 circuits/Dispatch/Other/FL (days)	R&B	0.00	0							0.00	0						
2.7.15.2.2.1	P-1	Other Non-Design/>=10 circuits/Non-Dispatch/Facility/FL (days)	R&B	0.00	0							0.00	0						
2.7.15.2.2.2	P-1	Other Non-Design/>=10 circuits/Non-Dispatch/Equipment/FL (days)	R&B	0.00	0							0.00	0						
2.7.15.2.2.3	P-1	Other Non-Design/>=10 circuits/Non-Dispatch/Other/FL (days)	R&B	0.00	0							0.00	0						
2.7.16.1.1.1	P-1	INP (Standalone)/<10 circuits/Dispatch/Facility/FL (days)	R&B (POTS)	8.94	325			11.232				8.17	456			8.322			
2.7.16.1.1.2	P-1	INP (Standalone)/<10 circuits/Dispatch/Equipment/FL (days)	R&B (POTS)	0.00	0							0.00	0						
2.7.16.1.1.3	P-1	INP (Standalone)/<10 circuits/Dispatch/Other/FL (days)	R&B (POTS)	6.68	148			9.347				6.68	93			8.804			
2.7.16.1.2.1	P-1	INP (Standalone)/<10 circuits/Non-Dispatch/Facility/FL (days)	R&B (POTS)	0.00	0							0.00	0						
2.7.16.1.2.2	P-1	INP (Standalone)/<10 circuits/Non-Dispatch/Equipment/FL (days)	R&B (POTS)	0.00	0							0.00	0						
2.7.16.1.2.3	P-1	INP (Standalone)/<10 circuits/Non-Dispatch/Other/FL (days)	R&B (POTS)	8.67	3			8.083				26.50	4			16.902			
2.7.16.2.1.1	P-1	INP (Standalone)/>=10 circuits/Dispatch/Facility/FL (days)	R&B (POTS)	0.00	0							0.00	0						
2.7.16.2.1.2	P-1	INP (Standalone)/>=10 circuits/Dispatch/Equipment/FL (days)	R&B (POTS)	0.00	0							0.00	0						
2.7.16.2.1.3	P-1	INP (Standalone)/>=10 circuits/Dispatch/Other/FL (days)	R&B (POTS)	0.00	0							0.00	0						
2.7.16.2.2.1	P-1	INP (Standalone)/>=10 circuits/Non-Dispatch/Facility/FL (days)	R&B (POTS)	0.00	0							0.00	0						
2.7.16.2.2.2	P-1	INP (Standalone)/>=10 circuits/Non-Dispatch/Equipment/FL (days)	R&B (POTS)	0.00	0							0.00	0						
2.7.16.2.2.3	P-1	INP (Standalone)/>=10 circuits/Non-Dispatch/Other/FL (days)	R&B (POTS)	0.00	0							0.00	0						
2.7.17.1.1.1	P-1	LNP (Standalone)/<10 circuits/Dispatch/Facility/FL (days)	R&B (POTS)	8.94	325	0.00	0	11.232			YES	8.17	456	0.00	0	8.322			YES
2.7.17.1.1.2	P-1	LNP (Standalone)/<10 circuits/Dispatch/Equipment/FL (days)	R&B (POTS)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.17.1.1.3	P-1	LNP (Standalone)/<10 circuits/Dispatch/Other/FL (days)	R&B (POTS)	6.68	148	0.00	0	9.347			YES	6.68	93	0.00	0	8.804			YES
2.7.17.1.2.1	P-1	LNP (Standalone)/<10 circuits/Non-Dispatch/Facility/FL (days)	R&B (POTS)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.17.1.2.2	P-1	LNP (Standalone)/<10 circuits/Non-Dispatch/Equipment/FL (days)	R&B (POTS)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.17.1.2.3	P-1	LNP (Standalone)/<10 circuits/Non-Dispatch/Other/FL (days)	R&B (POTS)	8.67	3	0.00	0	8.083			YES	26.50	4	3.00	4	16.902	11.95129	1.9663	YES
2.7.17.2.1.1	P-1	LNP (Standalone)/>=10 circuits/Dispatch/Facility/FL (days)	R&B (POTS)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.17.2.1.2	P-1	LNP (Standalone)/>=10 circuits/Dispatch/Equipment/FL (days)	R&B (POTS)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.17.2.1.3	P-1	LNP (Standalone)/>=10 circuits/Dispatch/Other/FL (days)	R&B (POTS)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.17.2.2.1	P-1	LNP (Standalone)/>=10 circuits/Non-Dispatch/Facility/FL (days)	R&B (POTS)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.17.2.2.2	P-1	LNP (Standalone)/>=10 circuits/Non-Dispatch/Equipment/FL (days)	R&B (POTS)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.17.2.2.3	P-1	LNP (Standalone)/>=10 circuits/Non-Dispatch/Other/FL (days)	R&B (POTS)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.18.1.1.1	P-1	Digital Loop < DS1/<10 circuits/Dispatch/Facility/FL (days)	Digital Loop < DS1	17.96	116	0.00	0	24.694			YES	10.44	82	6.33	3	9.546	5.84343	0.3725	YES
2.7.18.1.1.2	P-1	Digital Loop < DS1/<10 circuits/Dispatch/Equipment/FL (days)	Digital Loop < DS1	1.00	1	0.00	0	0.000			YES	4.00	1	0.00	0	0.000			YES
2.7.18.1.1.3	P-1	Digital Loop < DS1/<10 circuits/Dispatch/Other/FL (days)	Digital Loop < DS1	8.76	23	0.00	0	15.817			YES	6.80	5	2.00	1	4.858	5.10294	0.9406	YES
2.7.18.1.2.1	P-1	Digital Loop < DS1/<10 circuits/Non-Dispatch/Facility/FL (days)	Digital Loop < DS1	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.18.1.2.2	P-1	Digital Loop < DS1/<10 circuits/Non-Dispatch/Equipment/FL (days)	Digital Loop < DS1	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES
2.7.18.1.2.3	P-1	Digital Loop < DS1/<10 circuits/Non-Dispatch/Other/FL (days)	Digital Loop < DS1	8.50	2	0.00	0	8.364			YES	3.00	1	0.00	0	0.000			YES
2.7.18.2.1.1	P-1	Digital Loop < DS1/>=10 circuits/Dispatch/Facility/FL (days)	Digital Loop < DS1	0.00	0							0.00	0						
2.7.18.2.1.2	P-1	Digital Loop < DS1/>=10 circuits/Dispatch/Equipment/FL (days)	Digital Loop < DS1	0.00	0							0.00	0						
2.7.18.2.1.3	P-1	Digital Loop < DS1/>=10 circuits/Dispatch/Other/FL (days)	Digital Loop < DS1	0.00	0							0.00	0						
2.7.18.2.2.1	P-1	Digital Loop < DS1/>=10 circuits/Non-Dispatch/Facility/FL (days)	Digital Loop < DS1	0.00	0							0.00	0						
2.7.18.2.2.2	P-1	Digital Loop < DS1/>=10 circuits/Non-Dispatch/Equipment/FL (days)	Digital Loop < DS1	0.00	0							0.00	0						

BellSouth Monthly Performance Summary

Florida Permanent, September 2002 - May 2002

			July								June									May			
			BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity	BST Measure	BST Volume	CLEC Measure	CLEC Volume
1.2.7.10.1.2.3	P-1	2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/Other/FL (days)	9.23	31	0.00	0	11.523			YES	9.48	54			14.821				8.63	56	0.00	0
1.2.7.10.2.1.1	P-1	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/Facility/FL (days)	0.00	0							0.00	0							1.43	7		
1.2.7.10.2.1.2	P-1	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/Equipment/FL (days)	0.00	0							0.00	0							0.00	0		
1.2.7.10.2.1.3	P-1	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/Other/FL (days)	0.00	0							0.00	0							1.43	7		
1.2.7.10.2.2.1	P-1	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0							0.00	0							0.00	0		
1.2.7.10.2.2.2	P-1	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0							0.00	0							0.00	0		
1.2.7.10.2.2.3	P-1	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/Other/FL (days)	0.00	0							0.00	0							0.00	0		
1.2.7.11.1.1.1	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/Facility/FL (days)	8.84	382	0.00	0	8.466			YES	9.15	412	0.00	0	7.989			YES	8.54	404	0.00	0
1.2.7.11.1.1.2	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.11.1.1.3	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/Other/FL (days)	9.23	31	0.00	0	11.523			YES	9.48	54	0.00	0	14.821			YES	8.92	53	0.00	0
1.2.7.11.1.2.1	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.11.1.2.2	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.11.1.2.3	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/Other/FL (days)	2.50	2	0.00	0	0.707			YES	5.00	1	0.00	0	0.000			YES	0.00	0	0.00	0
1.2.7.11.2.1.1	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/Facility/FL (days)	0.00	0							0.00	0							0.00	0		
1.2.7.11.2.1.2	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/Equipment/FL (days)	0.00	0							0.00	0							0.00	0		
1.2.7.11.2.1.3	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/Other/FL (days)	0.00	0							0.00	0							0.00	0		
1.2.7.11.2.2.1	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0							0.00	0							0.00	0		
1.2.7.11.2.2.2	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0							0.00	0							0.00	0		
1.2.7.11.2.2.3	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/Other/FL (days)	0.00	0							0.00	0							0.00	0		
1.2.7.12.1.1.1	P-1	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/Facility/FL (days)	8.84	382	0.00	0	8.466			YES	9.17	416	0.00	0	7.998			YES	6.57	407	26.00	1
1.2.7.12.1.1.2	P-1	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.12.1.1.3	P-1	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/Other/FL (days)	9.23	31	0.00	0	11.523			YES	9.48	54	0.00	0	14.821			YES	8.63	56	0.00	0
1.2.7.12.1.2.1	P-1	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/Facility/FL (days)	8.84	382	0.00	0	8.466			YES	9.17	416	0.00	0	7.998			YES	6.57	407	0.00	0
1.2.7.12.1.2.2	P-1	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.12.1.2.3	P-1	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/Other/FL (days)	9.23	31	0.00	0	11.523			YES	9.48	54	0.00	0	14.821			YES	8.63	56	0.00	0
1.2.7.12.2.1.1	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	1.43	7	0.00	0
1.2.7.12.2.1.2	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.12.2.1.3	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	1.43	7	0.00	0
1.2.7.12.2.2.1	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.12.2.2.2	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.12.2.2.3	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.13.1.1.1	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/Facility/FL (days)	8.84	382	0.00	0	8.466			YES	9.15	412	0.00	0	7.989			YES	8.54	404	0.00	0
1.2.7.13.1.1.2	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.13.1.1.3	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/Other/FL (days)	9.23	31	0.00	0	11.523			YES	9.48	54	0.00	0	14.821			YES	8.92	53	0.00	0
1.2.7.13.1.2.1	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0	20.00	1				NO	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.13.1.2.2	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.13.1.2.3	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/Other/FL (days)	2.50	2	0.00	0	0.707			YES	5.00	1	0.00	0	0.000			YES	0.00	0	0.00	0
1.2.7.13.2.1.1	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.13.2.1.2	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.13.2.1.3	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.13.2.2.1	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.13.2.2.2	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.13.2.2.3	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.14.1.1.1	P-1	Other Design/<10 circuits/Dispatch/Facility/FL (days)	8.08	12	0.00	0	5.143			YES	11.30	5			8.955				12.00	1		
1.2.7.14.1.1.2	P-1	Other Design/<10 circuits/Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0							45.88	8		
1.2.7.14.1.1.3	P-1	Other Design/<10 circuits/Dispatch/Other/FL (days)	8.86	7	0.00	0	5.843			YES	17.25	20			11.548				0.00	0		
1.2.7.14.1.2.1	P-1	Other Design/<10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0							0.00	0		
1.2.7.14.1.2.2	P-1	Other Design/<10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0							0.00	0		
1.2.7.14.1.2.3	P-1	Other Design/<10 circuits/Non-Dispatch/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0							0.00	0		
1.2.7.14.2.1.1	P-1	Other Design/>=10 circuits/Dispatch/Facility/FL (days)	0.00	0							0.00	0							0.00	0		
1.2.7.14.2.1.2	P-1	Other Design/>=10 circuits/Dispatch/Equipment/FL (days)	0.00	0							0.00	0							0.00	0		
1.2.7.14.2.1.3	P-1	Other Design/>=10 circuits/Dispatch/Other/FL (days)	0.00	0							0.00	0							0.00	0		
1.2.7.14.2.2.1	P-1	Other Design/>=10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0							0.00	0							0.00	0		
1.2.7.14.2.2.2	P-1	Other Design/>=10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0							0.00	0							0.00	0		
1.2.7.14.2.2.3	P-1	Other Design/>=10 circuits/Non-Dispatch/Other/FL (days)	0.00	0							0.00	0							0.00	0		
1.2.7.15.1.1.1	P-1	Other Non-Design/<10 circuits/Dispatch/Facility/FL (days)	8.84	382	0.00	0	8.466			YES									6.57	407	0.00	0
1.2.7.15.1.1.2	P-1	Other Non-Design/<10 circuits/Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES									0.00	0	0.00	0
1.2.7.15.1.1.3	P-1	Other Non-Design/<10 circuits/Dispatch/Other/FL (days)	9.23	31	0.00	0	11.523			YES									8.63	56	0.00	0
1.2.7.15.1.2.1	P-1	Other Non-Design/<10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES									0.00	0	0.00	0
1.2.7.15.1.2.2	P-1	Other Non-Design/<10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES									0.00	0	0.00	0
1.2.7.15.1.2.3	P-1	Other Non-Design/<10 circuits/Non-Dispatch/Other/FL (days)	2.50	2	0.00	0	0.707			YES									1.43	7		
1.2.7.15.2.1.1	P-1	Other Non-Design/>=10 circuits/Dispatch/Facility/FL (days)	0.00	0															0.00	0		
1.2.7.15.2.1.2	P-1	Other Non-Design/>=10 circuits/Dispatch/Equipment/FL (days)	0.00	0															0.00	0		
1.2.7.15.2.1.3	P-1	Other Non-Design/>=10 circuits/Dispatch/Other/FL (days)	0.00	0															0.00	0		
6.2.7.15.2.2.1	P-1	Other Non-Design/>=10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0															0.00	0		
1.2.7.15.2.2.2	P-1	Other Non-Design/>=10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0															0.00	0		
1.2.7.15.2.2.3	P-1	Other Non-Design/>=10 circuits/Non-Dispatch/Other/FL (days)	0.00	0															6.54	404		
1.2.7.16.1.1.1	P-1	INP (Standalone)/<10 circuits/Dispatch/Facility/FL (days)	8.84	382			8.466				9.15	412			7.989				0.00	0		
1.2.7.16.1.1.2	P-1	INP (Standalone)/<10 circuits/Dispatch/Equipment/FL (days)	0.00	0							0.00	0							8.92	53		
1.2.7.16.1.1.3	P-1	INP (Standalone)/<10 circuits/Dispatch/Other/FL (days)	9.23	31			11.523				9.48	54			14.821				0.00	0		
1.2.7.16.1.2.1	P-1	INP (Standalone)/<10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0							0.00	0							0.00	0		
1.2.7.16.1.2.2	P-1	INP (Standalone)/<10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0							0.00	0							0.00	0		
1.2.7.16.1.2.3	P-1	INP (Standalone)/<10 circuits/Non-Dispatch/Other/FL (days)	2.50	2			0.707				5.00	1			0.000				0.00	0		
1.2.7.16.2.1.1	P-1	INP (Standalone)/>=10 circuits/Dispatch/Facility/FL (days)	0.00	0							0.00	0							0.00	0		
1.2.7.16.2.1.2	P-1	INP (Standalone)/>=10 circuits/Dispatch/Equipment/FL (days)	0.00	0							0.00	0							0.00	0		
1.2.7.16.2.1.3	P-1	INP (Standalone)/>=10 circuits/Dispatch/Other/FL (days)	0.00	0							0.00	0							0.00	0		
1.2.7.16.2.2.1	P-1	INP (Standalone)/>=10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0							0.00	0							0.00	0		
1.2.7.16.2.2.2	P-1	INP (Standalone)/>=10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0							0.00	0							0.00	0		
1.2.7.16.2.2.3	P-1	INP (Standalone)/>=10 circuits/Non-Dispatch/Other/FL (days)	0.00	0							0.00	0							0.00	0		
1.2.7.17.1.1.1	P-1	LNP (Standalone)/<10 circuits/Dispatch/Facility/FL (days)	8.84	382	0.00	0	8.466			YES	9.15	412	0.00	0	7.989			YES	6.54	404	0.00	0
1.2.7.17.1.1.2	P-1	LNP (Standalone)/<10 circuits/Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.17.1.1.3	P-1	LNP (Standalone)/<10 circuits/Dispatch/Other/FL (days)	9.23	31	0.00	0	11.523			YES	9.48	54	0.00	0	14.821			YES	8.92	53	0.00	0
1.2.7.17.1.2.1	P-1	LNP (Standalone)/<10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.17.1.2.2	P-1	LNP (Standalone)/<10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.17.1.2.3	P-1	LNP (Standalone)/<10 circuits/Non-Dispatch/Other/FL (days)	2.50	2	0.00	0	0.707			YES	5.00	1	0.00	0	0.000			YES	0.00	0	0.00	0
1.2.7.17.2.1.1	P-1	LNP (Standalone)/>=10 circuits/Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.17.2.1.2	P-1	LNP (Standalone)/>=10 circuits/Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.17.2.1.3	P-1	LNP (Standalone)/>=10 circuits/Dispatch/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.17.2.2.1	P-1	LNP (Standalone)/>=10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.17.2.2.2	P-1	LNP (Standalone)/>=10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.17.2.2.3	P-1	LNP (Standalone)/>=10 circuits/Non-Dispatch/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.18.1.1.1	P-1	Digital Loop < DS1/<10 circuits/Dispatch/Facility/FL (days)	7.87	46	10.80	5	6.043	2.84553	-1.0296	YES	10.72	36	8.00	2	9.291	6.74968	0.4033	YES	18.00	84	19.00	1
1.2.7.18.1.1.2	P-1	Digital Loop < DS1/<10 circuits/Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.18.1.1.3	P-1	Digital Loop < DS1/<10 circuits/Dispatch/Other/FL (days)	2.25	20	0.00	0	2.403			YES	10.25	4	0.00	0	11.295			YES	1.80	10	0.00	0
1.2.7.18.1.2.1	P-1	Digital Loop < DS1/<10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.18.1.2.2	P-1	Digital Loop < DS1/<10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.18.1.2.3	P-1	Digital Loop < DS1/<10 circuits/Non-Dispatch/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0	0.00	0
1.2.7.18.2.1.1	P-1	Digital Loop < DS1/>=10 circuits/Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0		
1.2.7.18.2.1.2	P-1	Digital Loop < DS1/>=10 circuits/Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0		
1.2.7.18.2.1.3	P-1	Digital Loop < DS1/>=10 circuits/Dispatch/Other/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0		
1.2.7.18.2.2.1	P-1	Digital Loop < DS1/>=10 circuits/Non-Dispatch/Facility/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0		
1.2.7.18.2.2.2	P-1	Digital Loop < DS1/>=10 circuits/Non-Dispatch/Equipment/FL (days)	0.00	0	0.00	0				YES	0.00	0	0.00	0				YES	0.00	0		

BellSouth Monthly Performance Summary

Florida Permanent, September 2002 - May 2002

			Standard Deviation	Standard Error	Zscore	Equity
B.2.7.10.1.2.3	P-1	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/Other/FL (days)	10.304			YES
B.2.7.10.2.1.1	P-1	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/Facility/FL (days)	1.134			
B.2.7.10.2.1.2	P-1	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/Equipment/FL (days)				
B.2.7.10.2.1.3	P-1	2W Analog Loop w/INP Design/>=10 circuits/Dispatch/Other/FL (days)				
B.2.7.10.2.2.1	P-1	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/Facility/FL (days)	1.134			
B.2.7.10.2.2.2	P-1	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/Equipment/FL (days)				
B.2.7.10.2.2.3	P-1	2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/Other/FL (days)				
B.2.7.11.1.1.1	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/Facility/FL (days)	6.936			YES
B.2.7.11.1.1.2	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/Equipment/FL (days)				YES
B.2.7.11.1.1.3	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/Other/FL (days)	10.486			YES
B.2.7.11.1.2.1	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/Facility/FL (days)				YES
B.2.7.11.1.2.2	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/Equipment/FL (days)				YES
B.2.7.11.1.2.3	P-1	2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/Other/FL (days)				YES
B.2.7.11.2.1.1	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/Facility/FL (days)				
B.2.7.11.2.1.2	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/Equipment/FL (days)				
B.2.7.11.2.1.3	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/Other/FL (days)				
B.2.7.11.2.2.1	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/Facility/FL (days)				
B.2.7.11.2.2.2	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/Equipment/FL (days)				
B.2.7.11.2.2.3	P-1	2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/Other/FL (days)				
B.2.7.12.1.1.1	P-1	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/Facility/FL (days)	6.984	6.99265	-2.5353	NO
B.2.7.12.1.1.2	P-1	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/Equipment/FL (days)				YES
B.2.7.12.1.1.3	P-1	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/Other/FL (days)	10.304			YES
B.2.7.12.1.2.1	P-1	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/Facility/FL (days)	6.984			YES
B.2.7.12.1.2.2	P-1	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/Equipment/FL (days)				YES
B.2.7.12.1.2.3	P-1	2W Analog Loop w/LNP Design/<10 circuits/Non-Dispatch/Other/FL (days)	10.304			YES
B.2.7.12.2.1.1	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/Facility/FL (days)	1.134			YES
B.2.7.12.2.1.2	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/Equipment/FL (days)				YES
B.2.7.12.2.1.3	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/Other/FL (days)				YES
B.2.7.12.2.2.1	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/Facility/FL (days)	1.134			YES
B.2.7.12.2.2.2	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/Equipment/FL (days)				YES
B.2.7.12.2.2.3	P-1	2W Analog Loop w/LNP Design/>=10 circuits/Non-Dispatch/Other/FL (days)				YES
B.2.7.13.1.1.1	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/Facility/FL (days)	6.936			YES
B.2.7.13.1.1.2	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/Equipment/FL (days)				YES
B.2.7.13.1.1.3	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/Other/FL (days)	10.486			YES
B.2.7.13.1.2.1	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/Facility/FL (days)				YES
B.2.7.13.1.2.2	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/Equipment/FL (days)				YES
B.2.7.13.1.2.3	P-1	2W Analog Loop w/LNP Non-Design/<10 circuits/Non-Dispatch/Other/FL (days)				YES
B.2.7.13.2.1.1	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/Facility/FL (days)				YES
B.2.7.13.2.1.2	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/Equipment/FL (days)				YES
B.2.7.13.2.1.3	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/Other/FL (days)				YES
B.2.7.13.2.2.1	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/Facility/FL (days)				YES
B.2.7.13.2.2.2	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/Equipment/FL (days)				YES
B.2.7.13.2.2.3	P-1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Non-Dispatch/Other/FL (days)				YES
B.2.7.14.1.1.1	P-1	Other Design/<10 circuits/Dispatch/Facility/FL (days)	0.000			
B.2.7.14.1.1.2	P-1	Other Design/<10 circuits/Dispatch/Equipment/FL (days)				
B.2.7.14.1.1.3	P-1	Other Design/<10 circuits/Dispatch/Other/FL (days)	38.264			
B.2.7.14.1.2.1	P-1	Other Design/<10 circuits/Non-Dispatch/Facility/FL (days)				
B.2.7.14.1.2.2	P-1	Other Design/<10 circuits/Non-Dispatch/Equipment/FL (days)				
B.2.7.14.1.2.3	P-1	Other Design/<10 circuits/Non-Dispatch/Other/FL (days)				
B.2.7.14.2.1.1	P-1	Other Design/>=10 circuits/Dispatch/Facility/FL (days)				
B.2.7.14.2.1.2	P-1	Other Design/>=10 circuits/Dispatch/Equipment/FL (days)				
B.2.7.14.2.1.3	P-1	Other Design/>=10 circuits/Dispatch/Other/FL (days)				
B.2.7.14.2.2.1	P-1	Other Design/>=10 circuits/Non-Dispatch/Facility/FL (days)				
B.2.7.14.2.2.2	P-1	Other Design/>=10 circuits/Non-Dispatch/Equipment/FL (days)				
B.2.7.14.2.2.3	P-1	Other Design/>=10 circuits/Non-Dispatch/Other/FL (days)				
B.2.7.15.1.1.1	P-1	Other Non-Design/<10 circuits/Dispatch/Facility/FL (days)	6.984			YES
B.2.7.15.1.1.2	P-1	Other Non-Design/<10 circuits/Dispatch/Equipment/FL (days)				YES
B.2.7.15.1.1.3	P-1	Other Non-Design/<10 circuits/Dispatch/Other/FL (days)	10.304			YES
B.2.7.15.1.2.1	P-1	Other Non-Design/<10 circuits/Non-Dispatch/Facility/FL (days)				YES
B.2.7.15.1.2.2	P-1	Other Non-Design/<10 circuits/Non-Dispatch/Equipment/FL (days)				YES
B.2.7.15.1.2.3	P-1	Other Non-Design/<10 circuits/Non-Dispatch/Other/FL (days)				YES
B.2.7.15.2.1.1	P-1	Other Non-Design/>=10 circuits/Dispatch/Facility/FL (days)	1.134			
B.2.7.15.2.1.2	P-1	Other Non-Design/>=10 circuits/Dispatch/Equipment/FL (days)				
B.2.7.15.2.1.3	P-1	Other Non-Design/>=10 circuits/Dispatch/Other/FL (days)				
B.2.7.15.2.2.1	P-1	Other Non-Design/>=10 circuits/Non-Dispatch/Facility/FL (days)				
B.2.7.15.2.2.2	P-1	Other Non-Design/>=10 circuits/Non-Dispatch/Equipment/FL (days)				
B.2.7.15.2.2.3	P-1	Other Non-Design/>=10 circuits/Non-Dispatch/Other/FL (days)				
B.2.7.16.1.1.1	P-1	INP (Standalone)/<10 circuits/Dispatch/Facility/FL (days)	6.936			
B.2.7.16.1.1.2	P-1	INP (Standalone)/<10 circuits/Dispatch/Equipment/FL (days)				
B.2.7.16.1.1.3	P-1	INP (Standalone)/<10 circuits/Dispatch/Other/FL (days)	10.486			
B.2.7.16.1.2.1	P-1	INP (Standalone)/<10 circuits/Non-Dispatch/Facility/FL (days)				
B.2.7.16.1.2.2	P-1	INP (Standalone)/<10 circuits/Non-Dispatch/Equipment/FL (days)				
B.2.7.16.1.2.3	P-1	INP (Standalone)/<10 circuits/Non-Dispatch/Other/FL (days)				
B.2.7.16.2.1.1	P-1	INP (Standalone)/>=10 circuits/Dispatch/Facility/FL (days)				
B.2.7.16.2.1.2	P-1	INP (Standalone)/>=10 circuits/Dispatch/Equipment/FL (days)				
B.2.7.16.2.1.3	P-1	INP (Standalone)/>=10 circuits/Dispatch/Other/FL (days)				
B.2.7.16.2.2.1	P-1	INP (Standalone)/>=10 circuits/Non-Dispatch/Facility/FL (days)				
B.2.7.16.2.2.2	P-1	INP (Standalone)/>=10 circuits/Non-Dispatch/Equipment/FL (days)				
B.2.7.16.2.2.3	P-1	INP (Standalone)/>=10 circuits/Non-Dispatch/Other/FL (days)				
B.2.7.17.1.1.1	P-1	LNP (Standalone)/<10 circuits/Dispatch/Facility/FL (days)	6.936			YES
B.2.7.17.1.1.2	P-1	LNP (Standalone)/<10 circuits/Dispatch/Equipment/FL (days)				YES
B.2.7.17.1.1.3	P-1	LNP (Standalone)/<10 circuits/Dispatch/Other/FL (days)	10.486			YES
B.2.7.17.1.2.1	P-1	LNP (Standalone)/<10 circuits/Non-Dispatch/Facility/FL (days)				YES
B.2.7.17.1.2.2	P-1	LNP (Standalone)/<10 circuits/Non-Dispatch/Equipment/FL (days)				YES
B.2.7.17.1.2.3	P-1	LNP (Standalone)/<10 circuits/Non-Dispatch/Other/FL (days)				YES
B.2.7.17.2.1.1	P-1	LNP (Standalone)/>=10 circuits/Dispatch/Facility/FL (days)				YES
B.2.7.17.2.1.2	P-1	LNP (Standalone)/>=10 circuits/Dispatch/Equipment/FL (days)				YES
B.2.7.17.2.1.3	P-1	LNP (Standalone)/>=10 circuits/Dispatch/Other/FL (days)				YES
B.2.7.17.2.2.1	P-1	LNP (Standalone)/>=10 circuits/Non-Dispatch/Facility/FL (days)				YES
B.2.7.17.2.2.2	P-1	LNP (Standalone)/>=10 circuits/Non-Dispatch/Equipment/FL (days)				YES
B.2.7.17.2.2.3	P-1	LNP (Standalone)/>=10 circuits/Non-Dispatch/Other/FL (days)				YES
B.2.7.18.1.1.1	P-1	Digital Loop < DS1/<10 circuits/Dispatch/Facility/FL (days)	18.155	18.26303	-0.0546	YES
B.2.7.18.1.1.2	P-1	Digital Loop < DS1/<10 circuits/Dispatch/Equipment/FL (days)				YES
B.2.7.18.1.1.3	P-1	Digital Loop < DS1/<10 circuits/Dispatch/Other/FL (days)	2.830			YES
B.2.7.18.1.2.1	P-1	Digital Loop < DS1/<10 circuits/Non-Dispatch/Facility/FL (days)				YES
B.2.7.18.1.2.2	P-1	Digital Loop < DS1/<10 circuits/Non-Dispatch/Equipment/FL (days)				YES
B.2.7.18.1.2.3	P-1	Digital Loop < DS1/<10 circuits/Non-Dispatch/Other/FL (days)				
B.2.7.18.2.1.1	P-1	Digital Loop < DS1/>=10 circuits/Dispatch/Facility/FL (days)				
B.2.7.18.2.1.2	P-1	Digital Loop < DS1/>=10 circuits/Dispatch/Equipment/FL (days)				
B.2.7.18.2.1.3	P-1	Digital Loop < DS1/>=10 circuits/Dispatch/Other/FL (days)				
B.2.7.18.2.2.1	P-1	Digital Loop < DS1/>=10 circuits/Non-Dispatch/Facility/FL (days)				
B.2.7.18.2.2.2	P-1	Digital Loop < DS1/>=10 circuits/Non-Dispatch/Equipment/FL (days)				