Florida Performance Metric Data

Metric Number	Metric Name [SQM Number] and Disaggregation	May		June		July		August		September		
		BST	CLEC	BST	CLEC	BST	CLEC	BST	CLEC	BST	CLEC	Notes
B.2.1.3.1.3	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL (days)	0.33	0.33	0.33	0.33	0.33	0.33	0.33	0.33	0.33	0.33	
B.2.1.3.1.4	Loop + Port Combinations/<10 circuits/Dispatch In/FL (days)	1.61	1.20	1.54	1.18	1.61	1.23	1.55	1.23	1.90	1.38	
B.2.1.3.2.1	Loop + Port Combinations/>=10 circuits/Dispatch/FL (days)	10.00	5.06	11.41	4.20	10.61	5.36	11.32	3.37	11.16	3.33	1,4,5
B.2.1.3.2.2	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL (days)	3.38	0.44	7.04	7.00	3.10	4.11	2.67	6.50	3.89		1,2,3,4
B.2.1.3.2.4	Loop + Port Combinations/>=10 circuits/Dispatch In/FL (days)	4.98	1.00	8.77	7.00	3.81	6.00	4.27	6.50	5.95		1,2,3,4
B.2.1.4.1.1	Combo Other/<10 circuits/Dispatch/FL (days)	4.72	11.97	5.79	11.57	6.31	12.09	5.38	11.44	5.92	12.21	
B.2.1.6.3.1	UNE ISDN/<6 circuits/Dispatch/FL (days)	11.88	10.70	13.80	10.02	14.88	9.92	13.64	10.69	13.15	10.62	
B.2.1.6.4.1	UNE ISDN/6-13 circuits/Dispatch/FL (days)						12.00	12.00		11.00		3
B.2.1.7.3.1	Line Sharing/<6 circuits/Dispatch/FL (days)	3.77	6.30	2.80	4.87	2.75	6.69	2.58	3.45	2.73	3.54	
B.2.1.7.3.2	Line Sharing/<6 circuits/Non-Dispatch/FL (days)	3.49	3.81	2.32	3.77	2.33	3.65	2.20	2.16	2.38	2.20	
B.2.1.8.1.1	2W Analog Loop Design/<10 circuits/Dispatch/FL (days)	4.03	4.83	4.82	4.52	5.33	4.63	4.50	4.58	5.00	4.21	
B.2.1.8.2.1	2W Analog Loop Design/>=10 circuits/Dispatch/FL (days)	10.00	8.00	11.41	6.50	10.61	7.00	11.32	6.00	11.16	5.67	1,2,3,4,5
B.2.1.9.1.1	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL (days)	4.01	3.64	4.81	3.73	5.31	4.29	4.54	3.75	5.01	3.76	
B.2.1.9.1.4	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL (days)	1.61	3.33	1.53	3.11	1.59	3.64	1.52	3.50	1.89	4.22	4,5
B.2.1.9.2.1	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL (days)	9.03	4.55	9.89	6.14	10.18	8.08	11.42	7.92	11.33	5.90	
B.2.1.11.1.4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL (days)	1.61		1.53		1.59	4.00	1.52		1.89		3

Florida Performance Metric Data

Metric Number	Metric Name [SQM Number] and Disaggregation	May BST	May CLEC	June BST	June CLEC	July BST	July CLEC	August BST	August CLEC	September BST	September CLEC	Notes
B.2.1.12.1.1	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL (days)	4.03	5.57	4.82	5.59	5.33	5.42	4.50	5.46	5.00	4.59	
B.2.1.12.2.1	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL (days)	10.00	8.38	11.41	6.50	10.61	6.33	11.32	9.00	11.16		1,2,3,4
B.2.1.13.1.1	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL (days)	4.01	4.98	4.81	4.94	5.31	5.16	4.54	5.32	5.01	5.22	
B.2.1.13.1.4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL (days)	1.61	4.96	1.53	5.16	1.59	5.30	1.52	5.27	1.89	5.20	
B.2.1.13.2.1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL (days)	9.03	7.74	9.89	7.35	10.18	7.67	11.42	8.31	11.33	10.33	5
B.2.1.13.2.4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL (days)	5.00	7.00	16.40	7.00	1.00	7.36	2.43	7.25	2.00	7.50	1,2,4,5
B.2.1.14.1.1	Other Design/<10 circuits/Dispatch/FL (days)	21.96		22.71		23.53	17.00	25.42		22.68	20.67	3,5
B.2.1.15.1.1	Other Non-Design/<10 circuits/Dispatch/FL (days)	4.03	7.25	4.82	5.80	5.33	10.57	4.50	6.00	5.00	12.00	3,4,5
B.2.1.15.1.2	Other Non-Design/<10 circuits/Non-Dispatch/FL (days)	0.89		0.89		0.88		0.87		1.04		
B.2.1.16.1.2	INP (Standalone)/<10 circuits/Non-Dispatch/FL (days)	0.89		0.88		0.87		0.86		1.03		
B.2.1.17.1.1	LNP (Standalone)/<10 circuits/Dispatch/FL (days)	4.01	5.00	4.81	0.33	5.31	0.33	4.54	0.33	5.01		1,3,4
B.2.1.17.1.2	LNP (Standalone)/<10 circuits/Non-Dispatch/FL (days)	0.89	0.77	0.88	0.77	0.87	0.72	0.86	0.71	1.03	0.85	
B.2.1.17.2.2	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL (days)	4.07	0.33	13.72	0.57	0.50	0.33	1.00	0.89	0.83	0.33	1,2,4,5
B.2.1.18.1.1	Digital Loop < DS1/<10 circuits/Dispatch/FL (days)	4.77	8.89	3.69	7.64	3.58	7.77	3.27	8.24	3.17	7.99	

Florida Performance Metric Data

Metric Number	Metric Name [SQM Number] and Disaggregation	May BST	May CLEC	June BST	June CLEC	July BST	July CLEC	August BST	August CLEC	September BST	September CLEC	Notes
B.2.1.19.1.1	Digital Loop >= DS1/<10 circuits/Dispatch/FL (days)	28.05	6.29	20.86	6.00	21.80	6.70	7.42	6.42	13.70	6.55	
	Order Completion Interval within X days											
B.2.2.1	xDSL (ADSL, HDSL and UCL) Loop with Conditioning/<6 circuits/Dispatch/FL (days)		5.00									1
B.2.2.2	xDSL (ADSL, HDSL and UCL) Loop w/o Conditioning/<6 circuits/Dispatch/FL (days)		4.56		4.69		4.66		4.61		4.31	
	% Jeopardies - Mechanized											
B.2.5.3	Loop + Port Combinations/FL (%)	0.65%	0.15%	0.65%	0.17%	0.63%	0.13%	0.72%	0.18%	0.60%	0.15%	
B.2.5.4	Combo Other/FL (%)	7.20%	100.00%	8.99%	60.00%	8.37%	77.78%	8.87%	64.86%	8.59%	66.04%	1,2
B.2.5.5	xDSL (ADSL, HDSL and UCL)/FL (%)	7.96%		7.00%	5.33%	7.84%	4.40%	6.92%	4.32%	5.38%	5.08%	
B.2.5.6	UNE ISDN/FL (%)	8.71%	38.89%	13.86%	39.34%	11.94%	36.43%	7.68%	43.44%	10.43%	36.11%	
B.2.5.7	Line Sharing/FL (%)	7.96%	0.00%	7.00%	0.00%	7.84%	0.00%	6.92%	0.00%	5.38%	0.00%	1,2,3,4,5
B.2.5.8	2W Analog Loop Design/FL (%)	0.65%	16.84%	0.65%	13.98%	0.63%	19.08%	0.72%	18.67%	0.60%	19.79%	
B.2.5.9	2W Analog Loop Non-Design/FL (%)	1.17%	7.21%	1.21%	7.15%	1.23%	6.25%	1.38%	7.79%	1.15%	7.24%	
B.2.5.10	2W Analog Loop w/INP Design/FL (%)	0.65%		0.65%		0.63%		0.72%		0.60%		
B.2.5.12	2W Analog Loop w/LNP Design/FL (%)	0.65%	12.97%	0.65%	7.00%	0.63%	8.52%	0.72%	10.85%	0.60%	15.66%	
B.2.5.13	2W Analog Loop w/LNP Non-Design/FL (%)	1.17%	4.25%	1.21%	3.89%	1.23%	4.74%	1.38%	4.64%	1.15%	7.62%	
B.2.5.14	Other Design/FL (%)	16.39%		16.67%		15.68%		18.39%		16.01%		
B.2.5.15	Other Non-Design/FL (%)	0.65%		0.65%		0.63%	100.00%	0.72%		0.60%		3
B.2.5.16	INP (Standalone)/FL (%)	0.64%		0.64%		0.62%		0.72%		0.59%		
B.2.5.17	LNP (Standalone)/FL (%)	0.64%	0.00%	0.64%	0.00%	0.62%	0.00%	0.72%	0.00%	0.59%	0.00%	
B.2.5.18	Digital Loop < DS1/FL (%)	8.45%	38.89%	7.90%	21.13%	8.14%	24.55%	7.37%	21.51%	5.65%	19.23%	
B.2.5.19	Digital Loop >= DS1/FL (%)	8.27%	72.26%	10.03%	63.60%	13.08%	53.46%	10.18%	66.54%	10.33%	72.69%	
	% Jeopardies - Non-Mechanized											
B.2.6.2	Local Interoffice Transport/FL (%)		0.00%		0.00%		0.00%		0.00%		0.00%	3,4
B.2.6.3	Loop + Port Combinations/FL (%)		0.63%		1.84%		1.12%		1.24%		1.27%	

Florida Performance Metric Data

Metric Number	Metric Name [SQM Number] and Disaggregation	May BST	May CLEC	June BST	June CLEC	July BST	July CLEC	August BST	August CLEC	Septe BST		
B.2.6.4	Combo Other/FL (%)		55.43%		54.46%		40.00%		41.82%		52.69%	
B.2.6.5	xDSL (ADSL, HDSL and UCL)/FL (%)		6.56%		10.32%		9.14%		11.36%		17.95%	
B.2.6.6	UNE ISDN/FL (%)		23.40%		20.29%		20.59%		29.51%		20.45%	
B.2.6.7	Line Sharing/FL (%)		0.45%		0.00%		0.00%		0.72%		0.00%	
B.2.6.8	2W Analog Loop Design/FL (%)		4.85%		7.02%		6.67%		10.53%		17.54%	
B.2.6.9	2W Analog Loop Non-Design/FL (%)		3.21%		2.73%		6.67%		3.64%		6.78%	
B.2.6.10	2W Analog Loop w/INP Design/FL (%)		0.00%				0.00%		0.00%			1,3,4
B.2.6.11	2W Analog Loop w/INP Non-Design/FL (%)		0.00%		33.33%		0.00%		0.00%		0.00%	1,2,3,4,5
B.2.6.12	2W Analog Loop w/LNP Design/FL (%)		8.00%		11.11%		0.00%		15.38%		0.00%	2,5
B.2.6.13	2W Analog Loop w/LNP Non-Design/FL (%)		5.56%		0.00%		7.41%		5.88%		0.00%	
B.2.6.14	Other Design/FL (%)						0.00%		0.00%		0.00%	3,4,5
B.2.6.15	Other Non-Design/FL (%)		3.13%		8.00%		0.00%		0.00%		0.00%	3,4,5
B.2.6.17	LNP (Standalone)/FL (%)		0.00%		0.00%		0.00%		0.00%		0.00%	
B.2.6.18	Digital Loop < DS1/FL (%)		15.33%		15.16%		14.67%		23.60%		18.85%	
B.2.6.19	Digital Loop >= DS1/FL (%)		45.97%		56.99%		49.43%		51.72%		45.35%	
	Average Jeopardy Notice Interval - Mechanized											
B.2.8.3	Loop + Port Combinations/FL (hours)		121.73		117.58		138.34		133.34		159.74	
B.2.8.4	Combo Other/FL (hours)		280.03		293.61		362.44		339.80		356.47	1,2
B.2.8.5	xDSL (ADSL, HDSL and UCL)/FL (hours)				95.37		76.45		118.06		96.99	2,3,4,5
B.2.8.6	UNE ISDN/FL (hours)		314.60		324.35		327.15		318.40		332.18	
B.2.8.8	2W Analog Loop Design/FL (hours)		200.55		147.19		173.25		173.69		184.88	
B.2.8.9	2W Analog Loop Non-Design/FL (hours)		113.61		99.83		153.12		111.21		127.21	
B.2.8.12	2W Analog Loop w/LNP Design/FL (hours)		163.91		178.08		153.69		141.32		176.90	

Florida Performance Metric Data

Metric Number	Metric Name [SQM Number] and Disaggregation	May BST	May CLEC	June BST	June CLEC	July BST	July CLEC	August BST	August CLEC	September BST	September CLEC	Notes
B.2.8.13	2W Analog Loop w/LNP Non-Design/FL (hours)		149.60		112.94		150.39		120.06		158.49	
B.2.8.18	Digital Loop < DS1/FL (hours)		314.60		298.43		312.96		301.12		304.98	
B.2.8.19	Digital Loop >= DS1/FL (hours)		182.61		188.20		198.27		185.40		203.47	
	Average Jeopardy Notice Interval - Non-Mechanized											
B.2.9.3	Loop + Port Combinations/FL (hours)		196.35		149.99		137.32		126.05		168.22	
B.2.9.4	Combo Other/FL (hours)		317.51		339.52		366.99		382.68		339.44	
B.2.9.5	xDSL (ADSL, HDSL and UCL)/FL (hours)		126.33		174.12		195.53		121.84		238.78	4,5
B.2.9.6	UNE ISDN/FL (hours)		272.17		272.64		297.07		254.13		293.25	
B.2.9.8	2W Analog Loop Design/FL (hours)		101.24		90.25		226.85		106.08		203.11	1,2,3,4
B.2.9.9	2W Analog Loop Non-Design/FL (hours)		142.18		121.13		245.21		300.11		188.34	1,2,3,4,5
B.2.9.12	2W Analog Loop w/LNP Design/FL (hours)		127.73		206.80				138.84			1,2,4
B.2.9.18	Digital Loop < DS1/FL (hours)		246.40		248.58		277.03		243.50		291.21	
B.2.9.19	Digital Loop >= DS1/FL (hours)		221.03		257.18		219.12		246.22		325.73	
	% Jeopardy Notice >= 48 hours - Mechanized											
B.2.10.3	Loop + Port Combinations/FL (%)		70.00%		88.71%		98.21%		98.68%		100.00%	
B.2.10.4	Combo Other/FL (%)		100.00%		100.00%		100.00%		100.00%		100.00%	1,2
B.2.10.5	xDSL (ADSL, HDSL and UCL)/FL (%)				83.33%		100.00%		100.00%		87.50%	2,3,4,5
B.2.10.6	UNE ISDN/FL (%)		96.61%		97.87%		100.00%		100.00%		100.00%	
B.2.10.8	2W Analog Loop Design/FL (%)		93.62%		100.00%		96.36%		100.00%		100.00%	
B.2.10.9	2W Analog Loop Non-Design/FL (%)		96.20%		97.62%		98.18%		98.28%		92.16%	
B.2.10.12	2W Analog Loop w/LNP Design/FL (%)		100.00%		100.00%		100.00%		100.00%		100.00%	
B.2.10.13	2W Analog Loop w/LNP Non-Design/FL (%)		96.30%		100.00%		90.63%		100.00%		100.00%	
B.2.10.18	Digital Loop < DS1/FL (%)		96.61%		96.23%		100.00%		100.00%		98.31%	
B.2.10.19	Digital Loop >= DS1/FL (%)		100.00%		99.39%		99.41%		99.44%		100.00%	
	% Jeopardy Notice >= 48 hours - Non-Mechanized											

Florida Performance Metric Data

Metric Number	Metric Name [SQM Number] and Disaggregation	May BST	May CLEC	June BST	June CLEC	July BST	July CLEC	August BST	August CLEC	September BST	September CLEC	Notes
B.2.11.3	Loop + Port Combinations/FL (%)		100.00%		86.96%		92.31%		92.00%		91.67%	
B.2.11.4	Combo Other/FL (%)		100.00%		100.00%		100.00%		100.00%		100.00%	
B.2.11.5	xDSL (ADSL, HDSL and UCL)/FL (%)		82.35%		83.33%		92.31%		80.00%		100.00%	4,5
B.2.11.6	UNE ISDN/FL (%)		98.55%		95.83%		100.00%		96.97%		87.50%	
B.2.11.8	2W Analog Loop Design/FL (%)		100.00%		100.00%		100.00%		100.00%		100.00%	1,2,3,4
B.2.11.9	2W Analog Loop Non-Design/FL (%)		83.33%		50.00%		80.00%		100.00%		100.00%	1,2,3,4,5
B.2.11.12	2W Analog Loop w/LNP Design/FL (%)		100.00%		100.00%				100.00%			1,2,4
B.2.11.18	Digital Loop < DS1/FL (%)		96.43%		93.75%		98.04%		94.29%		90.00%	
B.2.11.19	Digital Loop >= DS1/FL (%)		100.00%		100.00%		100.00%		100.00%		97.56%	
	Coordinated Customers Conversions											
B.2.12.2	Loops with LNP/FL (%)		99.84%		100.00%		100.00%		100.00%		99.95%	
	% Hot Cuts > 15 minutes Early											
B.2.13.1	Time-Specific SL1/FL (%)		0.20%		0.49%		0.20%		0.00%		0.22%	
B.2.13.2	Time-Specific SL2/FL (%)		2.86%		0.00%		0.00%		6.25%		0.00%	5
B.2.13.3	Non-Time Specific SL1/FL (%)		0.00%		0.00%		0.00%		0.00%		0.00%	
B.2.13.4	Non-Time Specific SL2/FL (%)		0.00%		0.00%		0.00%		0.00%		0.00%	
	Hot Cut Timeliness											
B.2.14.1	Time-Specific SL1/FL (%)		98.58%		97.07%		99.80%		100.00%		99.78%	
B.2.14.2	Time-Specific SL2/FL (%)		97.14%		95.24%		100.00%		93.75%		100.00%	5
B.2.14.3	Non-Time Specific SL1/FL (%)		100.00%		100.00%		100.00%		100.00%		100.00%	
B.2.14.4	Non-Time Specific SL2/FL (%)		100.00%		100.00%		100.00%		100.00%		100.00%	
	% Hot Cuts > 15 minutes Late											
B.2.15.1	Time-Specific SL1/FL (%)		1.22%		2.44%		0.00%		0.00%		0.00%	
B.2.15.2	Time-Specific SL2/FL (%)		0.00%		4.76%		0.00%		0.00%		0.00%	5
B.2.15.3	Non-Time Specific SL1/FL (%)		0.00%		0.00%		0.00%		0.00%		0.00%	
B.2.15.4	Non-Time Specific SL2/FL (%)		0.00%		0.00%		0.00%		0.00%		0.00%	
	Average Recovery Time - CCC											
B.2.16.2	Loops with LNP/FL (minutes)		454.23		553.91		661.27		284.27		322.00	
	% Provisioning Troubles within 7 Days - Hot Cuts											
B.2.17.1.1	UNE Loop Design/Dispatch/FL (%)		2.55%		2.70%		2.10%		4.09%		4.62%	

Florida Performance Metric Data

Metric Number	Metric Name [SQM Number] and Disaggregation	May		June		July		August		September		
		BST	CLEC	BST	CLEC	BST	CLEC	BST	CLEC	BST	CLEC	Notes
B.2.17.2.1	UNE Loop Non-Design/Dispatch/FL (%)		1.22%		0.76%		1.18%		1.10%		0.86%	
B.2.17.2.2	UNE Loop Non-Design/Non-Dispatch/FL (%)		0.52%		0.27%		0.25%		0.40%		0.46%	
	% Missed Installation Appointments											
B.2.18.2.1.1	Local Interoffice Transport/<10 circuits/Dispatch/FL (%)	0.76%	0.00%	0.95%	0.00%	1.15%	0.00%	1.51%	0.00%	0.72%	0.00%	3,4
B.2.18.3.1.1	Loop + Port Combinations/<10 circuits/Dispatch/FL (%)	3.41%	3.67%	5.40%	4.08%	6.35%	5.39%	5.40%	5.06%	5.31%	6.14%	
B.2.18.3.1.2	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL (%)	0.10%	0.20%	0.03%	0.13%	0.02%	0.09%	0.02%	0.19%	0.14%	0.09%	
B.2.18.3.1.3	Loop + Port Combinations/<10 circuits/Switch Rased Orders/FL (%)	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	
B.2.18.3.1.4	Loop + Port Combinations/<10 circuits/Dispatch In/FL (%)	0.19%	0.43%	0.06%	0.22%	0.05%	0.14%	**0.04%**	**0.29%**	**0.28%**	**0.11%**	
B.2.18.3.2.1	Loop + Port Combinations/>=10 circuits/Dispatch/FL (%)	3.63%	9.09%	8.64%	0.00%	6.11%	**14.29%**	**3.35%**	**7.14%**	**5.37%**	**7.69%**	
B.2.18.3.2.2	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL (%)	0.00%	0.00%	0.00%	0.00%	0.00%	**0.00%**	0.00%	0.00%	0.58%	25.00%	1,2,3,4,5
B.2.18.3.2.4	Loop + Port Combinations/>=10 circuits/Dispatch In/FL (%)	0.00%	0.00%	0.00%	0.00%	0.00%	**0.00%**	0.00%	0.00%	0.92%	25.00%	1,2,3,4,5
B.2.18.4.1.1	Combo Other/<10 circuits/Dispatch/FL (%)	3.38%	4.27%	5.26%	3.70%	6.19%	**1.06%**	5.30%	2.20%	5.20%	2.29%	
B.2.18.5.1.1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL (%)	3.32%	1.77%	4.67%	2.21%	5.51%	**2.66%**	3.10%	2.58%	4.17%	0.95%	
B.2.18.6.1.1	UNE ISDN/<10 circuits/Dispatch/FL (%)	2.66%	2.59%	7.00%	2.11%	5.48%	**2.32%**	**347%**	**6.50%**	**3.11%**	**4.08%**	
B.2.18.7.1.1	Line Sharing/<10 circuits/Dispatch/FL (%)	3.32%	12.86%	4.67%	5.41%	5.51%	**8.57%**	**3.10%**	**0.00%**	**4.17%**	**2.22%**	
B.2.18.7.1.2	Line Sharing/<10 circuits/Non-Dispatch/FL (%)	0.00%	0.00%	0.00%	0.00%	0.02%	**0.00%**	0.05%	0.00%	0.07%	0.36%	
B.2.18.8.1.1	2W Analog Loop Design/<10 circuits/Dispatch/FL (%)	3.41%	1.30%	**5.40%**	**1.38%**	**6.35%**	**2.12%**	5.40%	2.31%	5.31%	0.83%	

Florida Performance Metric Data

Metric Number	Metric Name [SQM Number] and Disaggregation	May BST	May CLEC	June BST	June CLEC	July BST	July CLEC	August BST	August CLEC	September BST	September CLEC	
B.2.18.8.2.1	2W Analog Loop Design/>=10 circuits/Dispatch/FL (%)	3.63%	0.00%	8.64%	0.00%	6.11%	0.00%	3.35%	0.00%	5.37%	0.00%	1,2,3,4,5
B.2.18.9.1.1	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL (%)	3.41%	2.47%	5.38%	2.30%	6.37%	1.90%	5.53%	1.46%	5.34%	1.82%	
B.2.18.9.1.4	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL (%)	0.19%	0.00%	0.06%	0.00%	0.05%	0.00%	0.04%	0.00%	0.28%	0.00%	4,5
B.2.18.9.2.1	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL (%)	3.92%	2.04%	4.63%	5.00%	6.10%	3.45%	3.96%	5.00%	4.63%	0.00%	
		3.41%	0.00%	5.40%		6.35%	0.00%	5.40%	0.00%	5.31%		1,3,4
B.2.18.11.1.1	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL (%)	3.41%	0.00%	5.38%	0.00%	6.37%		5.53%	0.00%	5.34%	0.00%	1,2,4,5
B.2.18.11.1.4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL (%)	0.19%	0.00%	0.06%	0.00%	0.05%	0.00%	0.04%	0.00%	0.28%		1,2,3,4
B.2.18.12.1.1	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL (%)	3.41%	0.27%	5.40%	1.22%	6.35%	0.54%	5.40%	0.81%	5.31%	1.22%	
B.2.18.12.2.1	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL (%)	3.63%	7.69%	8.64%	0.00%	6.11%	0.00%	3.35%	11.11%	5.37%	0.00%	4,5
B.2.18.13.1.1	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL (%)	3.41%	0.36%	5.38%	0.23%	6.37%	0.24%	5.53%	0.70%	5.34%	1.15%	
B.2.18.13.1.4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL (%)	0.19%	0.23%	0.06%	0.00%	0.05%	0.00%	0.04%	0.42%	0.28%	0.94%	
B.2.18.13.2.1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL (%)	3.92%	0.00%	4.63%	0.00%	6.10%	0.00%	3.96%	0.00%	4.63%	0.00%	

Florida Performance Metric Data

Metric Number	Metric Name [SQM Number] and Disaggregation	May		June		July		August		September		
		BST	CLEC	BST	CLEC	BST	CLEC	BST	CLEC	BST	CLEC	Notes
B.2.18.13.2.4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL (%)	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	1,2,4,5
B.2.18.14.1.1	Other Design/<10 circuits/Dispatch/FL (%)	2.55%		2.62%		3.09%	0.00%	2.75%		3.00%	0.00%	3,5
B.2.18.15.1.1	Other Non-Design/<10 circuits/Dispatch/FL (%)	3.41%	0.00%	5.40%	0.00%	6.35%	9.09%	5.40%	0.00%	5.31%	100.00%	4,5
B.2.18.15.1.2	Other Non-Design/<10 circuits/Non-Dispatch/FL (%)	0.10%		0.03%		0.02%		0.02%		0.14%		
B.2.18.16.1.2	INP (Standalone)/<10 circuits/Non-Dispatch/FL (%)	0.09%		0.03%		0.02%		0.02%		0.13%		
B.2.18.17.1.1	LNP (Standalone)/<10 circuits/Dispatch/FL (%)	3.41%	0.00%	5.38%	0.00%	6.37%	0.00%	5.53%	0.00%	5.34%	0.00%	1,3,4,5
B.2.18.17.1.2	LNP (Standalone)/<10 circuits/Non-Dispatch/FL (%)	0.09%	0.27%	0.03%	0.00%	0.02%	0.13%	0.02%	0.03%	0.13%	0.00%	
B.2.18.17.2.1	LNP (Standalone)/>=10 circuits/Dispatch/FL (%)	3.92%		4.63%		6.10%		3.96%		4.63%		
B.2.18.17.2.2	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL (%)	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	5
B.2.18.18.1.1	Digital Loop < DS1/<10 circuits/Dispatch/FL (%)	3.36%	2.20%	4.72%	1.89%	5.45%	2.53%	3.10%	4.64%	4.14%	2.67%	
B.2.18.19.1.1	Digital Loop >= DS1/<10 circuits/Dispatch/FL (%)	0.60%	2.16%	0.00%	1.81%	1.30%	3.15%	0.69%	4.01%	1.33%	4.37%	
	% Provisioning Troubles within 30 Days											
B.2.19.2.1.1	Local Interoffice Transport/<10 circuits/Dispatch/FL (%)	7.41%	5.00%	7.36%	0.00%	6.25%	7.14%	8.37%	0.00%	6.67%	0.00%	4,5
B.2.19.3.1.1	Loop + Port Combinations/<10 circuits/Dispatch/FL (%)	10.41%	8.56%	11.60%	8.87%	11.21%	9.06%	10.60%	9.36%	9.22%	8.96%	
B.2.19.3.1.2	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL (%)	3.41%	3.75%	3.54%	2.55%	4.28%	2.70%	4.44%	2.57%	4.38%	3.06%	
B.2.19.3.1.3	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL (%)	3.81%	3.92%	3.83%	2.29%	4.30%	2.03%	4.34%	2.07%	4.12%	3.12%	

Florida Performance Metric Data

Metric Number	Metric Name [SQM Number] and Disaggregation	May BST	May CLEC	June BST	June CLEC	July BST	July CLEC	August BST	August CLEC	September BST	September CLEC	Notes
2.19.3.1	Loop + Po In/FL (%)	2.98%	3.58%	3.24%	2.82%	4.26%	3.15%	4.55%	2.86%	4.68%	3.02%	
2.19.3.2.1	Loop (%)	21.66%	11.11%	21.09%	9.09%	20.68%	0.00%	16.29%	7.14%	18.53%	28.57%	1
2.19.3.2.2	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL (%)	5.65%	0.00%	11.85%	0.00%	7.56%	0.00%	5.99%	0.00%	6.51%	0.00%	1,2,3,4,5
2.19.3.2.3	Port Combi circuits/Switch Based Orders/FL (%)	7.25%		7.78%	0.00%	5.71%		5.36%	0.00%	6.67%		2,4
B.2.19.3.2.4	Loop +Port Combinations/>=10 circuits/Dispatch In/FL (%)	4.97%	0.00%	13.89%	0.00%	8.03%	0.00%	6.16%	0.00%	6.40%	0.00%	1,2,3,4,5
B.2.19.4.1.1	Combo Other/<10 circuits/Dispatch/FL (%)	10.27%	8.05%	11.39%	9.15%	10.96%	15.74%	10.44%	19.15%	9.09%	9.89%	
B.2.19.4.1.4	Combo Other/<10 circuits/Dispatch In/FL (%)	10.27%		11.39%		10.96%		10.44%		9.09%		
B.2.19.5.1.1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL (%)	4.32%	6.27%	4.56%	5.32%	4.95%	5.15%	4.52%	7.22%	3.99%	5.15%	
B.2.19.6.1.1	UNE ISDN/<10 circuits/Dispatch/FL (%)	7.84%	8.78%	6.08%	7.33%	9.73%	8.80%	10.37%	8.70%	3.27%	8.13%	
B.2.19.7.1.1	Line Sharing/<10 circuits/Dispatch/FL (%)	4.32%	25.00%	4.56%	32.86%	4.95%	27.03%	4.52%	37.14%	3.99%	28.00%	
B.2.19.7.1.2	Line Sharing/<10 circuits/Non-Dispatch/FL (%)	3.01%	11.57%	3.33%	11.54%	2.88%	10.08%	2.05%	16.07%	1.80%	10.87%	
B.2.19.8.1.1	2W Analog Loop Design/<10 circuits/Dispatch/FL (%)	10.41%	10.28%	11.60%	7.55%	11.21%	13.54%	10.60%	10.34%	9.22%	9.57%	
B.2.19.8.2.1	2W Analog Loop Design/>=10 circuits/Dispatch/FL (%)	21.66%	28.57%	21.09%	25.00%	20.68%	20.00%	16.29%	0.00%	18.53%	50.00%	1,2,3,4,5
B.2.19.9.1.1	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL (%)	10.40%	6.53%	11.63%	7.61%	11.23%	10.00%	10.62%	12.18%	9.37%	8.96%	
B.2.19.9.1.4	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL (%)	2.97%	11.90%	3.24%	12.00%	4.25%	6.45%	4.56%	6.67%	4.69%	22.22%	5
B.2.19.9.2.1	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL (%)	21.88%	20.00%	21.41%	20.41%	20.71%	10.00%	18.04%	17.24%	20.58%	15.00%	

Florida Performance Metric Data

Metric Number	Metric Name [SQM Number] and Disaggregation	May BST	May CLEC	June BST	June CLEC	July BST	July CLEC	August BST	August CLEC	September BST	September CLEC	Notes
B.2.19.10.1.1	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL (%)		0.00%	11.60%	0.00%	11.21%		10.60%	0.00%	9.22%	0.00%	1,2,4,5
B.2.19.11.1.1	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL (%)	10.40%	0.00%	11.63%	0.00%	11.23%	0.00%	10.62%		9.37%	100.00%	1,2,3,5
B.2.19.11.1.4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL (%)	2.97%	0.00%	3.24%	0.00%	4.25%	0.00%	4.56%	0.00%	4.69%	0.00%	1,2,3,4,5
B.2.19.11.2.1	2W Analog Loop w/INP Non-Design/>=10 circuits/Dispatch/FL (%)	21.88%		21.41%		20.71%		18.04%		20.58%		
B.2.19.12.1.1	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL (%)	10.41%	7.00%	11.60%	10.11%	11.21%	11.02%	10.60%	10.87%	9.22%	14.52%	
B.2.19.12.2.1	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL (%)	21.66%	16.67%	21.09%	30.77%	20.68%	0.00%	16.29%	30.00%	18.53%	44.44%	1,5
B.2.19.13.1.1	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL (%)	10.40%	3.51%	11.63%	3.99%	11.23%	4.12%	10.62%	4.94%	9.37%	5.26%	
B.2.19.13.1.	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL (%)	2.97%	3.36%	3.24%	3.18%	4.25%	3.65%	4.56%	3.79%	4.69%	3.38%	
B.2.19.13.2.1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL (%)	21.88%	8.33%	21.41%	19.35%	20.71%	12.50%	18.04%	5.88%	20.58%	5.56%	
B.2.19.13.2.	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL (%)	0.00%	5.56%	10.00%	0.00%	5.56%	0.00%	0.00%	23.08%	12.50%	12.50%	2,3,5
B.2.19.14.1.1	Other Design/<10 circuits/Dispatch/FL (%)	6.62%	100.00%	5.42%		5.96%		7.41%	0.00%	5.95%		1,4
B.2.19.15.1.1	Other Non-Design/<10 circuits/Dispatch/FL (%)	10.41%	36.54%	11.60%	23.08%	11.21%	8.00%	10.60%	9.09%	9.22%	0.00%	5

Metric Number	Metric Name [SQM Number] and Disaggregation	May		June		July		August		September		
		BST	CLEC	BST	CLEC	BST	CLEC	BST	CLEC	BST	CLEC	Notes
B.2.19.15.1.2	Other Non-Design/<10 circuits/Non-Dispatch/FL (%)	3.41%	0.00%	3.54%		4.28%		4.44%		4.38%		1
B.2.19.15.2.1	Other Non-Design/>=10 circuits/Dispatch/FL (%)	21.66%		21.09%		20.68%		16.29%		18.53%		
B.2.19.16.1.2	INP (Standalone)/<10 circuits/Non-Dispatch/FL (%)	3.41%	100.00%	3.54%		4.28%		4.44%		4.38%		1
B.2.19.17.1.1	LNP (Standalone)/<10 circuits/Dispatch/FL (%)	10.40%	0.00%	11.63%	0.00%	11.23%	0.00%	10.62%	0.00%	9.37%	0.00%	1,2,4,5
B.2.19.17.1.2	LNP (Standalone)/<10 circuits/Non-Dispatch/FL (%)	3.41%	0.00%	3.54%	0.00%	4.28%	0.00%	4.44%	0.00%	4.38%	0.00%	
B.2.19.17.2.1	LNP (Standalone)/>=10 circuits/Dispatch/FL (%)	21.88%		21.41%		20.71%		18.04%		20.58%		
B.2.19.17.2.2	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL (%)	5.00%	0.00%	8.33%	0.00%	4.76%	0.00%	11.11%	0.00%	4.35%	0.00%	
B.2.19.18.1.1	Digital Loop < DS1/<10 circuits/Dispatch/FL (%)	4.63%	7.22%	4.63%	6.61%	5.18%	6.99%	4.81%	8.28%	4.03%	6.96%	
B.2.19.19.1.1	Digital Loop >= DS1/<10 circuits/Dispatch/FL (%)	6.89%	11.17%	7.49%	9.31%	5.41%	10.57%	6.36%	9.93%	2.07%	12.04%	
	Average Completion Notice Interval - Mechanized											
B.2.21.3.1.1	Loop + Port Combinations/<10 circuits/Dispatch/FL (hours)	3.68	0.44	4.33	1.23	6.54	0.45	5.77	0.31	4.93	0.04	
B.2.21.3.1.2	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL (hours)	1.12	0.75	1.25	0.83	1.32	0.79	1.07	0.68	0.96	0.63	
B.2.21.3.1.3	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL (hours)	1.25	0.77	1.37	0.87	1.45	0.81	1.16	0.66	1.04	0.65	
B.2.21.3.1.4	Loop + Port Combinations/<10 circuits/Dispatch In/FL (hours)	0.99	0.73	1.13	0.80	1.15	0.77	0.96	0.69	0.88	0.62	
B.2.21.3.2.1	Loop + Port Combinations/>=10 circuits/Dispatch/FL (hours)	8.15	2.21	13.65	0.37	8.63	2.00	9.59	0.23	4.07	0.02	1,2,3,5
B.2.21.3.2.2	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL (hours)	3.05		1.89	1.37	1.06	0.02	1.90	0.28	2.94	0.50	2,3,4,5
B.2.21.3.2.4	Loop + Port Combinations/>=10 circuits/Dispatch In/FL (hours)	3.14		2.05	1.37	1.16	0.02	0.79	0.28	1.84	0.50	2,3,4,5

Florida Performance Metric Data

Metric Number	Metric Name [SQM Number] and Disaggregation	May		June		July		August		September		
		BST	CLEC	BST	CLEC	BST	CLEC	BST	CLEC	BST	CLEC	Notes
B.2.21.4.1.1	Combo Other/<10 circuits/Dispatch/FL (hours)	8.74	65.88	12.80	217.28	16.21	76.88	14.99	46.41	14.40	58.23	1,2
B.2.21.5.1.1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL (hours)	9.17		6.40	13.28	4.08	21.89	3.96	14.79	4.65	15.16	
B.2.21.6.1.1	UNE ISDN/<10 circuits/Dispatch/FL (hours)	42.99	9.38	38.49	23.83	45.23	14.31	23.11	28.46	47.21	22.90	
B.2.21.7.1.1	Line Sharing/<10 circuits/Dispatch/FL (hours)	9.17	17.02	6.40		4.08	0.43	3.96		4.65	0.02	1,3,5
B.2.21.7.1.2	Line Sharing/<10 circuits/Non-Dispatch/FL (hours)	1.15	0.53	1.52	0.55	1.22	0.57	0.81	0.54	1.12	0.45	1,2,3,4,5
B.2.21.8.1.1	2W Analog Loop Design/<10 circuits/Dispatch/FL (hours)	3.68	10.03	4.33	8.32	6.54	20.82	5.77	12.93	4.93	14.31	
B.2.21.8.2.1	2W Analog Loop Design/>=10 circuits/Dispatch/FL (hours)	8.15	0.02	13.65	0.02	8.63	21.33	9.59	46.23	4.07	71.48	1,2,3,4,5
8.2.21.9.1.1	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL (hours)	3.63	0.51	4.30	1.51	6.53	0.41	5.64	0.23	4.91	0.06	
B.2.21.9.1.4	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL (hours)	0.98	0.40	1.11	1.92	1.14	0.17	0.95	0.06	0.87	0.02	4,5
B.2.21.9.2.1	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL (hours)	8.62	**1.01**	13.37	2.48	6.53	0.39	4.24	0.41	3.65	0.03	
B.2.21.12.1.1	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL (hours)	3.68	16.77	4.33	7.69	6.54	11.81	5.77	10.83	4.93	13.81	
8.2.21.12.2.1	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL (hours)	8.15	23.35	13.65	0.56	8.63	0.02	9.59	18.32	4.07	356.47	3,4,5
B.2.21.13.1.1	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL (hours)	3.63	0.99	4.30	1.58	6.53	0.47	5.64	0.22	4.91	0.04	
B.2.21.13.1.4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL (hours)	0.98	**1.01**	1.11	1.39	1.14	0.29	0.95	0.17	0.87	0.03	

Florida Performance Metric Data

Metric Number	Metric Name [SQM Number] and Disaggregation	May BST	May CLEC	June BST	June CLEC	July BST	July CLEC	August BST	August CLEC	September BST	September CLEC	Notes
B.2.21.13.2.1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL (hours)	8.62	0.62	13.37	1.22	6.53	0.18	4.24	0.23	3.65	0.02	
B.2.21.13.2.4	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch In/FL (hours)	16.75	0.08	2.58	1.16	0.30	0.11	0.51	0.44	0.32	0.05	1,2,4,5
B.2.21.15.1.2	Other Non-Design/<10 circuits/Non-Dispatch/FL (hours)	1.12		1.25		1.32		1.07		0.96		
B.2.21.17.1.1	LNP (Standalone)/<10 circuits/Dispatch/FL (hours)	3.63	0.02	4.30	4.21	6.53	0.81	5.64		4.91		1,2,3
B.2.21.17.1.2	LNP (Standalone)/<10 circuits/Non-Dispatch/FL (hours)	1.11	0.72	1.24	0.67	1.31	0.62	1.06	0.60	0.96	0.60	
B.2.21.17.2.2	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL (hours)	14.07	1.23	2.60	0.44	0.71	0.62	15.58	0.50	0.35	0.55	1,2,3,4,5
B.2.21.18.1.1	Digital Loop < DS1/<10 circuits/Dispatch/FL (hours)	14.43	9.38	11.78	18.82	7.95	17.33	8.62	21.21	7.21	18.81	
B.2.21.19.1.1	Digital Loop >= DS1/<10 circuits/Dispatch/FL (hours)	259.55	24.59	278.46	25.71	207.20	41.38	65.83	28.24	137.11	51.59	
	Average Completion Notice Interval - Non-Mechanized											
B.2.22.2.1.1	Local Interoffice Transport/<10 circuits/Dispatch/FL (hours)		34.44		24.85		13.96		86.00		27.65	3,4
B.2.22.3.1.1	Loop + Port Combinations/<10 circuits/Dispatch/FL (hours)		17.07		15.87		45.16		17.79		20.94	
B.2.22.3.1.2	Loop + Port Combinations/<10 circuits/Non-Dispatch/FL (hours)		10.09		12.11		9.43		9.77		10.15	
B.2.22.3.1.3	Loop + Port Combinations/<10 circuits/Switch Based Orders/FL (hours)		10.53		11.32		7.13		7.85		7.49	
B.2.22.3.1.4	Loop + Port Combinations/<10 circuits/Dispatch In/FL (hours)		8.93		13.70		13.93		13.72		14.72	
B.2.22.3.2.1	Loop + Port Combinations/>=10 circuits/Dispatch/FL (hours)		19.03		26.35		13.04		10.56		94.59	1,2,3,4,5
B.2.22.3.2.2	Loop + Port Combinations/>=10 circuits/Non-Dispatch/FL (hours)		6.09		8.06		10.99		28.43		223.47	1,2,3,4,5

Florida Performance Metric Data

Metric Number	Metric Name [SQM Number] and Disaggregation	May		June		July		August		September		
		BST	CLEC	BST	CLEC	BST	CLEC	BST	CLEC	BST	CLEC	Notes
B.2.22.3.2.4	Loop + Port Combinations/>=10 circuits/Dispatch In/FL (hours)		13.67		8.06		21.60		28.43		223.47	1,2,3,4,5
B.2.22.4.1.1	Combo Other/<10 circuits/Dispatch/FL (hours)		55.67		71.75		65.14		53.99		67.12	
B.2.22.5.1.1	xDSL (ADSL, HDSL and UCL)/<10 circuits/Dispatch/FL (hours)		27.78		30.12		22.19		32.08		30.20	
B.2.22.6.1.1	UNE ISDN/<10 circuits/Dispatch/FL (hours)		35.65		28.44		32.57		37.04		46.61	
B.2.22.7.1.1	Line Sharing/<10 circuits/Dispatch/FL (hours)		4.20		6.81		19.96		2.28		1.97	
B.2.22.7.1.2	Line Sharing/<10 circuits/Non-Dispatch/FL (hours)		0.73		0.68		1.06		2.37		2.13	
B.2.22.8.1.1	2W Analog Loop Design/<10 circuits/Dispatch/FL (hours)		27.00		26.59		33.77		82.30		28.60	
B.2.22.8.2.1	2W Analog Loop Design/>=10 circuits/Dispatch/FL (hours)		20.13		18.32				17.37		23.26	1,2,4,5
B.2.22.9.1.1	2W Analog Loop Non-Design/<10 circuits/Dispatch/FL (hours)		21.47		18.72		18.40		17.73		15.41	
B.2.22.9.1.4	2W Analog Loop Non-Design/<10 circuits/Dispatch In/FL (hours)		24.07		19.13		19.41		6.99		21.22	3,4,5
B.2.22.9.2.1	2W Analog Loop Non-Design/>=10 circuits/Dispatch/FL (hours)		7.06		6.08		12.56		0.04		0.02	2,4,5
B.2.22.10.1.1	2W Analog Loop w/INP Design/<10 circuits/Dispatch/FL (hours)		15.32				17.25		21.00			1,3,4
B.2.22.11.1.1	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch/FL (hours)		17.82		17.90				22.08		22.94	1,2,4,5
B.2.22.11.1.4	2W Analog Loop w/INP Non-Design/<10 circuits/Dispatch In/FL (hours)		15.83		14.62		21.05		20.75			1,2,3,4

Florida Performance Metric Data

Metric Number	Metric Name [SQM Number] and Disaggregation	May BST	May CLEC	June BST	June CLEC	July BST	July CLEC	August BST	August CLEC	September BST	September CLEC	Notes
B.2.22.12.1.1	2W Analog Loop w/LNP Design/<10 circuits/Dispatch/FL (hours)		25.88		43.16		36.89		55.29		55.73	4,5
B.2.22.12.2.1	2W Analog Loop w/LNP Design/>=10 circuits/Dispatch/FL (hours)		17.52				19.67		66.60			1,3,4
B.2.22.13.1.1	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch/FL (hours)		17.81		11.35		16.09		20.97		22.26	2
B.2.22.13.1.4	2W Analog Loop w/LNP Non-Design/<10 circuits/Dispatch In/FL (hours)		18.22		18.66		14.99		19.98		14.87	2,4,5
B.2.22.13.2.1	2W Analog Loop w/LNP Non-Design/>=10 circuits/Dispatch/FL (hours)		30.66									1
B.2.22.14.1.1	Other Design/<10 circuits/Dispatch/FL (hours)						18.27				40.57	3,5
B.2.22.15.1.1	Other Non-Design/<10 circuits/Dispatch/FL (hours)		21.76		21.29		18.87		19.55		15.52	4,5
B.2.22.17.1.1	LNP (Standalone)/<10 circuits/Dispatch/FL (hours)				0.23		7.01		43.55		23.80	2,3,4,5
B.2.22.17.1.2	LNP (Standalone)/<10 circuits/Non-Dispatch/FL (hours)		4.12		4.34		4.24		3.16		1.80	
B.2.22.17.2.2	LNP (Standalone)/>=10 circuits/Non-Dispatch/FL (hours)		0.69		1.94		2.30		4.31		0.62	4,5
B.2.22.18.1.1	Digital Loop < DS1/<10 circuits/Dispatch/FL (hours)		32.20		29.48		27.96		35.60		42.39	
B.2.22.19.1.1	Digital Loop >= DS1/<10 circuits/Dispatch/FL (hours)		55.06		37.91		76.16		73.78		51.31	
	Service Order Accuracy											
B.2.34.1.1.1	**Design** (Specials)/<10 circuits/Dispatch/FL (%)		100.00%		100.00%		**100.00%**		**100.00%**		**99.55%**	
B.2.34.1.1.2	**Design** (Specials)/<10 circuits/Non-Dispatch/FL (%)		**67.07%**		**69.33%**		**84.42%**		**91.11%**		**99.01%**	
B.2.34.1.2.1	**Design** (Specials)/>=10 circuits/Dispatch/FL (%)		**100.00%**		100.00%		**96.00%**		**100.00%**		**100.00%**	

Florida Performance Metric Data

Metric Number	Metric Name [SQM Number] and Disaggregation	May BST	May CLEC	June BST	June CLEC	July BST	July CLEC	August BST	August CLEC	September BST	September CLEC	Notes
B.2.34.1.2.2	Design (Specials)/>=10 circuits/Non-Dispatch/FL (%)		100.00%		90.91%		85.71%		94.12%		100.00%	1,3
B.2.34.2.1.1	Loops Non-Design/<10 circuits/Dispatch/FL (%)		96.00%		96.33%		97.33%		97.60%		99.00%	
B.2.34.2.1.2	Loops Non-Design/<10 circuits/Non-Dispatch/FL (%)		100.00%		98.02%		97.05%		99.43%		100.00%	
B.2.34.2.2.1	Loops Non-Design/>=10 circuits/Dispatch/FL (%)		97.90%		97.73%		91.30%		98.98%		97.30%	
B.2.34.2.2.2	Loops Non-Design/>=10 circuits/Non-Dispatch/FL (%)		98.73%		97.53%		98.57%		98.99%		97.67%	
Unbundled Network Elements - Maintenance and Repair												
	Missed Repair Appointments											
B.3.1.1.1	Switch Ports/Dispatch/FL (%)	7.00%		10.45%		11.79%		10.55%		9.77%		
B.3.1.1.2	Switch Ports/Non-Dispatch/FL (%)	1.79%		1.14%		1.00%		1.00%		1.22%		
B.3.1.2.1	Local Interoffice Transport/Dispatch/FL (%)	0.29%	0.00%	0.08%	0.00%	0.14%	0.00%	0.68%	0.00%	0.60%	0.00%	1,2,3,4,5
B.3.1.2.2	Local Interoffice Transport/Non-Dispatch/FL (%)	0.12%	0.00%	0.00%	0.00%	0.74%	0.00%	0.14%	0.00%	0.00%	0.00%	2,4
B.3.1.3.1	Loop + Port Combinations/Dispatch/FL (%)	7.09%	4.94%	10.52%	7.62%	11.85%	6.69%	10.63%	6.51%	9.94%	7.75%	
B.3.1.3.2	Loop + Port Combinations/Non-Dispatch/FL (%)	1.85%	1.08%	1.18%	0.71%	1.04%	1.03%	1.08%	0.86%	1.28%	1.55%	
B.3.1.4.1	Combo Other/Dispatch/FL (%)	6.97%	0.00%	10.31%	0.00%	11.62%	0.00%	10.44%	1.52%	9.77%	0.00%	
B.3.1.4.2	Combo Other/Non-Dispatch/FL (%)	6.97%	0.00%	10.31%	0.00%	11.62%	0.00%	10.44%	0.00%	9.77%	0.00%	
B.3.1.5.1	xDSL (ADSL, HDSL and UCL)/Dispatch/FL (%)	32.70%	3.28%	38.52%	3.85%	43.96%	1.89%	37.21%	2.04%	34.00%	0.00%	
B.3.1.5.2	xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL (%)	1.97%	0.00%	1.46%	0.00%	1.82%	0.00%	2.04%	0.00%	1.92%	0.00%	
B.3.1.6.1	UNE ISDN/Dispatch/FL (%)	7.08%	1.02%	6.70%	1.63%	5.63%	1.97%	6.29%	2.94%	2.96%	1.61%	
B.3.1.6.2	UNE ISDN/Non-Dispatch/FL (%)	0.52%	0.00%	0.00%	0.00%	2.56%	0.00%	0.53%	0.00%	0.00%	0.00%	
B.3.1.7.1	Line Sharing/Dispatch/FL (%)	32.70%	18.18%	38.52%	21.43%	43.96%	17.65%	37.21%	11.11%	34.00%	13.51%	
B.3.1.7.2	Line Sharing/Non-Dispatch/FL (%)	1.97%	10.00%	1.46%	26.32%	1.82%	0.00%	2.04%	0.00%	1.92%	0.00%	

Florida Performance Metric Data

Metric Number	Metric Name [SQM Number] and Disaggregation	May BST	May CLEC	June BST	June CLEC	July BST	July CLEC	August BST	August CLEC	September BST	September CLEC	Notes
B.3.1.8.1	2W Analog Loop Design/Dispatch/FL (%)	7.09%	0.86%	10.52%	3.33%	11.85%	3.06%	10.63%	1.83%	9.94%	4.11%	
B.3.1.8.2	2W Analog Loop Design/Non-Dispatch/FL (%)	7.09%	0.00%	10.52%	0.43%	11.85%	0.00%	10.63%	0.00%	9.94%	1.68%	
B.3.1.9.1	2W Analog Loop Non-Design/Dispatch/FL (%)	6.98%	9.97%	10.44%	8.26%	11.77%	8.79%	10.53%	8.77%	9.75%	12.90%	
B.3.1.9.2	2W Analog Loop Non-Design/Non-Dispatch/FL (%)	1.75%	8.45%	1.05%	2.78%	0.91%	25.35%	0.94%	0.00%	1.04%	7.32%	
B.3.1.10.1	Other Design/Dispatch/FL (%)	2.61%	0.00%	1.89%	0.00%	2.10%	0.00%	2.45%	0.00%	1.98%	3.24%	2,3,4
B.3.1.10.2	Other Design/Non-Dispatch/FL (%)	0.46%	0.00%	0.34%	0.00%	0.78%	0.00%	0.79%	0.00%	0.25%	0.00%	2
B.3.1.11.1	Other Non-Design/Dispatch/FL (%)	7.09%	15.79%	10.52%	22.22%	11.85%	14.29%	10.63%	0.00%	9.94%	0.00%	2,3,4,5
B.3.1.11.2	Other Non-Design/Non-Dispatch/FL (%)	1.85%	0.00%	1.18%	0.00%	1.04%	0.00%	1.08%	0.00%	1.28%	0.00%	1,2,5
	Customer Trouble Report Rate											
B.3.2.1.1	Switch Ports/Dispatch/FL (%)	1.70%		2.02%		2.40%		2.24%		1.86%		
B.3.2.1.2	Switch Ports/Non-Dispatch/FL (%)	1.00%		1.02%		1.16%		1.08%		0.92%		
B.3.2.2.1	Local Interoffice Transport/Dispatch/FL (%)	1.86%	0.55%	2.15%	0.34%	2.57%	0.35%	2.33%	0.27%	1.75%	0.34%	
B.3.2.2.2	Local Interoffice Transport/Non-Dispatch/FL (%)	1.54%	0.69%	1.12%	0.21%	1.43%	0.69%	1.24%	0.62%	1.13%	0.68%	
B.3.2.3.1	Loop + Port Combinations/Dispatch/FL (%)	1.62%	1.27%	1.92%	1.41%	2.29%	1.69%	2.13%	1.54%	1.78%	1.27%	
B.3.2.3.2	Loop + Port Combinations/Non-Dispatch/FL (%)	0.96%	0.56%	0.98%	0.45%	1.11%	0.55%	1.04%	0.48%	0.89%	0.38%	
B.3.2.4.1	Combo Other/Dispatch/FL (%)	1.43%	2.97%	1.69%	2.73%	2.02%	3.41%	1.92%	3.23%	1.60%	1.98%	
B.3.2.4.2	Combo Other/Non-Dispatch/FL (%)	1.43%	1.60%	1.69%	1.42%	2.02%	2.14%	1.92%	1.22%	1.60%	1.56%	
B.3.2.5.1	xDSL (ADSL, HDSL and UCL)/Dispatch/FL (%)	1.09%	1.15%	1.21%	1.00%	1.54%	1.01%	1.46%	0.94%	1.36%	0.82%	
B.3.2.5.2	xDSL (ADSL. HDSL and UCL)/Non-Dispatch/FL (%)	2.19%	0.36%	2.67%	0.41%	2.93%	0.27%	2.63%	0.42%	2.91%	0.38%	
B.3.2.6.1	UNE ISDN/Dispatch/FL (%)	0.82%	1.48%	0.70%	1.87%	0.85%	2.32%	0.82%	2.06%	0.95%	1.88%	

Florida Performance Metric Data

Metric Number	Metric Name [SQM Number] and Disaggregation	May		June		July		August		September		Notes
		BST	CLEC	BST	CLEC	BST	CLEC	BST	CLEC	BST	CLEC	
B.3.2.6.2	UNE ISDN/Non-Dispatch/FL (%)	0.75%	0.89%	0.83%	0.68%	0.94%	0.63%	0.89%	0.71%	0.82%	0.55%	
B.3.2.7.1	Line Sharing/Dispatch/FL (%)	1.09%	1.01%	1.21%	1.24%	1.54%	1.49%	1.46%	0.94%	1.36%	1.88%	
B.3.2.7.2	Line Sharing/Non-Dispatch/FL (%)	2.19%	1.84%	2.67%	0.84%	2.93%	0.92%	2.63%	1.10%	2.91%	2.03%	
B.3.2.8.1	2W Analog Loop Design/Dispatch/FL (%)	1.62%	0.96%	1.92%	1.17%	2.29%	1.28%	2.13%	1.22%	1.78%	0.97%	
B.3.2.8.2	2W Analog Loop Design/Non-Dispatch/FL (%)	1.62%	0.33%	1.92%	0.33%	2.29%	0.23%	2.13%	0.23%	1.78%	0.18%	
B.3.2.9.1	2W Analog Loop Non-Design/Dispatch/FL (%)	1.70%	2.42%	2.01%	2.94%	2.40%	2.61%	2.23%	2.80%	1.86%	2.28%	
B.3.2.9.2	2W Analog Loop Non-Design/Non-Dispatch/FL (%)	0.84%	0.16%	0.89%	0.09%	1.02%	0.18%	0.94%	0.07%	0.79%	0.11%	
B.3.2.10.1	Other Design/Dispatch/FL (%)	0.26%	0.00%	0.28%	1.11%	0.34%	1.11%	0.36%	1.15%	0.28%	3.02%	
B.3.2.10.2	Other Design/Non-Dispatch/FL (%)	0.35%	0.00%	0.28%	1.11%	0.32%	0.00%	0.33%	0.00%	0.31%	1.32%	
B.3.2.11.1	Other Non-Design/Dispatch/FL (%)	1.62%	3.32%	1.92%	1.60%	2.29%	1.25%	2.13%	0.18%	1.78%	0.37%	
B.3.2.11.2	Other Non-Design/Non-Dispatch/FL (%)	0.96%	0.17%	0.98%	0.18%	1.11%	0.00%	1.04%	0.00%	0.89%	0.18%	
	Maintenance Average Duration											
B.3.3.1.1	Switch Ports/Dispatch/FL (hours)	15.50		21.85		27.21		22.32		22.05		
B.3.3.1.2	Switch Ports/Non-Dispatch/FL (hours)	5.22		7.27		8.95		7.28		7.37		
B.3.3.2.1	Local Interoffice Transport/Dispatch/FL (hours)	3.49	1.24	3.49	4.60	3.79	5.32	3.87	2.35	3.91	1.97	1,2,3,4,5
B.3.3.2.2	Local Interoffice Transport/Non-Dispatch/FL (hours)	1.58	2.77	1.60	0.93	1.86	1.58	1.62	2.04	1.44	1.73	2,4
B.3.3.3.1	Loop + Port Combinations/Dispatch/FL (hours)	15.48	13.81	21.79	18.13	27.13	21.65	22.26	16.21	22.00	16.68	
B.3.3.3.2	Loop + Port Combinations/Non-Dispatch/FL (hours)	5.20	4.00	7.22	4.94	8.89	5.40	7.23	4.22	7.32	4.16	
B.3.3.4.1	Combo Other/Dispatch/FL (hours)	15.24	3.53	21.41	4.17	26.62	4.68	21.91	6.41	21.64	5.11	
B.3.3.4.2	Combo Other/Non-Dispatch/FL (hours)	15.24	2.89	21.41	2.40	26.62	2.30	21.91	1.48	21.64	2.73	

Florida Performance Metric Data

Metric Number	Metric Name [SQM Number] and Disaggregation	May		June		July		August		September		Notes
		BST	CLEC	BST	CLEC	BST	CLEC	BST	CLEC	BST	CLEC	
B.3.3.5.1	xDSL (ADSL, HDSL and UCL)/Dispatch/FL (hours)	40.09	4.89	38.47	5.50	39.73	5.21	61.70	4.63	28.65	4.50	
B.3.3.5.2	xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL (hours)	2.80	2.11	2.10	1.28	4.06	1.16	8.03	1.37	1.66	1.71	
B.3.3.6.1	UNE ISDN/Dispatch/FL (hours)	8.07	4.25	7.46	4.76	8.53	5.43	7.13	8.74	6.25	4.38	
B.3.3.6.2	UNE ISDN/Non-Dispatch/FL (hours)	2.73	3.56	2.41	3.70	7.18	3.69	2.34	3.13	1.98	3.62	
B.3.3.7.1	Line Sharing/Dispatch/FL (hours)	40.09	31.32	38.47	30.54	39.73	32.06	61.70	35.44	28.65	18.70	
B.3.3.7.2	Line Sharing/Non-Dispatch/FL (hours)	2.80	10.43	2.10	14.37	4.06	6.86	8.03	9.19	1.66	7.10	
B.3.3.8.1	2W Analog Loop Design/Dispatch/FL (hours)	15.48	3.99	21.79	5.10	27.13	5.51	22.26	5.10	22.00	6.12	
B.3.3.8.2	2W Analog Loop Design/Non-Dispatch/FL (hours)	15.48	2.40	21.79	2.26	27.13	1.80	22.26	1.83	22.00	2.59	
B.3.3.9.1	2W Analog Loop Non-Design/Dispatch/FL (hours)	15.48	13.48	21.85	12.89	27.21	14.95	22.30	13.16	22.05	14.19	
B.3.3.9.2	2W Analog Loop Non-Design/Non-Dispatch/FL (hours)	5.39	7.03	7.79	6.08	9.65	12.10	7.60	7.37	7.79	9.02	
B.3.3.10.1	Other Design/Dispatch/FL (hours)	6.03	0.00	5.91	2.62	5.72	1.72	6.66	5.30	5.26	5.35	2,3,4
B.3.3.10.2	Other Design/Non-Dispatch/FL (hours)	2.52	0.00	2.85	2.88	2.44	0.00	13.22	0.00	2.05	1.35	2
B.3.3.11.1	Other Non-Design/Dispatch/FL (hours)	15.48	54.26	21.79	26.11	27.13	13.29	22.26	17.00	22.00	11.00	2,3,4,5
B.3.3.11.2	Other Non-Design/Non-Dispatch/FL (hours)	5.20	1.00	7.22	3.00	8.89	0.00	7.23	0.00	7.32	0.00	1,2,5
	% Repeat Troubles within 30 Days											
B.3.4.1.1	Switch Ports/Dispatch/FL (%)	15.30%		15.79%		17.04%		16.84%		16.91%		
B.3.4.1.2	Switch Ports/Non-Dispatch/FL (%)	13.44%		14.54%		15.35%		15.16%		14.97%		
B.3.4.2.1	Local Interoffice Transport/Dispatch/FL (%)	17.16%	12.50%	22.83%	20.00%	22.84%	80.00%	24.17%	0.00%	22.46%	60.00%	1,2,3,4,5
B.3.4.2.2	Local Interoffice Transport/Non-Dispatch/FL (%)	14.48%	10.00%	14.38%	33.33%	21.16%	0.00%	21.19%	22.22%	15.83%	20.00%	2,4
B.3.4.3.1	Loop + Port Combinations/Dispatch/FL (%)	15.25%	12.59%	15.74%	11.73%	16.98%	12.00%	16.84%	11.94%	16.85%	11.92%	

Florida Performance Metric Data

Metric Number	Metric Name [SQM Number] and Disaggregation	May BST	May CLEC	June BST	June CLEC	July BST	July CLEC	August BST	August CLEC	September BST	September CLEC	Notes
B.3.4.3.2	Loop + Port Combinations/Non-Dispatch/FL (%)	13.43%	11.47%	14.48%	11.05%	15.32%	11.20%	15.15%	12.21%	14.90%	11.19%	
B.3.4.4.1	Combo Other/Dispatch/FL (%)	15.30%	15.38%	15.84%	15.38%	17.11%	11.94%	16.98%	33.33%	16.91%	26.19%	
B.3.4.4.2	Combo Other/Non-Dispatch/FL (%)	15.30%	3.57%	15.84%	3.70%	17.11%	21.43%	16.98%	16.00%	16.91%	9.09%	
B.3.4.5.1	xDSL (ADSL, HDSL and UCL)/Dispatch/FL (%)	25.06%	11.48%	26.79%	11.54%	28.24%	9.43%	28.24%	10.20%	28.83%	7.32%	
B.3.4.5.2	xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL (%)	22.89%	0.00%	27.26%	14.29%	27.08%	0.00%	26.00%	9.09%	25.18%	21.05%	
B.3.4.6.1	UNE ISDN/Dispatch/FL (%)	11.32%	16.33%	12.29%	19.51%	15.96%	17.11%	15.43%	13.97%	17.73%	21.77%	
B.3.4.6.2	UNE ISDN/Non-Dispatch/FL (%)	13.92%	10.17%	14.15%	13.33%	9.83%	9.76%	13.16%	19.15%	10.92%	8.33%	
B.3.4.7.1	Line Sharing/Dispatch/FL (%)	25.06%	50.00%	26.79%	17.86%	28.24%	35.29%	28.24%	38.89%	28.83%	35.14%	
B.3.4.7.2	Line Sharing/Non-Dispatch/FL (%)	22.89%	37.50%	27.26%	10.53%	27.08%	33.33%	26.00%	33.33%	25.18%	35.00%	
B.3.4.8.1	2W Analog Loop Design/Dispatch/FL (%)	15.25%	6.87%	15.74%	9.24%	16.98%	9.99%	16.84%	9.38%	16.85%	8.85%	
B.3.4.8.2	2W Analog Loop Design/Non-Dispatch/FL (%)	15.25%	7.44%	15.74%	6.09%	16.98%	6.92%	16.84%	8.39%	16.85%	7.56%	
B.3.4.9.1	2W Analog Loop Non-Design/Dispatch/FL (%)	15.27%	14.77%	15.77%	12.57%	17.02%	7.91%	16.82%	9.51%	16.88%	8.11%	
B.3.4.9.2	2W Analog Loop Non-Design/Non-Dispatch/FL (%)	13.28%	12.68%	14.43%	8.33%	15.28%	9.86%	15.11%	7.41%	14.96%	9.76%	
B.3.4.10.1	Other Design/Dispatch/FL (%)	17.08%	0.00%	19.85%	0.00%	22.40%	0.00%	22.66%	0.00%	19.96%	25.00%	2,3,4
B.3.4.10.2	Other Design/Non-Dispatch/FL (%)	16.75%	0.00%	17.55%	0.00%	18.27%	0.00%	18.46%	0.00%	14.28%	11.70%	2
B.3.4.11.1	Other Non-Design/Dispatch/FL (%)	15.25%	10.53%	15.74%	44.44%	16.98%	0.00%	16.84%	0.00%	16.85%	0.00%	2,3,4,5
B.3.4.11.2	Other Non-Design/Non-Dispatch/FL (%)	13.43%	0.00%	14.48%	0.00%	15.32%	0.00%	15.15%	0.00%	14.90%	0.00%	1,2,5
	Out of Service > 24 hours											
B.3.5.1.1	Switch Ports/Dispatch/FL (%)	10.92%		22.85%		32.15%		22.78%		21.13%		
B.3.5.1.2	Switch Ports/Non-Dispatch/FL (%)	3.90%		9.09%		12.83%		8.16%		8.97%		
B.3.5.2.1	Local Interoffice Transport/Dispatch/FL (%)	0.29%	0.00%	0.08%	0.00%	0.14%	0.00%	0.68%	0.00%	0.60%	0.00%	1,2,3,4,5

Florida Performance Metric Data

Metric Number	Metric Name [SQM Number] and Disaggregation	May BST	May CLEC	June BST	June CLEC	July BST	July CLEC	August BST	August CLEC	September BST	September CLEC	Notes
B.3.5.2.2	Local Interoffice Transport/Non-Dispatch/FL (%)	0.12%	0.00%	0.00%	0.00%	0.74%	0.00%	0.14%	0.00%	0.00%	0.00%	2,4
B.3.5.3.1	Loop + Port Combinations/Dispatch/FL (%)	11.00%	8.71%	22.76%	16.67%	32.01%	22.87%	22.72%	12.04%	21.11%	14.12%	
B.3.5.3.2	Loop + Port Combinations/Non-Dispatch/FL (%)	3.87%	1.52%	8.91%	3.11%	12.63%	3.86%	8.01%	2.21%	8.85%	2.41%	
B.3.5.4.1	Combo Other/Dispatch/FL (%)	10.69%	0.00%	22.04%	0.00%	30.97%	0.00%	22.03%	1.52%	20.50%	0.00%	
B.3.5.4.2	Combo Other/Non-Dispatch/FL (%)	10.69%	0.00%	22.04%	0.00%	30.97%	0.00%	22.03%	0.00%	20.50%	0.00%	
B.3.5.5.1	xDSL (ADSL, HDSL and UCL)/Dispatch/FL (%)	32.70%	3.28%	38.52%	3.85%	43.96%	1.89%	37.21%	2.04%	34.00%	0.00%	
B.3.5.5.2	xDSL (ADSL, HDSL and UCL)/Non-Dispatch/FL (%)	1.97%	0.00%	1.46%	0.00%	1.82%	0.00%	2.04%	0.00%	1.92%	0.00%	
B.3.5.6.1	UNE ISDN/Dispatch/FL (%)	7.55%	1.02%	6.70%	1.63%	5.71%	1.97%	6.29%	2.94%	2.96%	1.61%	
B.3.5.6.2	UNE ISDN/Non-Dispatch/FL (%)	0.52%	0.00%	0.00%	0.00%	2.58%	0.00%	0.53%	0.00%	0.00%	0.00%	
B.3.5.7.1	Line Sharing/Dispatch/FL (%)	32.70%	0.00%	38.52%	100.00%	43.96%	0.00%	37.21%	0.00%	34.00%	0.00%	2,5
B.3.5.7.2	Line Sharing/Non-Dispatch/FL (%)	1.97%	0.00%	1.46%	0.00%	1.82%	0.00%	2.04%	0.00%	1.92%	0.00%	2,5
B.3.5.8.1	2W Analog Loop Design/Dispatch/FL (%)	11.00%	0.86%	22.76%	3.33%	32.01%	3.06%	22.72%	1.83%	21.11%	4.11%	
B.3.5.8.2	2W Analog Loop Design/Non-Dispatch/FL (%)	11.00%	0.00%	22.76%	0.43%	32.01%	0.00%	22.72%	0.00%	21.11%	1.68%	
B.3.5.9.1	2W Analog Loop Non-Design/Dispatch/FL (%)	10.91%	36.67%	22.85%	28.13%	32.14%	20.83%	22.77%	26.47%	21.13%	33.33%	
B.3.5.9.2	2W Analog Loop Non-Design/Non-Dispatch/FL (%)	3.90%	0.00%	9.09%	0.00%	12.85%	33.33%	8.17%	0.00%	8.95%	40.00%	1,2,3,4,5
B.3.5.10.1	Other Design/Dispatch/FL (%)	2.61%	0.00%	1.89%	0.00%	2.10%	0.00%	2.45%	0.00%	1.98%	3.24%	2,3,4
B.3.5.10.2	Other Design/Non-Dispatch/FL (%)	0.46%	0.00%	0.34%	0.00%	0.78%	0.00%	0.79%	0.00%	0.25%	0.00%	2
B.3.5.11.1	Other Non-Design/Dispatch/FL (%)	11.00%	33.33%	22.76%	33.33%	32.01%	0.00%	22.72%	0.00%	21.11%	0.00%	2,3,4,5
B.3.5.11.2	Other Non-Design/Non-Dispatch/FL (%)	3.87%	0.00%	8.91%	0.00%	12.63%	0.00%	8.01%	0.00%	8.85%	0.00%	1,2
Unbundled Network Elements - Billing												
	(Invoice Accuracy											

Florida Performance Metric Data

Metric Number	Metric Name [SQM Number] and Disaggregation	May BST	May CLEC	June BST	June CLEC	July BST	July CLEC	August BST	August CLEC	September BST	September CLEC	Notes
B.4.1	FL (%)	97.66%	99.67%	98.27%	99.97%	97.59%	95.04%	94.48%	99.82%	98.02%	98.73%	
	Mean Time to Deliver Invoices - CRIS											
B.4.2	Region (business days)	3.47	3.77915	3.82	3.45794	4.42	3.86304	3.24	3.32341	4.05	3.63143	
Local Interconnection Trunks - Ordering												
	% Rejecled Service Requests											
C.1.1	Local Interconnection Trunks/FL (%)		46.33%		43.70%		39.50%		48.06%		35.96%	
	Reject Interval											
C.1.2	Local Interconnection Trunks/FL (%)		99.01%		99.31%		100.00%		98.39%		100.00%	
	FOC Timeliness				100.00%							
C.1.3	Local Interconnection Trunks/FL (%)		100.00%		100.00%		100.00%		100.00%		100.00%	
	FOC & Reject Response Completeness											
C.1.4	Local Interconnection Trunks/FL (%)		100.00%		100.00%		100.00%		100.00%		100.00%	
Local Interconnection Trunks • Provisioning												
	Order Completion Interval											
c.2. I	Local Interconnection Trunks/FL (days)	29.98	19.56	1 6 . 7 1	24.75	24.82	24.53	18.60	15.69	20.55	18.66	
	Held Orders											
c.2.2	Local Interconnection TrunksiFL (days)	*0.00*	*0.00*	0.00	*0.00*	*0.00*	*0.00*	*0.00*	*0.00*	*0.00*	*0.00*	
	% Jeopardies											
C.2.3	Local Interconnection Trunks/FL (%)	0.00%	0.00%	**0.00%**	**0.00%**	**0.00%**	*0.00%*	*0.00%*	*0.00%*	*0.00%*	*0.00%*	
	% Missed Installation Appointments											
C.2.5	Local Interconnection Trunks/FL (%)	0.00%	0.00%	0.00%	*0.00%*	*0.00%*	0.93%	1.23%	0.00%	*0.00%*	*0.00%*	
	% Provisioning Troubles within 30 Days											
C.2.6	Local Interconnection Trunks/FL (%)	0.44%	0.00%	**1.38%**	**0.00%**	**1.62%**	*0.00%*	0.00%	0.00%	1.38%	0.00%	
	Average Completion Notice Interval											

Florida Performance Metric Data

Metric Number	Metric Name [SQM Number] and Disaggregation	May BST	May CLEC	June BST	June CLEC	July BST	July CLEC	August BST	August CLEC	September BST	September CLEC	Notes
C.2.7	Local Interconnection Trunks/FI (hours)	206.93	20.91	37.64	17.46	81.01	13.86	61.98	12.70	133.30	25.00	
	Total Service Order Cycle Time											
C.2.8	Local Interconnection Trunks/FL (days)		20.00		21.79		24.00		17.61		17.78	
	% Completions w/o Notice or c 24 hours											
C.2.10.1	Local Interconnection Trunks/Dispatch/FL (%)		0.00%		0.00%		0.00%		0.00%		0.00%	
	Service Order Accuracy											
C.2.11.1.1	Local Interconnection Trunks/<10 circuits/Dispatch/FL (%)		100.00%		100.00%		100.00%		100.00%		100.00%	
C.2.11.1.2	Local Interconnection Trunks/<10 circuits/Non-Dispatch/FL (%)		100.00%		100.00%		100.00%		100.00%		100.00%	
C.2.11.2.1	Local Interconnection Trunks/>=10 circuits/Dispatch/FL (%)		100.00%		100.00%		100.00%		100.00%		100.00%	2,3,4
C.2.11.2.2	Local Interconnection Trunks/>=10 circuits/Non-Dispatch/FL (%)		100.00%		100.00%		100.00%		100.00%		100.00%	
Local Interconnection Trunks - Maintenance and Repair												
	Missed Repair Appointments											
C.3.1.1	Local Interconnection Trunks/Dispatch/FL (%)	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	
C.3.1.2	Local Interconnection Trunks/Non-Dispatch/FL (%)	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	1,2,3,4,5
	Customer Trouble Report Rate											
C.3.2.1	Local Interconnection Trunks/Dispatch/FL (%)	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	
C.3.2.2	Local Interconnection Trunks/Non-Dispatch/FL (%)	0.05%	0.00%	0.02%	0.00%	0.06%	0.00%	0.03%	0.00%	0.05%	0.00%	
	Maintenance Average Duration											
C.3.3.1	Local Interconnection Trunks/Dispatch/FL (hours)	0.00	0.00	1.71	0.00	3.22	0.00	0.00	0.00	0.00	0.00	

Florida Performance Metric Data

Metric Number	Metric Name [SQM Number] and Disaggregation	May BST	May CLEC	June BST	June CLEC	July BST	July CLEC	August BST	August CLEC	September BST	September CLEC	Notes
C.3.3.2	Local Interconnection Trunks/Non-Dispatch/FL (hours)	0.16	0.05	1.85	0.83	1.08	1.78	0.66	0.05	0.48	2.70	1,2,3,4,5
	% Repeat Troubles within 30 Days											
C.3.4.1	Local Interconnection Trunks/Dispatch/FL (%)	0.00%	0.00%	0.00%	0.00%	4.00%	0.00%	0.00%	0.00%	0.00%	0.00%	
C.3.4.2	Local Interconnection Trunks/Non-	0.4 %	0.00%	0.00%	0.00%	8.55%	0.00%	15.13%	0.00%	10.39%	0.00%	1,2,3,4,5
	Out of Service > 24 hours											
C.3.5.1	Local Interconnection Trunks/Dispatch/FL (%)	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	
C.3.5.2	Local Interconnection Trunks/Non-Dispatch/FL (%)	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	1,2,3,4,5
Local Interconnection Trunks - Billing												
	Invoice Accuracy											
c.4.1	JFL(%)	97.66%	99.56%	98.27%	97.54%	97.59%	99.76%	94.48%	99.35%	98.02%	97.01%	
	Mean Time to Deliver Invoices - CABS											
C.4.2	Region (calendar days)	4.96	4.92	4.54	4.29	4.59	4.61	4.47	4.30	4.68	4.16	
Local Interconnection Trunks - Trunk Blocking												
	Trunk Group Performance - Aggregate											
C.5.1	Local Interconnection Trunks/FL (%)		1		0		0		0		0	
Operations Support Systems - Pre-Ordering												
	% Interface Availability - CLEC											
D.1.1.1	EDI/Region (%)		99.64%		100.00%		100.00%		100.00%		100.00%	
D.1.1.2	LENS/Region (%)		99.85%		99.76%		99.93%		99.96%		99.93%	
D.1.1.3	LEO/Region (%)		100.00%		100.00%		100.00%		100.00%		99.95%	
D.1.1.4	LESOG/Region (%)		100.00%		100.00%		100.00%		100.00%		100.00%	
D.1.1.6	PSIMS/Region (%)		100.00%		100.00%		100.00%		100.00%		100.00%	
D.1.1.7	LNP Gateway/Region (%)		100.00%		99.86%		99.97%		100.00%		100.00%	
D.1.1.8	SGG/COG/Region (%)		100.00%		99.26%		99.87%		99.96%		99.99%	
	% Interface Availability - BST & CLEC											
D.1.2.1	ATLAS/Region (%)		100.00%		99.99%		99.98%		100.00%		99.99%	
D.1.2.2	COFFI/Region (%)		100.00%		99.99%		99.98%		100.00%		99.99%	
D.1.2.3	BOCRIS/CRIS/Region (%)		99.96%		99.99%		99.98%		99.99%		99.99%	

Florida Performance Metric Data

Metric Number	Metric Name [SQM Number] and Disaggregation	May BST	May CLEC	June BST	June CLEC	July BST	July CLEC	August BST	August CLEC	September BST	September CLEC	Notes
D.1.2.4	DSAP/Region (%)		100.00%		99.98%		99.96%		99.31%		99.98%	
D.1.2.5	RSAG/Region (%)		100.00%		99.99%		99.97%		100.00%		99.98%	
D.1.2.6	SOCS/Region (%)		100.00%		99.99%		99.98%		100.00%		99.99%	
D.1.2.7	SONGS/Region (%)		100.00%		99.99%		99.98%		100.00%		99.99%	
D.1.2.8	DOE/Region (%)		99.99%		100.00%		100.00%		100.00%		100.00%	
	Average Response Interval - CLEC (LENS) (BST Measure Includes Additional 2 Seconds)											
D.1.3.1.1	RSAG, by TN/Region (seconds)	2.87	1.20	2.88	1.02	2.80	1.67	2.71	1.67	2.69	1.58	
D.1.3.1.2	RSAG, by TN/Region (seconds)	2.94	1.20	2.91	1.02	2.88	1.67	2.80	1.67	2.78	1.58	
D.1.3.2.1	RSAG, by ADDR/Region (seconds)	2.99	**1.10**	3.03	0.93	2.94	**1.13**	**2.84**	**1.06**	2.81	**1.14**	
D.1.3.2.2	RSAG, by ADDR/Region (seconds)	4.77	1.10	4.76	0.93	4.80	1.13	4.59	1.06	4.68	1.14	
D.1.3.3.1	ATLAS/Region (seconds)	2.95	0.88	3.01	0.80	2.93	1.07	2.77	1.03	2.78	1.21	
D.1.3.3.2	ATLAS/Region (seconds)	2.60	0.88	2.61	0.80	2.63	1.07	2.58	1.03	2.49	1.21	
D.1.3.4.1	DSAP/Region (seconds)	2.71	0.53	2.71	0.52	2.68	2.87	2.61	2.84	2.56	5.27	
D.1.3.4.2	DSAP/Region (seconds)	2.57	0.53	2.57	0.52	2.58	2.87	2.55	2.84	2.53	5.27	
D.1.3.5.1	CRSECSRL/Region (seconds)	3.21	2.12	3.24	1.14	3.20	2.01	3.07	1.76	3.01	1.13	
D.1.3.5.2	CRSECSRL/Region (seconds)	2.87	2.12	2.95	1.14	2.94	2.01	2.85	1.76	2.60	1.13	
D.1.3.6.1	COFFI/Region (seconds)	4.60	0.63	4.35	0.64	3.43	2.97	3.15	3.38	3.12	4.24	
D.1.3.6.2	COFFI/Region (seconds)	7.28	0.63	3.73	0.64	3.68	2.97	3.68	3.38	3.70	4.24	
D.1.3.7.1	PSIMS/ORB/Region (seconds)	4.60	0.04	4.35	0.04	3.43	0.63	3.15	0.58	3.12	0.35	
D.1.3.7.2	PSIMS/ORB/Region (seconds)	7.28	0.04	3.73	0.04	3.68	0.63	3.68	0.58	3.70	0.35	
	Average Response Interval - CLEC (TAG) (BST Measure Includes Additional 2 Seconds)											
D.1.4.1.1	RSAG, by TN/Region (seconds)	2.87	1.60	2.88	1.52	2.80	1.52	2.71	1.42	2.69	1.28	
D.1.4.1.2	RSAG, by TN/Region (seconds)	2.94	1.60	2.91	1.52	2.88	1.52	2.80	1.42	2.78	1.28	
D.1.4.2.1	RSAG, by ADDR/Region (seconds)	2.99	3.05	3.03	2.34	2.94	2.30	2.84	2.19	2.81	2.17	
D.1.4.2.2	RSAG, by ADDR/Region (seconds)	4.77	3.05	4.76	2.34	4.80	2.30	4.59	2.19	4.68	2.17	
D.1.4.4.1	ATLAS - DID/Region (seconds)		1.76		1.37		0.65		0.60		0.93	
D.1.4.4.2	ATLAS - DID/Region (seconds)		1.76		1.37		0.65		0.60		0.93	
D.1.4.5.1	ATLAS - TN/Region (seconds)	2.95	1.86	3.01	1.89	2.93	1.74	2.77	1.54	2.78	1.69	
D.1.4.5.2	ATLAS - TN/Region (seconds)	2.60	1.86	2.61	1.89	2.63	1.74	2.58	1.54	2.49	1.69	
D.1.4.6.1	DSAP/Region (seconds)	2.71	1.74	2.71	1.83	2.68	0.93	2.61	0.89	2.56	1.06	